

Product Summary

BV _{DSS}	Rds(on)	I _D TA = +25°C
50V	1.6Ω @ V _{GS} = 10V	350mA
507	$2.5\Omega @ V_{GS} = 4.5V$	200mA

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (RDs(ON)) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Driving
- Power Management Functions
- Load Switching

Features and Benefits

- N-Channel MOSFET
- Low On-Resistance
- Very Low Gate Threshold VoltageLow Input Capacitance
- Fast Switching Speed
- Low Input/ Output Leakage
- Ultra-Small Surface Mount Package
- ESD Protected to 2kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

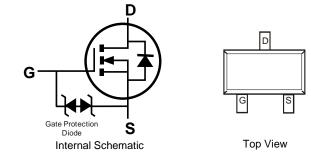
- Case: SOT523
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.002 grams (Approximate)





SOT523

Top View



Ordering Information (Note 4)

	Part Number	Case	Packaging			
	DMN53D0LT-7	SOT523	3000/Tape & Reel			
	DMN53D0LT-13	SOT523	10000/Tape & Reel			
Notes:	otes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.					

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

					T53 = Product Type Marking Code							
			T53	YM	YM =	Date Cod	le Marking ex: H = 202	•				
Date Code Key					M = 1	Month (ex:	9 = Septer	nber)				
Year	2014		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	В		Н	I	J	K	L	М	N	0	Р	R
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V _{DSS}	50	V
Gate-Source Voltage	Vgss	±20	V
Drain Current (Note 5)	ID	350	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	300	mW
Thermal Resistance, Junction to Ambient (Note 5)	R _{0JA}	420	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BVDSS	50	_	_	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	IDSS	_	_	1.0	μA	$V_{DS} = 50V, V_{GS} = 0V$	
Gate-Body Leakage	Igss	_	_	_	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	Vgs(th)	0.8	_	1.5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	Rds(on)		1.08 1.09 1.45	1.6 2.5 4.5	$ \begin{array}{l} V_{GS} = 10 V, \ I_{D} = 500 n \\ V_{GS} = 4.5 V, \ I_{D} = 200 n \\ V_{GS} = 2.5 V, \ I_{D} = 100 n \end{array} $		
Source-Drain Diode Forward Voltage	Vsd	—	0.88	1.4	V	$V_{GS} = 0V, I_{S} = 500 mA$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss	_	46		pF		
Output Capacitance		—	5.3		pF	VDS = 25V, VGS = 0V f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	4.0	_	pF		
Total Gate Charge	Qg	_	0.6	—	nC		
Gate-Source Charge		_	0.2	—	nC	VGS = 4.5V, VDS = 10V, ID = 250mA	
Gate-Drain Charge	Q _{gd}	_	0.1	_	nC		
Turn-On Delay Time	tD(ON)	_	2.7		ns		
Turn-On Rise Time	tR	_	2.5		ns	$V_{DD} = 30V, V_{GS} = 10V,$	
Turn-Off Delay Time	tD(OFF)	_	19		ns	$R_G = 25\Omega, I_D = 200 mA$	
Turn-Off Fall Time	tF	_	11		ns		

 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect. Notes:

7. Guaranteed by design. Not subject to product testing.



4.5 5

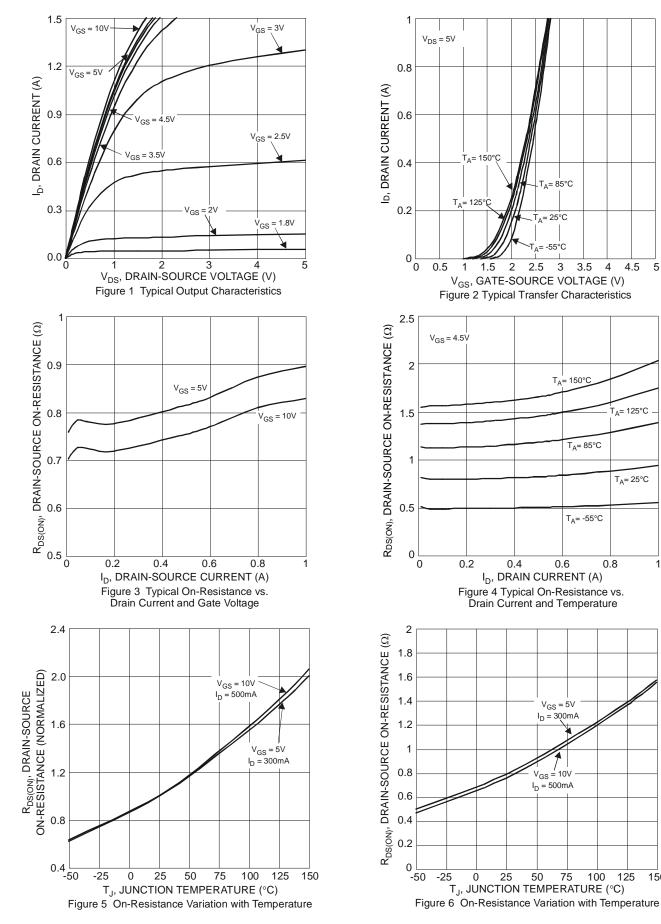
T_A= 25°C

1

T_A= 85°C

T_A= -55°C

0.8



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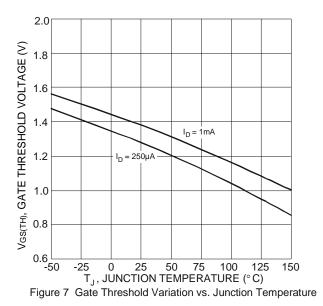
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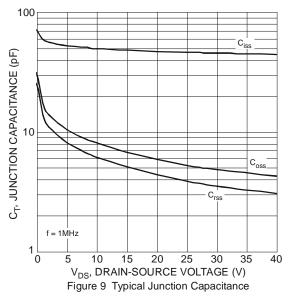
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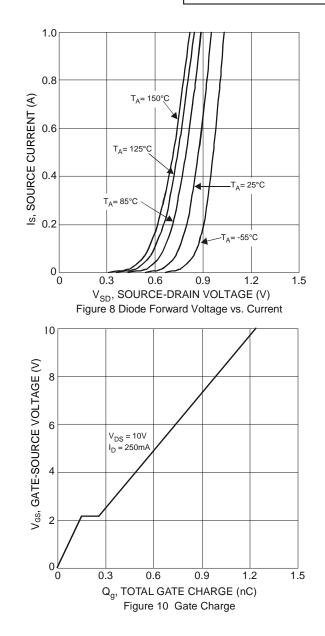
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DMN53D0LT



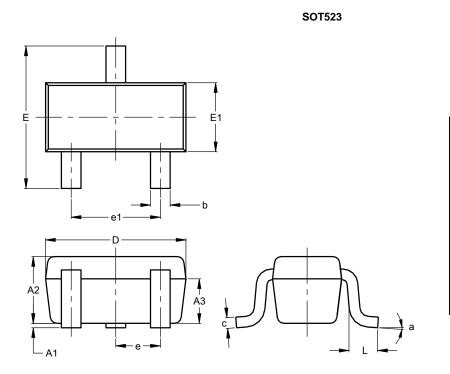






Package Outline Dimensions

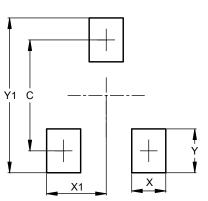
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT523							
Dim	Min Max Typ						
A1	0.00	0.10	0.05				
A2	0.60	0.80	0.75				
A3	0.45	0.45 0.65 0.50					
b	0.15	0.30	0.22				
С	0.10	0.12					
D	1.50	1.70	1.60				
Е	1.45	1.75	1.60				
E1	0.75	0.80					
e		0.50 BS	С				
e1	0.90	1.10	1.00				
L	0.20	0.40	0.33				
а	0°		8°				
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.29
Х	0.40
X1	0.70
Y	0.51
Y1	1.80

SOT523



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