





### **60V N-CHANNEL ENHANCEMENT MODE MOSFET**

## **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C		
00)/	$40m\Omega$ @ $V_{GS} = 10V$	5.0A		
60V	$55mΩ @ V_{GS} = 4.5V$	4.2A		

## **Description**

This new generation MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## **Applications**

- Motor Control
- · Transformer Driving Switch
- DC-DC Converters
- Power Management Functions
- Uninterrupted Power Supply

### **Features**

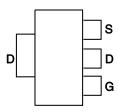
- 100% Unclamped Inductive Switch (UIS) Test in Production
- Low On-Resistance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

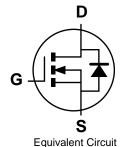
- Case: SOT223
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0 (Note 1)
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 (§3)
- Weight: 0.112 grams (Approximate)







Pin Out - Top View



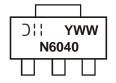
### Ordering Information (Note 4)

Ī	Part Number	Marking	Reel Size (inches)	Quantity per Reel
	DMN6040SE-13	N6040	13	2,500

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**



N6040 = Product Type Marking Code

);; = Manufacturer's Marking

YWW = Date Code Marking

Y = Year (ex: 16 = 2016)

WW = Week (01 - 53)



# 

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	$V_{DSS}$	60	V		
Gate-Source Voltage		$V_{GSS}$	±20	V	
Continuous Dusin Compant (Nata C) V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	5.0 4.0	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	t < 10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	7.1 5.5	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	30	Α		
Maximum Body Diode Continuous Current	Is	3.4	Α		
Avalanche Current (Note 7) L = 0.1mH	I <sub>AS</sub>	14.2	Α		
Avalanche Energy (Note 7) L = 0.1mH	E <sub>AS</sub>	10	mJ		

# Thermal Characteristics ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	TA = +25°C	C	1.2	- W
Total Power Dissipation (Note 5)	Ta = +70°C	$P_{D}$	0.7	
Thormal Posistance, Junction to Ambient (Note 5)	Steady State	C	106	- °C/W
Thermal Resistance, Junction to Ambient (Note 5)	t < 10s	$R_{\theta JA}$	53	
Total Power Discinstion (Note 6)	Ta = +25°C	0	2	W
Total Power Dissipation (Note 6)	Ta = +70°C	$P_{D}$	1.2	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	c	65	°C/W
Thermal Resistance, Junction to Ambient (Note 6)		$R_{\theta JA}$	34	C/VV
Thermal Resistance, Junction to Case (Note 6)	R <sub>0</sub> JC	9	°C/W	
Operating and Storage Temperature Range	$T_{J,}T_{STG}$	-55 to +150	°C	

# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Observatoristis	0	Min	T		1114	To al O an allilla an	
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	D) (	00	1	1	1 1/	N/ 01/1 050 A	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60			V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>		_	1	μΑ	$V_{DS} = 60V$ , $V_{GS} = 0V$	
Gate-Source Leakage	$I_{GSS}$	_		±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	$V_{GS(TH)}$	1	_	3	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	Dagger		30	40	mΩ	$V_{GS} = 10V, I_D = 12A$	
Static Diain-Source On-Nesistance	R <sub>DS(ON)</sub>		35	55		$V_{GS} = 4.5V, I_D = 6A$	
Diode Forward Voltage	$V_{SD}$	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C <sub>ISS</sub>		1,287	_		V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V f = 1.0MHz	
Output Capacitance	Coss		57	_	pF		
Reverse Transfer Capacitance	C <sub>RSS</sub>		44	_			
Gate Resistance	$R_{G}$		1.2	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V <sub>GS</sub> = 10V)	$Q_G$		22.4				
Total Gate Charge (V <sub>GS</sub> = 4.5V)	$Q_{G}$		10.4	_	nC	$V_{DS} = 30V, I_D = 4.3A$	
Gate-Source Charge	$Q_{GS}$		4.9	_	IIC		
Gate-Drain Charge	$Q_{GD}$		3.0	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	6.6	_		$V_{GS} = 10V, V_{DD} = 30V, R_G = 6\Omega,$ $I_D = 4.3A$	
Turn-On Rise Time	t <sub>R</sub>	_	8.1	_			
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	20.1		ns		
Turn-Off Fall Time	t <sub>F</sub>	_	4.0	_			
Body Diode Reverse Recovery Time	t <sub>RR</sub>		18		ns	$I_S = 4.3A$ , di/dt = 100A/ $\mu$ s	
Body Diode Reverse Recovery Charge	$Q_{RR}$	_	11.9	_	nC	$I_S = 4.3A$ , $di/dt = 100A/\mu s$	

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

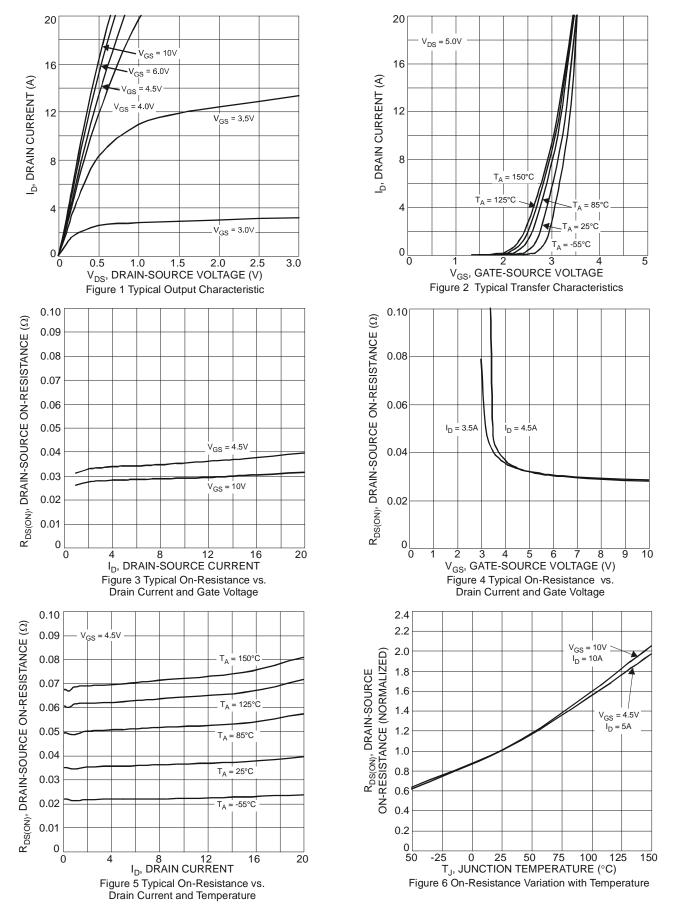
<sup>6.</sup> Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1-inch square copper plate.

<sup>7.</sup> I<sub>AS</sub> and E<sub>AS</sub> rating are based on low frequency and duty cycles to keep  $T_J = +25$ °C.

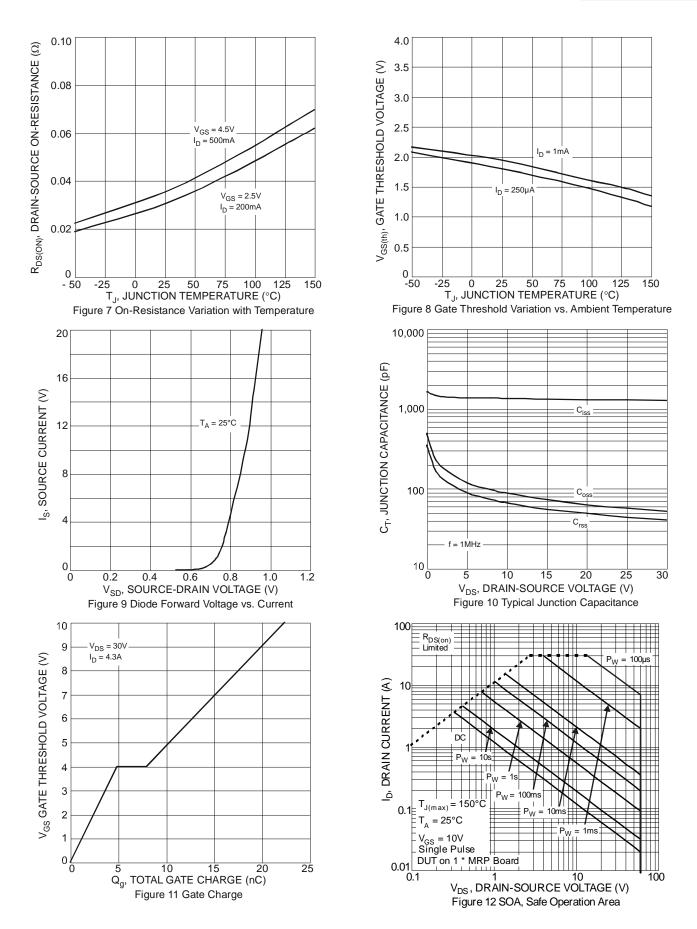
<sup>8.</sup> Short duration pulse test used to minimize self-heating effect.

<sup>9.</sup> Guaranteed by design. Not subject to product testing.

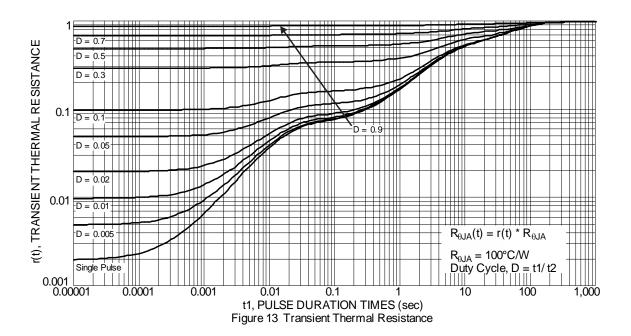








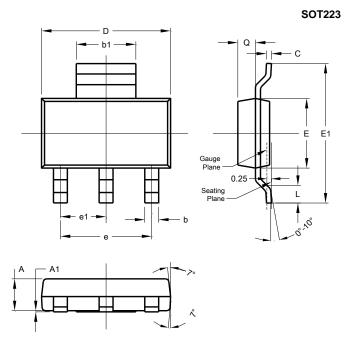






## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

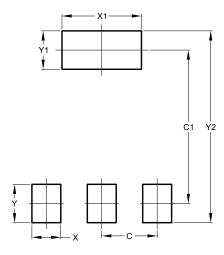


SOT223					
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
A1	0.010	0.15	0.05		
b	0.60	0.80	0.70		
b1	2.90	3.10	3.00		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
Е	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
е	_	_	4.60		
e1	_	-	2.30		
L	0.85	1.05	0.95		
ø	0.84	0.94	0.89		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### **SOT223**



Dimensions	Value (in mm)			
С	2.30			
C1	6.40			
Х	1.20			
X1	3.30			
Y	1.60			
Y1	1.60			
Y2	8.00			

July 2016



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