



600V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	RDS(ON) Max	I _D T _C = +25°C
600V	3.5Ω @ V _{GS} = 10V	2.8A

Description

This new generation complementary MOSFET features low onresistance and fast switching, making it ideal for high efficiency power management applications.

Applications

- Motor Control
- Backlighting
- DC-DC Converters
- Power Management Functions

Features

- Low Input Capacitance
- High BV_{DSS} Rating for Power Application
- Low Input/Output Leakage
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

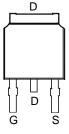
Mechanical Data

- Case: TO252 (DPAK) (Type TH)
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 ⁽³⁾
- Weight: 0.33 grams (Approximate)

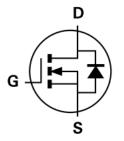




Top View



Top View



Internal Schematic

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN60H3D5SK3-13	TO252 (DPAK) (Type TH)	2,500/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



☐ H=Manufacturer's Marking
60H3D5S = Product Type Marking Code
YYWW = Date Code Marking
YY or <u>YY</u> = Last Two Digits of Year (ex: 17 = 2017)
WW or <u>WW</u> = Week Code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	600	V
Gate-Source Voltage	V_{GSS}	±30	V
Continuous Drain Current (Note 5) V _{GS} = 10V	Ι _D	2.8 1.8	Α
Maximum Body Diode Forward Current (Note 5)	Is	2.5	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	4.4	Α
Avalanche Current, L = 60mH (Note 7)	I _{AS}	1.0	Α
Avalanche Energy, L = 60mH (Note 7)	Eas	30	mJ
Peak Diode Recovery dv/dt (V _{DD} = 400V, I _D = 2.7A)	dv/dt	2.7	V/ns

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	$T_C = +25^{\circ}C$	Б	41	- W
Total Power Dissipation (Note 5)	$T_C = +100^{\circ}C$	P _D	16	
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	46	°C/W	
Thermal Resistance, Junction to Case (Note 5)	$R_{\theta JC}$	3.0	C/VV	
Operating and Storage Temperature Range	$T_{J,}T_{STG}$	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

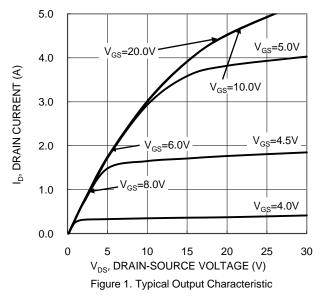
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	600		_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1.0	μΑ	$V_{DS} = 600V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 30V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	$V_{GS(TH)}$	2.0	3.1	4.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	2.7	3.5	Ω	$V_{GS} = 10V, I_D = 1.5A$	
Diode Forward Voltage	V_{SD}	_	0.9	1.5	V	$V_{GS} = 0V, I_{S} = 3.0A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss	_	354	_			
Output Capacitance	Coss	_	41	_	pF	$V_{DS} = 25V$, $V_{GS} = 0V$, $f = 1.0MHz$	
Reverse Transfer Capacitance	Crss	_	4	_			
Gate Resistance	Rg	_	2.6	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 10V)	Q_g	_	12.6	_			
Gate-Source Charge	Q_{gs}	_	1.7	_	nC	$V_{DS} = 480V, I_{D} = 2.5A$	
Gate-Drain Charge	Q_{gd}	_	7.1	_			
Turn-On Delay Time	t _{D(ON)}	_	10.6	_			
Turn-On Rise Time	t _R	_	22	_	ns	$V_{GS} = 10V$, $V_{DD} = 300V$, $R_{G} = 25\Omega$,	
Turn-Off Delay Time	t _{D(OFF)}	_	34	_	115	$I_D = 2.5A$	
Turn-Off Fall Time	t _F	_	28	_			
Body Diode Reverse Recovery Time	t _{RR}	_	198	_	ns	\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Body Diode Reverse Recovery Charge	Q _{RR}		952		nC	$V_{GS} = 0V$, $I_S = 2.5A$, $dI/dt = 100A/\mu$	

Notes:

- 5. Device mounted on infinite heatsink.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
 7. Guaranteed by design. Not subject to production testing.
 8. Short duration pulse test used to minimize self-heating effect.







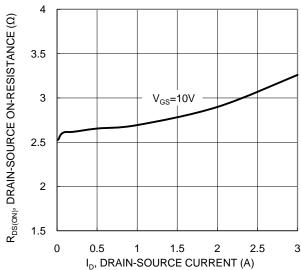


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

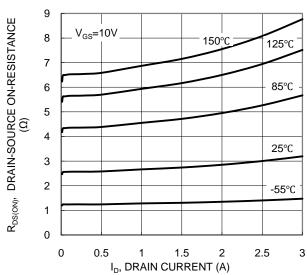
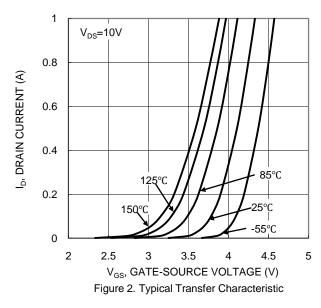
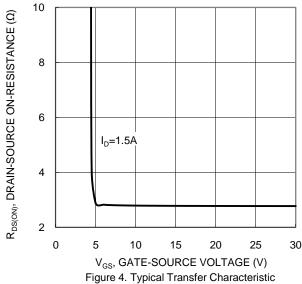


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





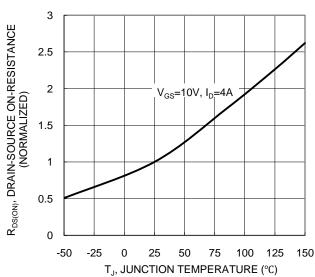


Figure 6. On-Resistance Variation with Temperature





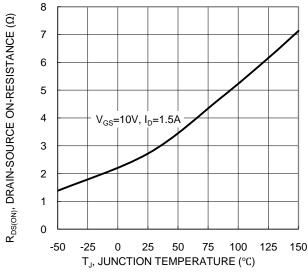
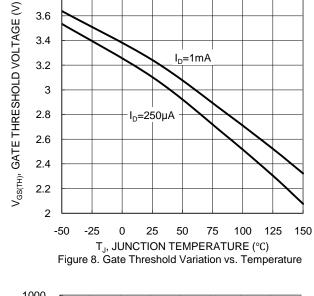
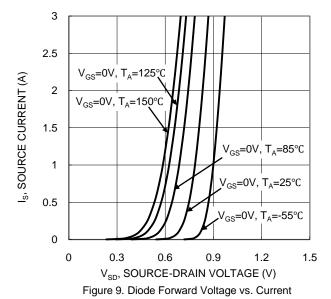
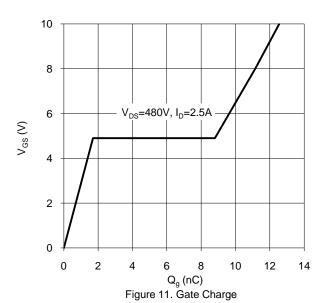


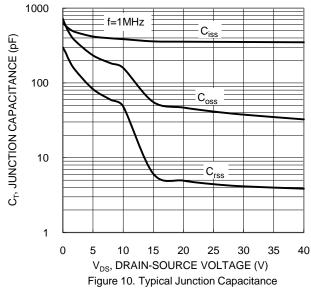
Figure 7. On-Resistance Variation with Temperature

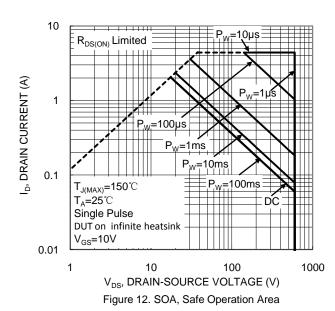


3.8











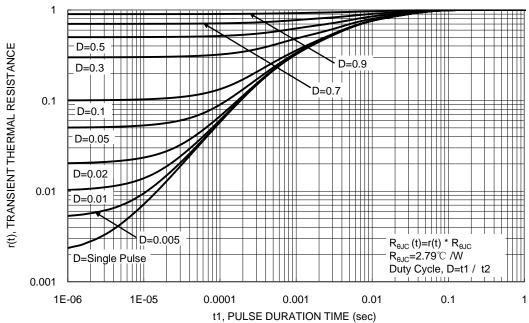


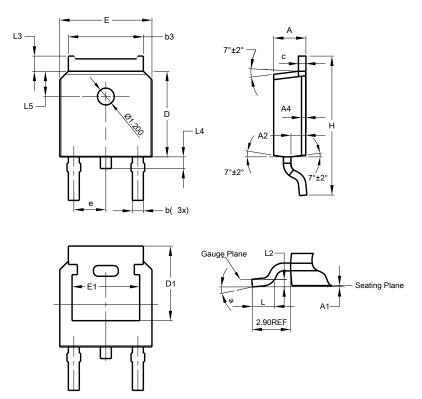
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO252 (DPAK) (Type TH)

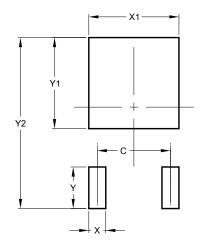


TO252 (DPAK)					
(Type TH)					
Dim	Min				
Α	2.20	2.38	2.30		
A1	0.00	0.10	-		
A2	0.97	1.17	1.07		
A4	0	.10 RE	F		
b	0.72	0.85	0.78		
b3	5.23	5.45	5.33		
С	0.47	0.58	0.53		
D	6.00	6.20	6.10		
D1	5.30 REF				
е	2.286 BSC				
Е	6.50	6.70	6.60		
E1	4.70	4.92	4.83		
Н	9.90	10.10	10.30		
L	1.40	1.70	1.60		
L2	0.51 BSC				
L3	0.90	1.25	-		
L4	0.60	1.00	0.80		
L5	1.70	1.90	1.80		
а	0°	8°	-		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO252 (DPAK) (Type TH)



Dimensions	Value (in mm)		
С	4.572		
Х	1.060		
X1	5.632		
Y	2.600		
Y1	5.700		
Y2	10 700		



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