



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C	
601/	2Ω @ $V_{GS} = 4.5V$	220m A	
60V	2.5Ω @ V _{GS} = 2.5V	320mA	

Description

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- Motor Control
- Power Management Functions

Features

- Low On-Resistance: R_{DS(ON)}
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 1kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

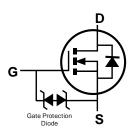
- Case: SOT523
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 <a> § 3
- Terminal Connections: See Diagram
- Weight: 0.002 grams (Approximate)



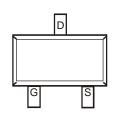




Top View



Equivalent Circuit



Top View Pin Out

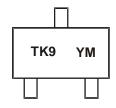
Ordering Information (Note 4)

Part Number	Case	Packaging		
DMN62D0UT-7	SOT523	3,000/Tape & Reel		
DMN62D0UT-13	SOT523	10,000/Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



TK9 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: D = 2016) M = Month (ex: 9 = September)

Date Code Key

Year	2016	2	2017	2018		2019	2020		2021	2022		2023
Code	D		E	F		G	Н		I	J		K
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Drain Current (Note 6) $V_{GS} = 4.5V$ Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$			I _D	320 260	mA
Maximum Continuous Body Diode Forward Current	(Note 6)	I _S	0.4	Α	
Pulsed Drain Current (10µs pulse, duty cycle = 1%))	I _{DM}	1.2	Α	

Thermal Characteristics (@T_A = +25°C unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)		P_{D}	230	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	546	°C/W
Total Power Dissipation (Note 6)		P_{D}	340	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	377	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C unless otherwise specified.)

	1			1		
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_		1.0	μΑ	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.5	_	1.0	V	$V_{DS} = 10V, I_D = 250\mu A$
			1.2	2.0		$V_{GS} = 4.5V, I_D = 0.1A$
Static Drain-Source On-Resistance	R _{DS(ON)}	_	1.4	2.5	Ω	$V_{GS} = 2.5V, I_D = 0.05A$
			1.8	3.0		$V_{GS} = 1.8V, I_D = 0.05A$
Forward Transconductance	Y _{fs}	_	1.8	_	S	$V_{DS} = 10V, I_D = 0.2A$
Diode Forward Voltage	V_{SD}	_	0.8	1.3	V	$V_{GS} = 0V, I_{S} = 115mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	32	_	рF	.,
Output Capacitance	Coss	_	3.9	_	рF	$V_{DS} = 30V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	2.4	_	рF	1 – 1.000112
Gate Resistance	R_g	_	101	_	Ω	$f = 1MHz$, $V_{GS} = 0V$, $V_{DS} = 0V$
Total Gate Charge	Qg	_	0.5	_	nC	\\\\ 4.5\\\\\\\\ 40\\\
Gate-Source Charge	Q_{gs}	_	0.09	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$ $I_{D} = 250 \text{mA}$
Gate-Drain Charge	Q_{gd}	_	0.09	_	nC	ID = 250IIIA
Turn-On Delay Time	t _{D(ON)}	_	2.4	_	ns	
Turn-On Rise Time	t _R	_	2.5	_	ns	$V_{DD} = 30V, V_{GS} = 10V,$
Turn-Off Delay Time	t _{D(OFF)}	_	22.6	_	ns	$R_G = 25\Omega$, $I_D = 200mA$
Turn-Off Fall Time	t _F		12.5	_	ns	

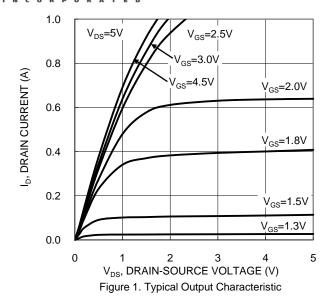
Notes:

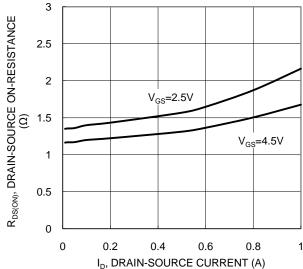
- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
 - 6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided. 7. Short duration pulse test used to minimize self-heating effect.

 - 8. Guaranteed by design. Not subject to product testing.



DMN62D0UT





I_D, DRAIN-SOURCE CURRENT (A)
Figure 3. Typical On-Resistance vs. Drain Current and
Gate Voltage

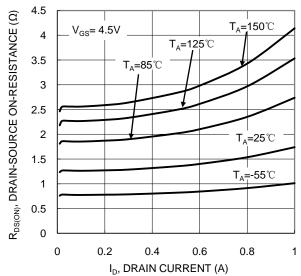
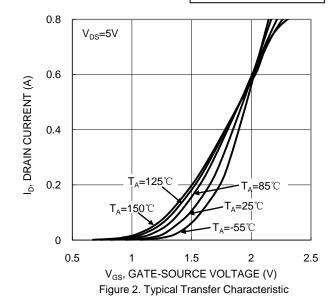
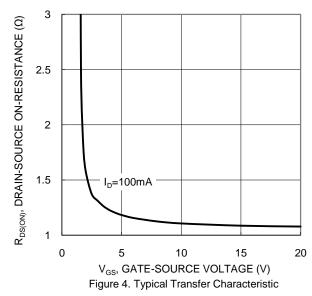


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





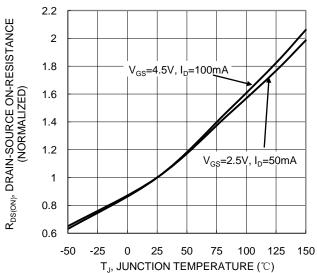
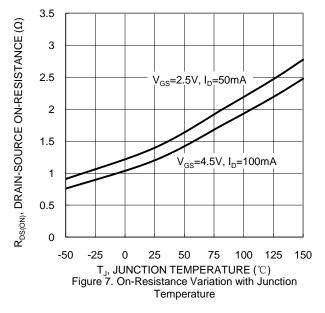
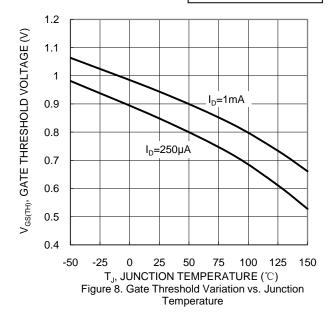


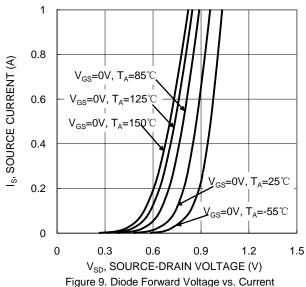
Figure 6. On-Resistance Variation with Junction Temperature

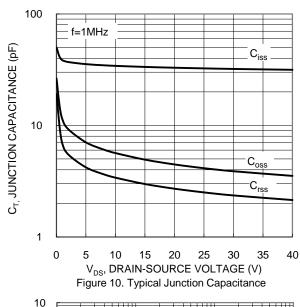


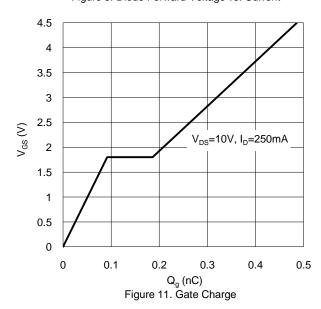
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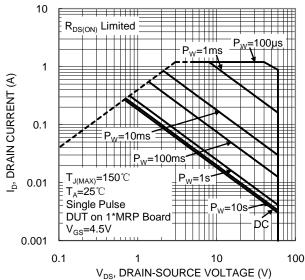


Figure 12. SOA, Safe Operation Area



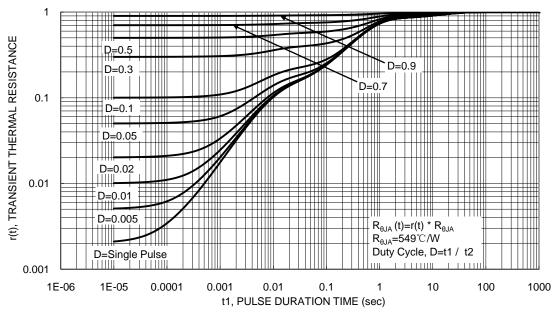


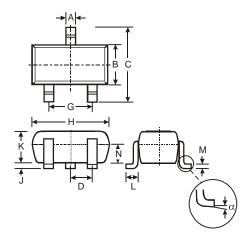
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT523

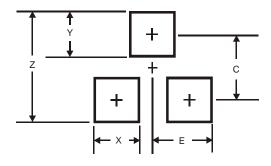


SOT523							
Dim	Min	Max	Тур				
Α	0.15	0.30	0.22				
В	0.75	0.85	0.80				
С	1.45	1.75	1.60				
D			0.50				
G	0.90	1.10	1.00				
Н	1.50	1.70	1.60				
J	0.00	0.10	0.05				
K	0.60	0.80	0.75				
L	0.10	0.30	0.22				
М	0.10	0.20	0.12				
N	0.45	0.65	0.50				
α	0°	8°	_				
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT523



Dimensions	Value (in mm)
Z	1.8
Х	0.4
Y	0.51
С	1.3
E	0.7



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