

DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
60V	2Ω @ V _{GS} = 4.5V	490mA
000	2.5Ω @ V _{GS} = 2.5V	430mA

Features and Benefits

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Mechanical Data

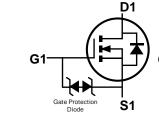
- Case: SOT563
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.006 grams (Approximate)

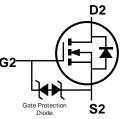


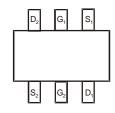


Top View

SOT563







Top View

Pin Out

Equivalent Circuit

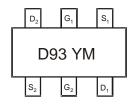
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN62D0UV-7	SOT563	3,000/Tape & Reel
DMN62D0UV-13	SOT563	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



D93 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: D = 2016) M = Month (ex: 9 = September)

Date Code Key

Year	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027
Code	D	Е	F	G	Н		J	K	L	М	N	0

	Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Ī	Code	1	2	3	4	5	6	7	8	9	0	Ν	D



Characteristic		Symbol	Value	Unit	
Drain-Source Voltage	V_{DSS}	60	V		
Gate-Source Voltage	V_{GSS}	±20	V		
Continuous Drain Current (Note 6) V _{GS} = 4.5V Steady State		$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	490 390	mA
Maximum Continuous Body Diode Forward Current	(Note 6)	Is	0.4	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	6)		I _{DM}	1.2	Α

Thermal Characteristics ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_{D}	470	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	271	°C/W
Total Power Dissipation (Note 6)		P _D	740	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	173	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)				•		
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1.0	μΑ	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.5	_	1.0	V	$V_{DS} = 10V, I_D = 250\mu A$
			1.2	2.0		$V_{GS} = 4.5V, I_D = 0.1A$
Static Drain-Source On-Resistance	R _{DS(ON)}	_	1.4	2.5	Ω	$V_{GS} = 2.5V, I_D = 0.05A$
			1.8	3.0		$V_{GS} = 1.8V, I_D = 0.05A$
Forward Transconductance	Y _{fs}	_	1.8	_	S	$V_{DS} = 10V, I_D = 0.2A$
Diode Forward Voltage	V _{SD}	_	8.0	1.3	V	$V_{GS} = 0V, I_{S} = 115mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	32	_	pF	.,
Output Capacitance	Coss	_	3.9	_	pF	$V_{DS} = 30V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	2.4	_	pF	1 = 1.000112
Gate Resistance	R_g	_	101	_	Ω	$f = 1MHz$, $V_{GS} = 0V$, $V_{DS} = 0V$
Total Gate Charge	Qg	_	0.5	_	nC	1/ 451/1/ 401/
Gate-Source Charge	Q_{gs}	_	0.09	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$
Gate-Drain Charge	Q _{gd}	_	0.09	_	nC	I _D = 250mA
Turn-On Delay Time	t _{D(ON)}	_	2.4	_	ns	
Turn-On Rise Time	t _R	_	2.5	_	ns	$V_{DD} = 30V, V_{GS} = 10V,$
Turn-Off Delay Time	t _{D(OFF)}	_	22.6	_	ns	$R_G = 25\Omega$, $I_D = 200 \text{mA}$
Turn-Off Fall Time	t _F		12.5		ns	

- Device mounted on FR-4 PCB, with minimum recommended pad layout.
 Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.



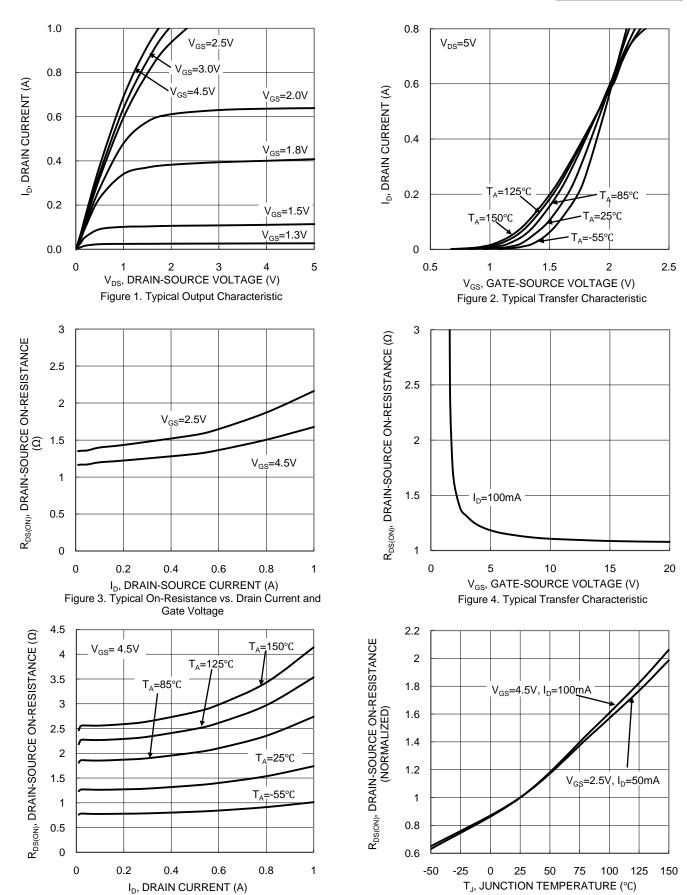
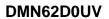


Figure 5. Typical On-Resistance vs. Drain Current and

Temperature

Figure 6. On-Resistance Variation with Junction

Temperature





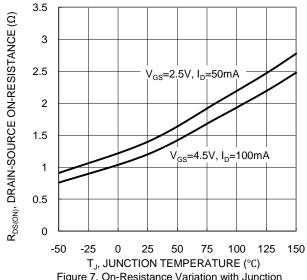
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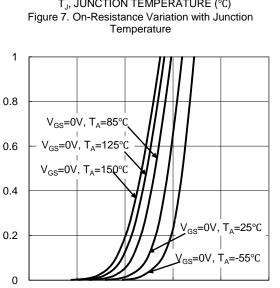
0.4

0

0.3

Is, SOURCE CURRENT (A)





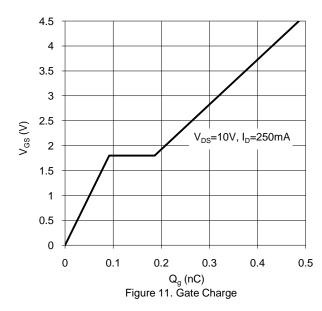
V_{SD}, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current

0.9

1.2

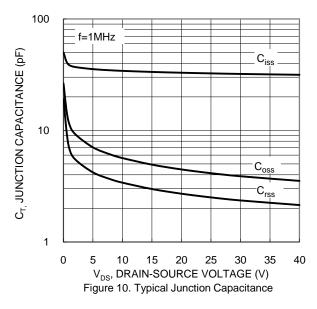
1.5

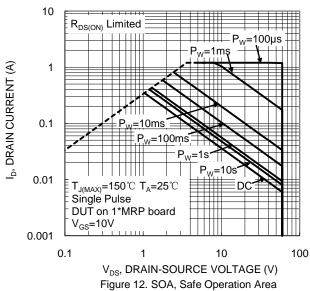
0.6



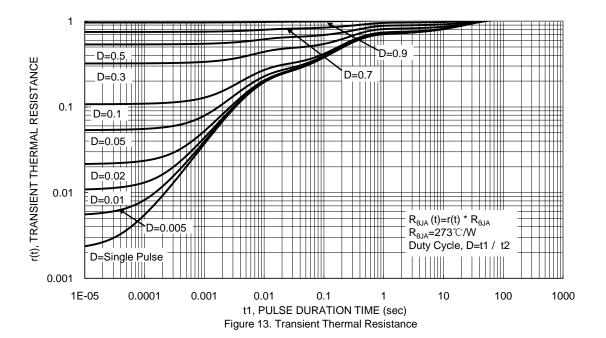
1.2 $V_{GS(TH)}$, GATE THRESHOLD VOLTAGE (V) 1.1 $I_D=1mA$ 0.9 0.8 $I_{D} = 250 \mu A$ 0.7 0.6 0.5 0.4 -50 -25 0 25 50 75 100 125 150 T_J, JUNCTION TEMPERATURE (°C) Figure 8. Gate Threshold Variation vs. Junction

Temperature









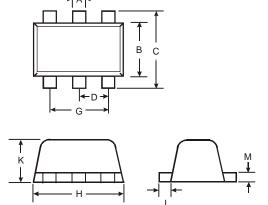
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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT563

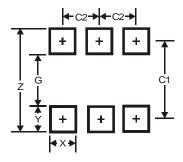


	SO	T563		
Dim	Min	Max	Тур	
Α	0.15	0.30	0.20	
В	1.10	1.25	1.20	
С	1.55	1.70	1.60	
D	-	-	0.50	
G	0.90	1.10	1.00	
Н	1.50	1.70	1.60	
K	0.55	0.60	0.60	
L	0.10	0.30	0.20	
М	0.10	0.18	0.11	
All	Dimens	sions in	mm	

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT563



Dimensions	Value (in mm)
Z	2.2
G	1.2
Х	0.375
Y	0.5
C1	1.7
C2	0.5



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