



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
60V	2Ω @ $V_{GS} = 4.5V$	340mA
000	2.5Ω @ V _{GS} = 2.5V	300mA

Description

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Motor Control
- **Power Management Functions**
- Backlighting

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- 100% Rg Test in Production
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- **PPAP Capable (Note 4)**

Mechanical Data

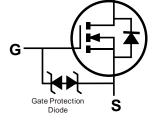
- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.006 grams (Approximate)

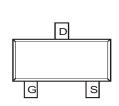






SOT323





Top View

Equivalent Circuit

Top View

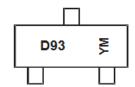
Ordering Information (Note 5)

Part Number	Case	Packaging		
DMN62D0UWQ-7	SOT323	3000/Tape & Reel		
DMN62D0UWQ-13	SOT323	10000/Tape & Reel		

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



D93= Product Type Marking Code YM = Date Code Marking Y = Year (ex: G = 2019)M = Month (ex: 9 = September)

Date Code Key

Year	2018	3 20	19	2020	2021	2022	2023	2024	4 2	2025	2026	2027
Code	F	(3	Н	I	J	K	L		M	N	0
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit		
Drain-Source Voltage		V _{DSS}	60	V		
Gate-Source Voltage		V _{GSS}	±20	V		
Continuous Dusin Compart (Alata 7) // 4.5/	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	340 270	mA	
Continuous Drain Current (Note 7) V _{GS} = 4.5V	t<5s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	400 300	mA	
Maximum Continuous Body Diode Forward Current	(Note 7)	Is	0.4	Α		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	<u>(</u>)	I _{DM}	1.2	Α		
Pulsed Source Current (10µs Pulse, Duty Cycle = 1	%)	I _{SM}	1.2	А		

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 6)		P_{D}	320	mW	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	398	°C/W	
Thermal Resistance, Junction to Ambient (Note o)	t<5s	$R_{\theta JA}$	306	C/VV	
Total Power Dissipation (Note 7)		P_{D}	470	mW	
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	D	273	°C/W	
Thermal Resistance, Junction to Ambient (Note 1)	t<5s	$R_{\theta JA}$	235	C/VV	
Operating and Storage Temperature Range	`	$T_{J_i}T_{STG}$	-55 to +150	°C	

Electrical Characteristics ($@T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_{D} = 10\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1.0	μΑ	$V_{DS} = 60V$, $V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	0.5	_	1.0	V	$V_{DS} = 10V, I_D = 250\mu A$
			1.2	2.0		$V_{GS} = 4.5V, I_D = 0.1A$
Static Drain-Source On-Resistance	R _{DS(ON)}		1.4	2.5	Ω	$V_{GS} = 2.5V, I_D = 0.05A$
			1.8	3.0		$V_{GS} = 1.8V, I_D = 0.05A$
Forward Transconductance	Y _{fs}	_	1.8	_	S	$V_{DS} = 10V, I_D = 0.2A$
Diode Forward Voltage	V_{SD}	_	0.8	1.3	V	$V_{GS} = 0V, I_{S} = 115mA$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss	_	32	_	pF	.,
Output Capacitance	Coss	_	3.9	_	pF	$V_{DS} = 30V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	2.4	_	pF	1 = 1.0WHZ
Gate Resistance	Rg	_	101	_	Ω	$f = 1MHz$, $V_{GS} = 0V$, $V_{DS} = 0V$
Total Gate Charge	Qg	_	0.5	_	nC	151/1/ 101/
Gate-Source Charge	Qgs	_	0.09	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$
Gate-Drain Charge	Q_{gd}	_	0.09	_	nC	I _D = 250mA
Turn-On Delay Time	t _{D(ON)}	_	2.4	_	ns	
Turn-On Rise Time		_	2.5	_	ns	$V_{DD} = 30V, V_{GS} = 10V,$
Turn-Off Delay Time	t _{D(OFF)}	_	22.6	_	ns	$R_G = 25\Omega$, $I_D = 200 \text{mA}$
Turn-Off Fall Time	t _F	_	12.5	_	ns	

Notes:

- 6. Device mounted on FR-4 PCB, with minimum recommended pad layout.
- 7. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to product testing.





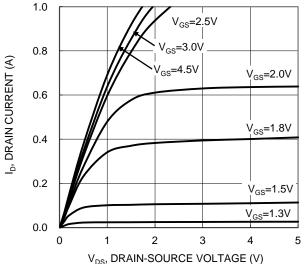
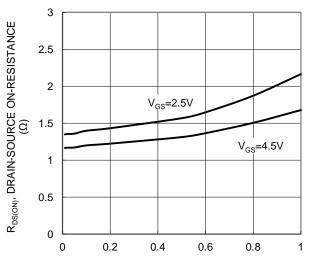


Figure 1. Typical Output Characteristic



I_D, DRAIN-SOURCE CURRENT (A) Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

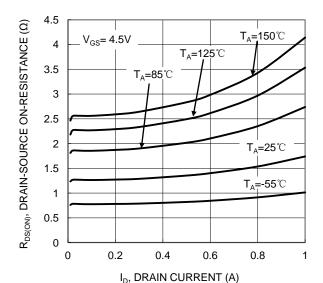


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

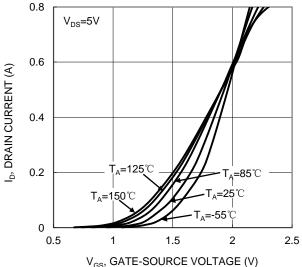


Figure 2. Typical Transfer Characteristic

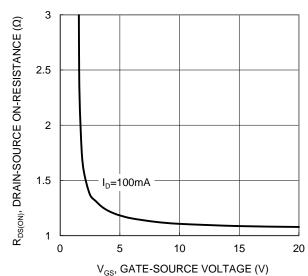


Figure 4. Typical Transfer Characteristic

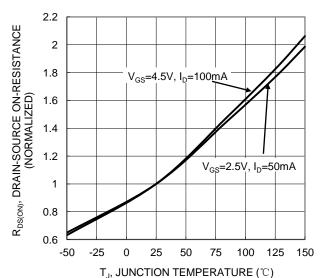


Figure 6. On-Resistance Variation with Junction Temperature





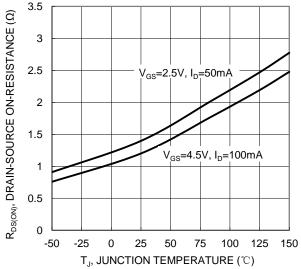


Figure 7. On-Resistance Variation with Junction Temperature

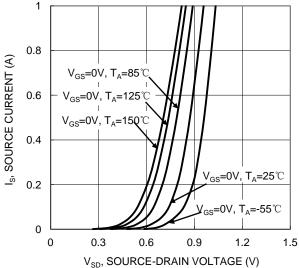
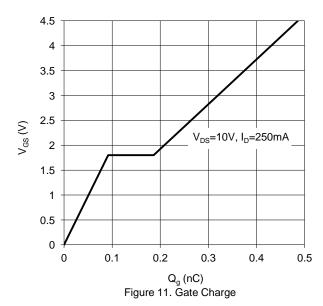
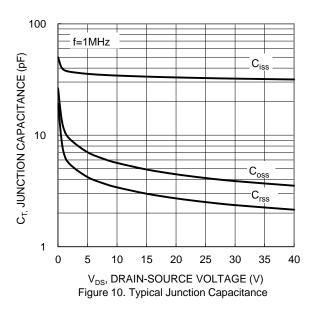


Figure 9. Diode Forward Voltage vs. Current



1.2 $V_{GS(TH)},$ GATE THRESHOLD VOLTAGE (V) 1.1 $I_D=1mA$ 0.9 8.0 I_D=250μA 0.7 0.6 0.5 0.4 -50 -25 0 25 50 75 100 125 150 T_J , JUNCTION TEMPERATURE ($^{\circ}$ C)

Figure 8. Gate Threshold Variation vs. Junction Temperature



10 R_{DS(ON)} LIMITED ID, DRAIN CURRENT (A) 0.1 P_W=100ms T_{J(Max)}=150℃ 0.01 T_A=25° ℃ 10s Single Pulse DC DUT on 1*MRP board V_{GS}=10V 0.001 0.1 100 V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



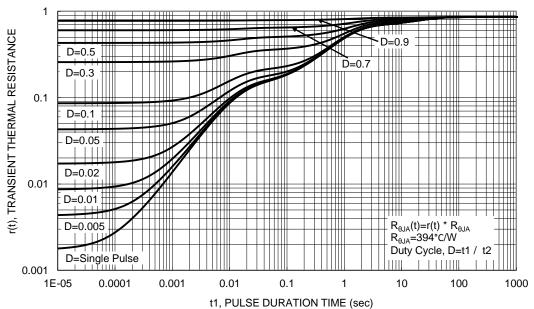


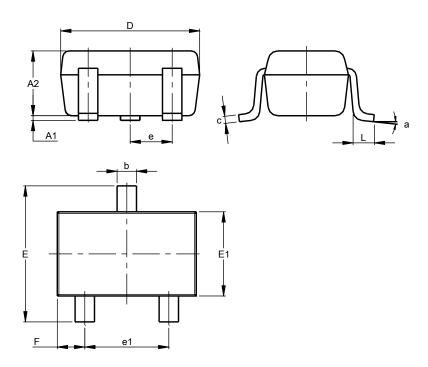
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323

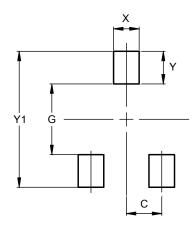


SOT323							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.25	0.40	0.30				
С	0.10	0.18	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	C).650 B	SC				
e1	1.20	1.40	1.30				
F	0.375	0.475	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2.500



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