



DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
60V	2Ω @ V_{GS} = 10 V	550mA
007	3Ω @ V_{GS} = $5V$	SSUIIA

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Motor Control
- Power Management Functions
- Backlighting

Features and Benefits

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected up to 2kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

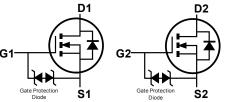
- Case: SOT563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 ⁽³⁾
- Weight: 0.006 grams (Approximate)



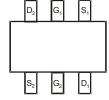


SOT563

Top View







Top View Pin out

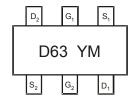
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN63D1LV-7	SOT563	3000/Tape & Reel
DMN63D1LV-13	SOT563	10000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



D63 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: C = 2015) M = Month (ex: 9 = September)

Date Code Key

Year	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025
Code	В	С	D	Е	F	G	Н		J	K	L	М

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	60	V		
Gate-Source Voltage	V_{GSS}	±20	V		
Continuous Drain Current (Note 6) V _{GS} = 10V Stead State		T _A = +25°C T _A = +70°C	I _D	550 450	mA
Maximum Continuous Body Diode Forward Curren	t (Note 6)	Is	0.5	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	6)	I _{DM}	1.2	Α	

Thermal Characteristics ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_D	450	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	283	°C/W
Total Power Dissipation (Note 6)		P_{D}	940	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	133	°C/W
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to +150	°C

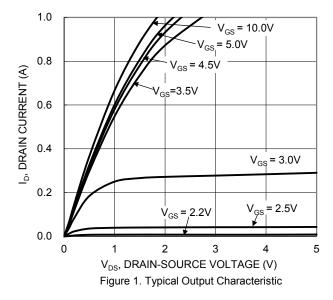
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

\						
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_{D} = 10\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1.0	μΑ	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	1.0	1.6	2.5	V	$V_{DS} = 10V, I_{D} = 1mA$
Static Drain-Source On-Resistance		_	_	2.0	Ω	$V_{GS} = 10V, I_D = 0.5A$
Static Dialit-Source Off-Resistance	R _{DS(ON)}	_	_	3.0	12	$V_{GS} = 5V, I_D = 0.05A$
Forward Transfer Admittance	Y _{fs}	80	_	_	mS	V _{DS} = 10V, I _D = 0.2A
Diode Forward Voltage	V_{SD}	_	0.75	1.2	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	30	_	pF	N/ 051/ N/ 01/
Output Capacitance	Coss	_	4.2	_	pF	$V_{DS} = 25V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	2.9	_	pF	1 - 1.0MHZ
Gate Resistance	Rg	_	133	_	Ω	f = 1MHz, V _{GS} = 0V, V _{DS} = 0V
Total Gate Charge	Qq	_	392	_	рC	4.5)/.)/
Gate-Source Charge	Qgs	_	157	_	рC	$V_{GS} = 4.5V, V_{DS} = 10V,$
Gate-Drain Charge	Q _{qd}	_	92	_	рC	I _D = 250mA
Turn-On Delay Time	t _{D(ON)}	_	3.9	_	ns	
Turn-On Rise Time	t _R	_	3.4	_	ns	V _{DD} = 30V, V _{GS} = 10V,
Turn-Off Delay Time	t _{D(OFF)}	_	15.7	_	ns	$R_G = 25\Omega$, $I_D = 200 \text{mA}$
Turn-Off Fall Time	t _F	_	9.9	_	ns	

- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
- 6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
- 7. Short duration pulse test used to minimize self-heating effect.

 8. Guaranteed by design. Not subject to product testing.





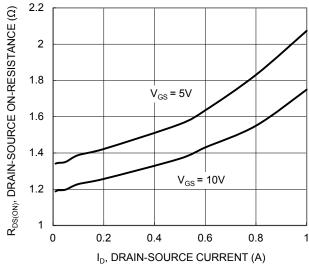
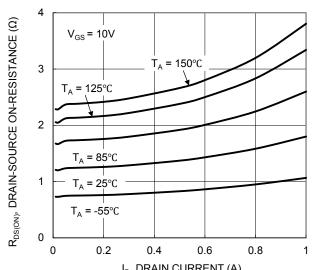
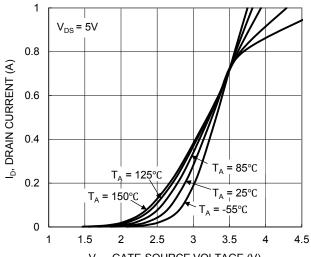


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage



I_D, DRAIN CURRENT (A) Figure 5. Typical On-Resistance vs. Drain Current and Temperature



V_{GS}, GATE-SOURCE VOLTAGE (V) Figure 2. Typical Transfer Characteristic

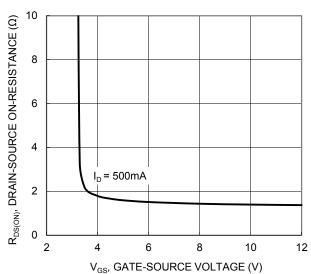


Figure 4. Typical Transfer Characteristic

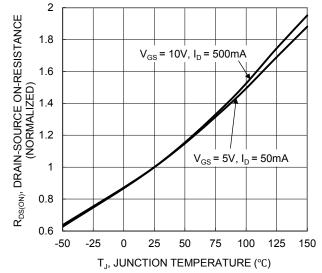


Figure 6. On-Resistance Variation with Junction Temperature



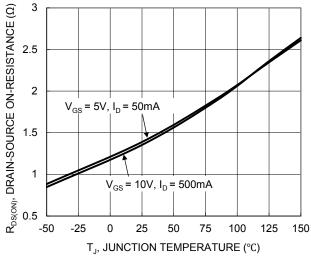
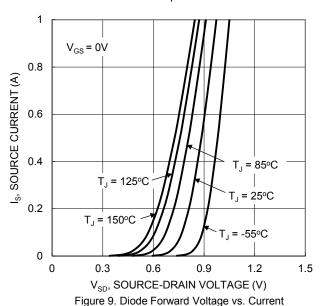


Figure 7. On-Resistance Variation with Junction Temperature



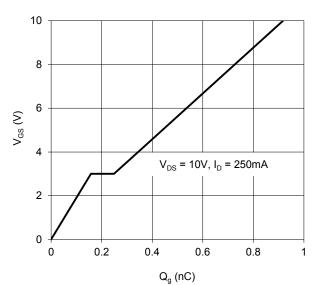


Figure 11. Gate Charge

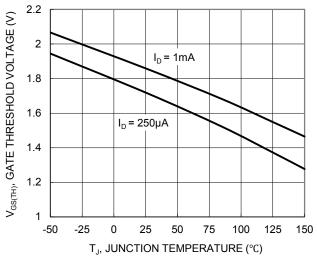
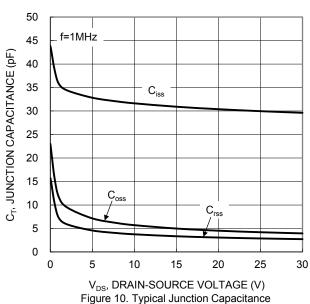


Figure 8. Gate Threshold Variation vs. Junction Temperature



0.01

R_{DS(ON)} Limited

P_W =1ms P_W =100µs

P_W =100ms

P_W =100ms

P_W =100ms

P_W =100ms

DC

Single Pulse
DUT on 1*MRP Board
V_{GS} = 10V

0.001

0.1

1 10 100

V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



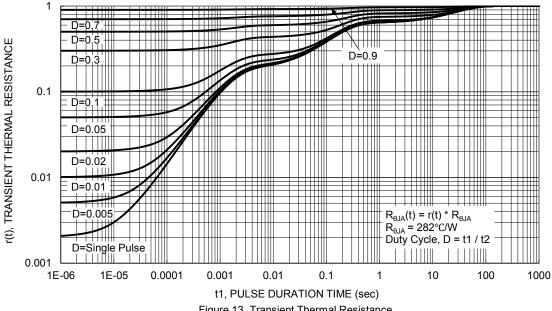
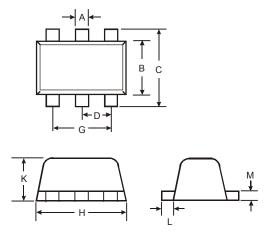


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

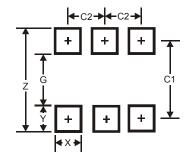
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



	SOT563								
Dim	Min	Max	Тур						
Α	0.15	0.30	0.20						
В	1.10	1.25	1.20						
С	1.55	1.70	1.60						
D	-	-	0.50						
G	0.90	1.10	1.00						
Н	1.50	1.70	1.60						
K	0.55	0.60	0.60						
L	0.10	0.30	0.20						
М	0.10	0.18	0.11						
All	Dimens	sions in	mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.2
G	1.2
X	0.375
Y	0.5
C1	1.7
C2	0.5



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