



#### N-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	Rds(on)	I <sub>D</sub> T <sub>A</sub> = +25°C
60V	3Ω @ V <sub>GS</sub> = 10V	310mA
607	4Ω @ VGS = 5V	270mA

### **Description and Applications**

This new generation MOSFET has been designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

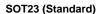
## **Features and Benefits**

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Small Surface Mount Package
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

  https://www.diodes.com/quality/product-definitions/
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMN65D8LQ)

### **Mechanical Data**

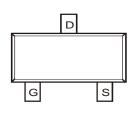
- Case: SOT23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Solderable per MIL-STD-202, Method 208
   Lead Free Plating (Matte Tin Finish Annealed over Alloy 42
   Leadframe).
- Terminal Connections: See Diagram
- Weight: 0.008487 grams (Approximate)



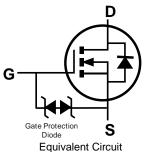




Top View



Top View Pin Configuration



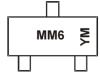
#### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMN65D8L-7	SOT23 (Standard)	3,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



 $\begin{array}{l} MM6 = Product\ Type\ Marking\ Code \\ YM = Date\ Code\ Marking \\ Y\ or\ \overline{Y} = Year\ (ex:\ I=2021) \\ M\ or\ \overline{M} = Month\ (ex:\ 9=September) \end{array}$ 

Date Code Key

Year	2012		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	Z		- 1	J	K	L	М	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



# **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	VDSS	60	V		
Gate-Source Voltage	$V_{GSS}$	±20	V		
Continuous Drain Current (Note 6) Vgs = 10V	l <sub>D</sub>	310 240	mA		
Continuous Drain Current (Note 6) Vgs = 5V	lo	270 210	mA		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	800	mA		
Maximum Body Diode Continuous Current (Note 6)			Is	310	mA

### Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation	(Note 6)	Pn	370	mW	
Total Fower Dissipation	(Note 5)	FD	540		
Thermal Resistance, Junction to Ambient	(Note 6)	0	348		
Thermal Resistance, Junction to Ambient	(Note 5)	$R_{\theta JA}$	241	°C/W	
Thermal Resistance, Junction to Case	(Note 5)	R <sub>θ</sub> JC	91		
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

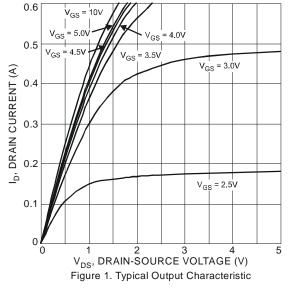
# **Electrical Characteristics** (@TA = +25°C, unless otherwise specified.)

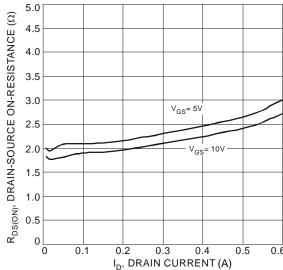
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)				•	•		
Drain-Source Breakdown Voltage	BVDSS	60	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1.0	μΑ	$V_{DS} = 60V$ , $V_{GS} = 0V$	
Gate-Body Leakage	Igss	_	_	±5	μΑ	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)	-						
Gate Threshold Voltage	Vgs(TH)	1.2	_	2.0	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	D	_	1.9	3	Ω	V <sub>G</sub> S = 10V, I <sub>D</sub> = 0.115A	
Static Drain-Source Off-Resistance	RDS(ON)	_	2.2	4	Ω	$V_{GS} = 5V, I_D = 0.115A$	
Forward Transconductance	grs	80	290	_	ms	V <sub>DS</sub> = 10V, I <sub>D</sub> = 0.115A	
Diode Forward Voltage	VsD	_	0.8	1.2	V	V <sub>G</sub> S = 0V, I <sub>S</sub> = 115mA	
DYNAMIC CHARACTERISTICS (Note 8)		•			•		
Input Capacitance	Ciss	_	22	_			
Output Capacitance	Coss	_	3.2	_	pF	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$	
Reverse Transfer Capacitance	Crss	_	2.0	_			
Gate Resistance	Rg	_	79.9	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1.0MHz$	
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	0.87	_			
Total Gate Charge (VGS = 4.5V)	Qg	_	0.43	_		V <sub>G</sub> S = 10V, V <sub>D</sub> S = 30V,	
Gate-Source Charge	Qgs	_	0.11	_	nC	I <sub>D</sub> = 150mA	
Gate-Drain Charge	Qgd	_	0.11	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	2.7	_			
Turn-On Rise Time	t <sub>R</sub>	_	2.8	_		$V_{DD} = 30V$ , $I_{D} = 0.115A$ , $V_{GEN} = 10V$ , $R_{GEN} = 25\Omega$	
Turn-Off Delay Time	tD(OFF)	_	12.6	_	ns		
Turn-Off Fall Time	t <sub>F</sub>	_	7.3	_	1		

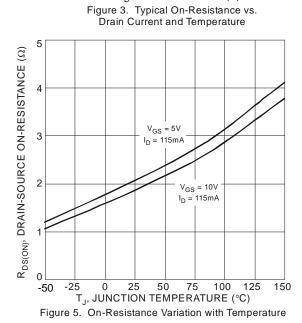
Notes:

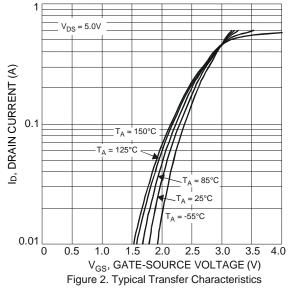
- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
  6. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  7. Short duration pulse test used to minimize self-heating effect.
  8. Guaranteed by design. Not subject to production testing.











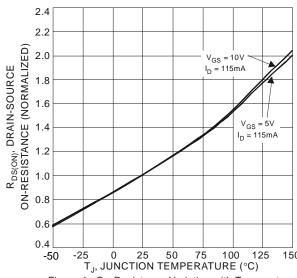


Figure 4. On-Resistance Variation with Temperature

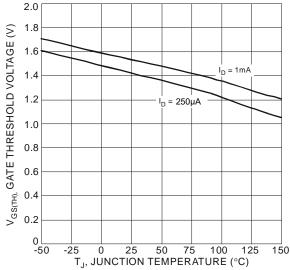
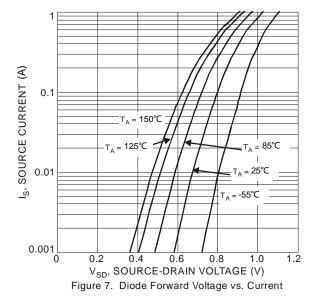


Figure 6. Gate Threshold Variation vs. Junction Temperature





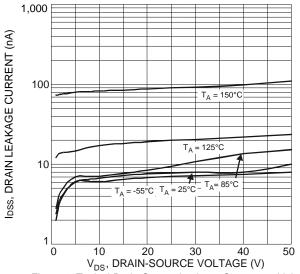
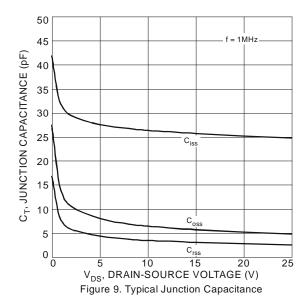


Figure 8. Typical Drain-Source Leakage Current vs. Voltage

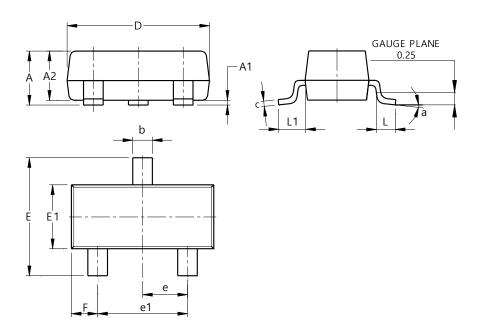




## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23 (Standard)

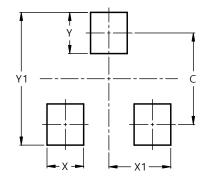


S	SOT23 (Standard)							
Dim	Min	Max	Тур					
Α	0.90	1.15	1.025					
A1	0.00	0.10	0.05					
A2	0.85	1.10	0.975					
b	0.30	0.51	0.40					
С	0.080	0.202	0.11					
D	2.80	3.00	2.90					
Е	2.25	2.55	2.40					
E1	1.20	1.40	1.30					
е	0.89	1.03	0.915					
e1	1.78	2.05	1.83					
F	0.40	0.60	0.535					
L1	0.45	0.61	0.55					
L	0.25	0.55	0.40					
а	0°	8°						
All Dimensions in mm								

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23 (Standard)



Dimensions	Value (in mm)
С	2.0
X	0.8
X1	1.35
Υ	0.9
Y1	2.9



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