



DMN65D9L

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	4.0Ω @ V _{GS} = 10V	335mA
60V	4.1Ω @ V _{GS} = 5V	330mA
	4.2Ω @ V _{GS} = 4V	327mA

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDs(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Control
- Power Management Functions
- Backlighting

N-CHANNEL ENHANCEMENT MODE MOSFET

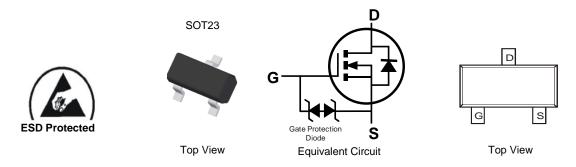
Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative.

https://www.diodes.com/guality/product-definitions/

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe.
 Solderable per MIL-STD-202, Method 208 (€3)
- Terminal Connections: See Diagram
- Weight: 0.008 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMN65D9L-7	SOT23	3000/Tape & Reel
DMN65D9L-13	SOT23	10000/Tape & Reel

No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

5D9 <mark>∑</mark>	

5D9 = Product Type Marking Code

 $\overline{Y}M = Date Code Marking$

 \overline{Y} = Year (ex: I = 2021)

M = Month (ex: 9 = September)

Date Code Key

Notes:

Year	2018		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	F			J	K	L	М	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			Vdss	60	V
Gate-Source Voltage			V _{GSS}	±16	V
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	ID	335 268	mA
Maximum Body Diode Forward Current (Note 6)			ls	335	mA
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%		Ідм	700	mA	
Pulsed Source Current (10µs Pulse, Duty Cycle =	1%)		lsм	700	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	270	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	293	°C/W
Total Power Dissipation (Note 6)		PD	670	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	186	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	C°

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	-					•
Drain-Source Breakdown Voltage	BVDSS	60		—	V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	IDSS	-	_	1.0	μA	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	lgss	—	_	±10	μA	$V_{GS} = \pm 16V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	VGS(TH)	1.0		2.5	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
			3.2	4.0		VGS = 10V, ID = 0.5A
Static Drain-Source On-Resistance	R _{DS(ON)}	—	2.9	4.1	Ω	$V_{GS} = 5V, I_D = 0.2A$
			3.0	4.2		V _{GS} = 4V, I _D = 0.2A
Diode Forward Voltage	Vsd	_	0.8	1.1	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS (Note 8)					-	
Input Capacitance	Ciss	—	41	—	pF	
Output Capacitance	Coss	—	4.4	—	рF	V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss		2.6	—	pF	1 = 1.000112
Gate Resistance	Rg	-	900	—	Ω	$f = 1MHz$, $V_{GS} = 0V$, $V_{DS} = 0V$
Total Gate Charge	Qg		0.4	—	nC	
Gate-Source Charge	Qgs	—	0.2	—	nC	Vgs = 4.5V, Vps = 10V, Ip = 250mA
Gate-Drain Charge	Q _{gd}	_	0.1	—	nC	ID = 250MA
Turn-On Delay Time	tD(ON)	—	3.7	—	ns	
Turn-On Rise Time	tR	—	3.6		ns	V _{DD} = 30V, V _{GS} = 10V,
Turn-Off Delay Time	tD(OFF)	—	102	—	ns	$R_g = 25\Omega, I_D = 200 mA$
Turn-Off Fall Time	tF	—	22	—	ns	1
Reverse Recovery Time	t _{RR}	_	20	—	ns	I _F =1A, di/dt = 100A/µs
Reverse Recovery Charge	Q _{RR}	—	7.9		nC	IF = 1A, di/dt = 100A/µs

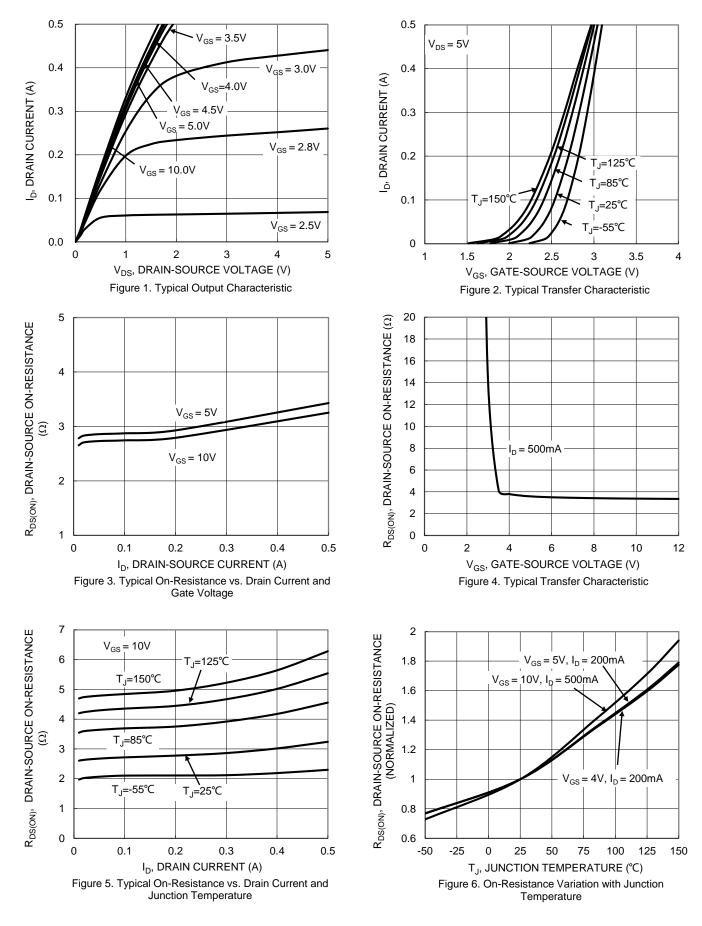
Notes: 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.

Device mounted on 1° x 1° FR-4 PCB with high coverage 2oz. copper, single sided.
 Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.



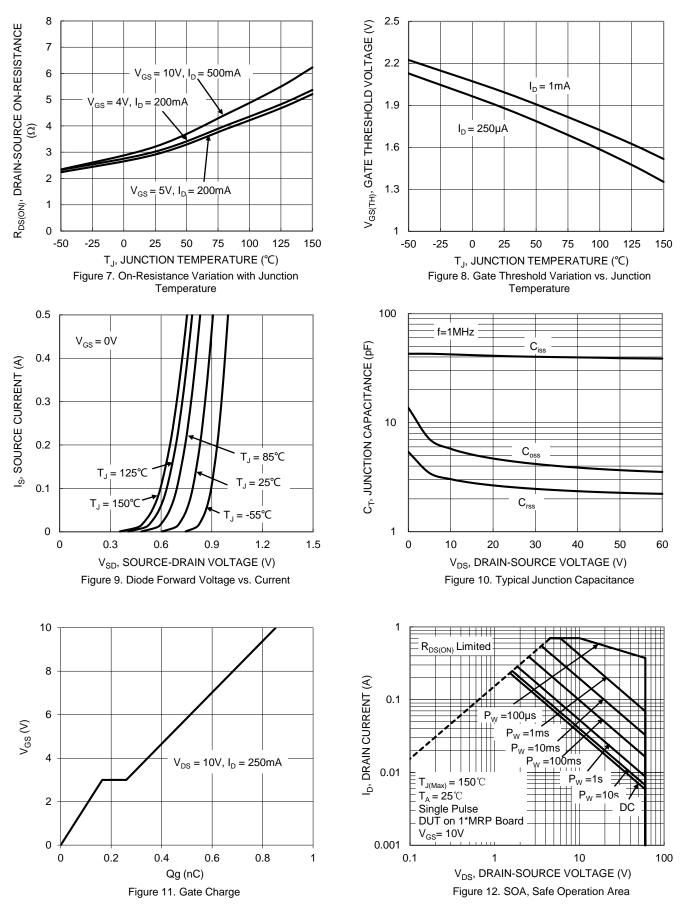
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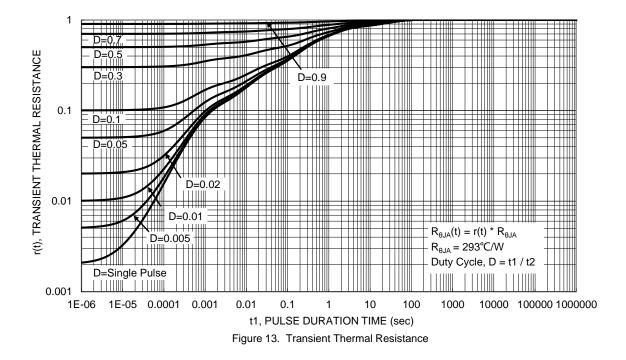


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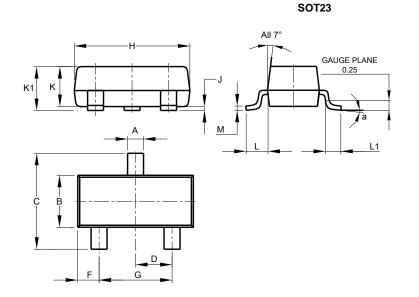






Package Outline Dimensions

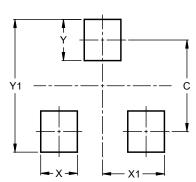
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23 Dim Min Max Тур 0.37 0.51 0.40 А В 1.20 1.40 1.30 С 2.30 2.50 2.40 1.03 D 0.89 0.915 F 0.45 0.60 0.535 G 1.78 2.05 1.83 Н 2.80 3.00 2.90 J 0.05 0.013 0.10 Κ 0.890 1.00 0.975 K1 0.903 1.10 1.025 L 0.45 0.61 0.55 L1 0.25 0.55 0.40 М 0.085 0.150 0.110 а 0° 8° All Dimensions in mm

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23

Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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