

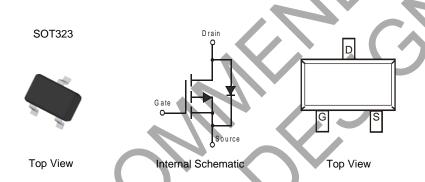
P-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low On-Resistance
- $100m\Omega$ @ $V_{GS} = -4.5V$
- $120m\Omega$ @ $V_{GS} = -2.5V$
- $160 \text{m}\Omega$ @ $V_{GS} = -1.8V$
- Very Low Gate Threshold Voltage V_{GS(TH)} ≤ 1V
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- · Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMP2160UWQ</u>)

Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.006 grams (Approximate)



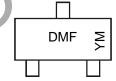
Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
DMP2160UW-7	Standard	SOT323	3000/Tape & Reel

Notes:

- 1. No purposely added Jead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



DMF = Marking Code YM = Date Code Marking Y = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

Date Code Rey												
Year	2018		2019	2020		2021	2022		2023	2024		2025
Code	F		G	Н		I	J		K	L		M
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
											N	_



DMP2160UW

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	-20	V
Gate-Source Voltage		V _{GSS}	±12	V
Drain Current (Note 5)	$T_A = +25$ °C $T_A = +70$ °C	I _D	-1.5 -1.2	А
Pulsed Drain Current		I _{DM}	-10	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_{D}	350	mW
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	360	°C/W
Operating and Storage Temperature Range	T_{J}, T_{STG}	-55 to +150	°C

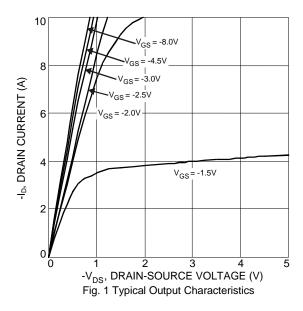
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

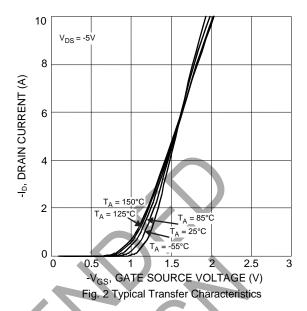
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)			7			
Drain-Source Breakdown Voltage	BV _{DSS}	-20		1	>	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	1	1	-1.0	μA	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}		1	±100 ±800	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$ $V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)		•				
Gate Threshold Voltage	$V_{GS(TH)}$	-0.4	-0.6	-0.9	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$
			75	100		$V_{GS} = -4.5V$, $I_D = -1.5A$
Static Drain-Source On-Resistance	R _{DS(ON)}	1-1	90	120	mΩ	$V_{GS} = -2.5V$, $I_D = -1.2A$
			120	160		$V_{GS} = -1.8V, I_D = -1A$
Forward Transconductance	g FS	+	4	1	S	$V_{DS} = -10V$, $I_D = -1.5A$
Diode Forward Voltage (Note 6)	V _{SD}	_	-	-1.0	V	$V_{GS} = 0V$, $I_{S} = -1.0A$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}	_	627	_	pF	
Output Capacitance	Coss	_	64	_	pF	$V_{DS} = -10V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	53	_	pF	1 - 1.01VII 12

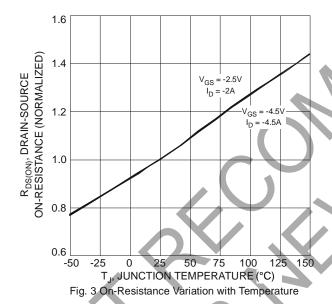
Notes: 5. Device mounted on 1inch² FR-4 PCB with 2 oz. Copper. t ≤ 10 sec. 6. Short duration pulse test used to minimize self-heating effect.

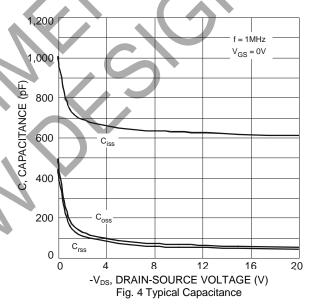


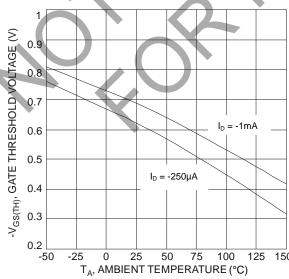
DMP2160UW











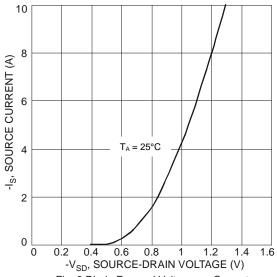


Fig. 5 Gate Threshold Variation vs. Ambient Temperature







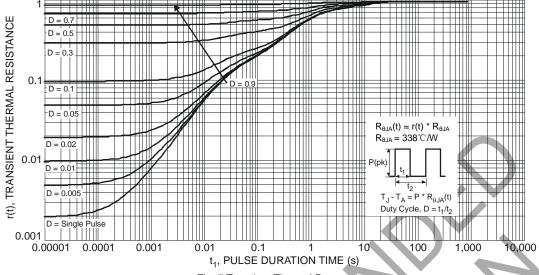


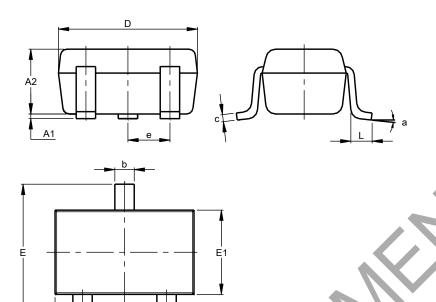
Fig. 7 Transient Thermal Response



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323

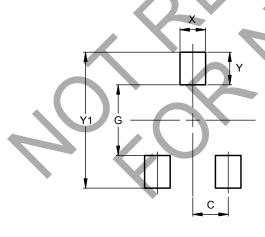


Dim Min Max Typ A1 0.00 0.10 0.05 A2 0.90 1.00 0.95 b 0.25 0.40 0.30 c 0.10 0.18 0.11 D 1.80 2.20 2.15 E 2.00 2.20 2.10 E1 1.15 1.35 1.30 e 0.650 BSC e1 1.20 1.40 1.30 F 0.375 0.475 0.425 L 0.25 0.40 0.30 a 0° 8° All Dimensions in mm	SOT323								
A2 0.90 1.00 0.95 b 0.25 0.40 0.30 c 0.10 0.18 0.11 D 1.80 2.20 2.15 E 2.00 2.20 2.10 E1 1.15 1.35 1.30 e 0.650 BSC e1 1.20 1.40 1.30 F 0.375 0.475 0.425 L 0.25 0.40 0.30 a 0° 8°	Dim	Min	Max	Тур					
b 0.25 0.40 0.30 c 0.10 0.18 0.11 D 1.80 2.20 2.15 E 2.00 2.20 2.10 E1 1.15 1.35 1.30 e 0.650 BSC e1 1.20 1.40 1.30 F 0.375 0.475 0.425 L 0.25 0.40 0.30 a 0° 8°	A1	0.00	0.10	0.05					
c 0.10 0.18 0.11 D 1.80 2.20 2.15 E 2.00 2.20 2.10 E1 1.15 1.35 1.30 e 0.650 BSC e1 1.20 1.40 1.30 F 0.375 0.475 0.425 L 0.25 0.40 0.30 a 0° 8°	A2	0.90	1.00	0.95					
D 1.80 2.20 2.15 E 2.00 2.20 2.10 E1 1.15 1.35 1.30 e 0.650 BSC e1 1.20 1.40 1.30 F 0.375 0.475 0.425 L 0.25 0.40 0.30 a 0° 8°	b	0.25	0.40	0.30					
E 2.00 2.20 2.10 E1 1.15 1.35 1.30 e 0.650 BSC e1 1.20 1.40 1.30 F 0.375 0.475 0.425 L 0.25 0.40 0.30 a 0° 8°	C	0.10	0.18	0.11					
E1 1.15 1.35 1.30 e 0.650 BSC e1 1.20 1.40 1.30 F 0.375 0.475 0.425 L 0.25 0.40 0.30 a 0° 8°	D	1.80	2.20	2.15					
e 0.650 BSC e1 1.20 1.40 1.30 F 0.375 0.475 0.425 L 0.25 0.40 0.30 a 0° 8°	Е	2.00	2.20	2.10					
e1 1.20 1.40 1.30 F 0.375 0.475 0.425 L 0.25 0.40 0.30 a 0° 8°	E1	1.15	1.35	1.30					
F 0.375 0.475 0.425 L 0.25 0.40 0.30 a 0° 8°	е	e 0.650 BSC							
L 0.25 0.40 0.30 a 0° 8°	e1	1.20	1.40	1.30					
a 0° 8°	F	0.375	0.475	0.425					
• •	L	0.25	0.40	0.30					
All Dimensions in mm	а	0°	8°						
	All	Dimen	sions	in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323



Dimensions	Value (in mm)
C	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2.500



DMP2160UW

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