



P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	Ι _D T _C = +25°C
-20V	100mΩ @ V _{GS} = -4.5V	-1.5A

Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

SOT323

Top View

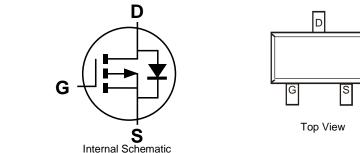
- Engine Management Systems
- DC-DC Converters
- Body Control Electronics

Features and Benefits

- Low On-Resistance: R_{DS(ON)}
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 €3
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)



Ordering Information (Note 5)

Part Number	Case	Packaging
DMP2160UWQ-7	SOT323	3,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

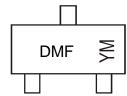
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



 $\begin{array}{l} \mathsf{DMF}=\mathsf{Product Type Marking Code}\\ \mathsf{YM}=\mathsf{Date Code Marking}\\ \mathsf{Y or }\overline{\mathsf{Y}}=\mathsf{Year} \ (\mathsf{ex: E}=2017)\\ \mathsf{M}=\mathsf{Month} \ (\mathsf{ex: 9}=\mathsf{September}) \end{array}$

Date Code Key

Notes:

Year	201	6	2017		2018	20	19	2020		2021	2	2022
Code	D		E		F	(G	Н				J
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	4	•	0	4	-	0	7	0	0	0	NI	



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Drain-Source Voltage		V _{DSS}	-20	V
Gate-Source Voltage		V _{GSS}	±10	V
Drain Current (Note 6) Steady State	T _A = +25°C T _A = +70°C	۱ _D	-1.5 -1.2	A
Pulsed Drain Current	•	IDM	-10	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 6)	PD	350	mW
Thermal Resistance, Junction to Ambient	R _{θJA}	360	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

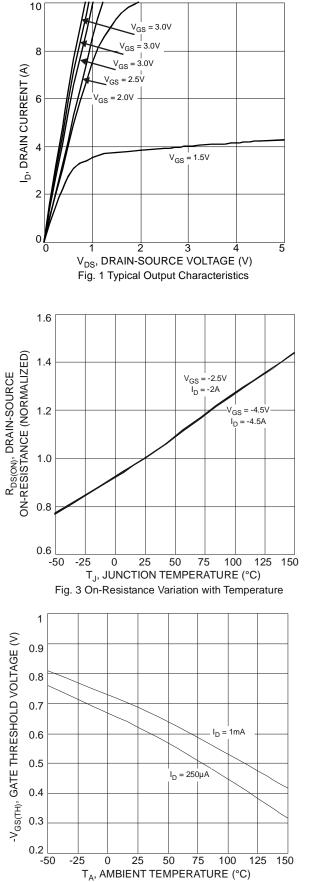
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)			• • • •	•		÷
Drain-Source Breakdown Voltage	BV _{DSS}	-20	—	—	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	IDSS	—	—	1	μA	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±100 ±800	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$ $V_{GS} = \pm 10V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	-0.4	-0.6	-0.9	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance	R _{DS(ON)}	_	75 90	100 120	mΩ	$V_{GS} = -4.5V, I_D = -1.5A$ $V_{GS} = -2.5V, I_D = -1.2A$
Forward Transconductance	g fs	_	4	_	S	V _{DS} =-10V, I _D = -1.5A
Diode Forward Voltage (Note 8)	V _{SD}			-1.0	V	$V_{GS} = 0V, I_{S} = -1.0A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	CISS	_	627	—	pF	
Output Capacitance	C _{OSS}	_	64	—	pF	$V_{DS} = -10V, V_{GS} = 0V$ = f = 1.0MHz
Reverse Transfer Capacitance	C _{RSS}	_	53	_	pF	

Notes:

6.Device mounted on $1in^2$ FR-4 PCB with 2 oz. Copper. t \leq 10 sec. 7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to production testing.



DMP2160UWQ



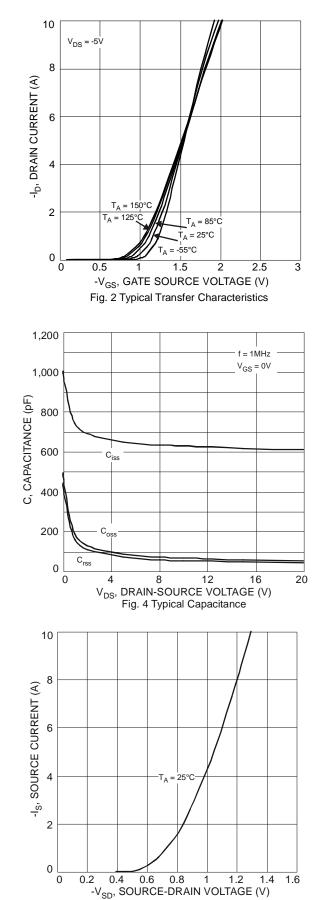
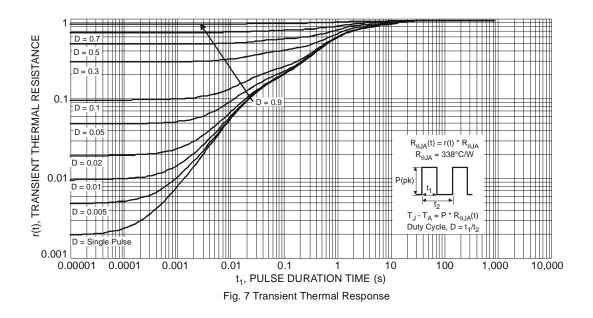


Fig. 5 Gate Threshold Variation vs. Ambient Temperature

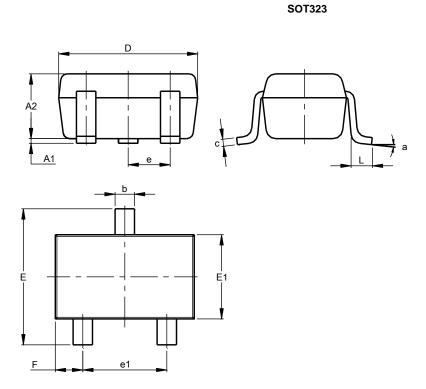






Package Outline Dimensions

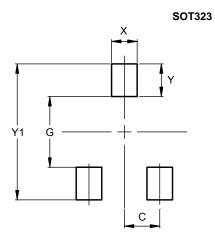
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT323							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.25	0.40	0.30				
c	0.10	0.18	0.11				
D	1.80	2.20	2.15				
ш	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
e	0.650 BSC						
e1	1.20	1.40	1.30				
F	0.375	0.475	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All	Dimen	sions i	in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2.500



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