





P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Rds(on)	ID TA = +25°C
	700mΩ @ V _{GS} = -4.5V	-0.5A
-20V	900mΩ @ V _{GS} = -2.5V	-0.48A
	1300mΩ @ V _{GS} = -1.8V	-0.4A

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Load Switch
- Power Management Functions

Mechanical Data

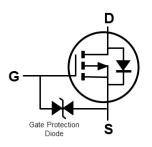
- Case: SOT523
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.002 grams (Approximate)



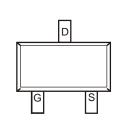


SOT523

Top View



Equivalent Circuit



Top View

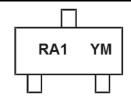
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2900UT-7	SOT523	3000/Tape & Reel
DMP2900UT-13	SOT523	10000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine <900ppm chlorine (<1500ppm total Br + Cl) and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



RA1 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Kev

Date Code Key												
Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	G	Н		J	K	L	М	N	0	Р	R	S
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Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

June 2020

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Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		VDSS	-20	V	
Gate-Source Voltage		Vgss	±6	V	
Continuous Drain Current (Note 6) V _{GS} = -4.5V	I _D	-0.5 -0.4	А		
Maximum Continuous Body Diode Forward Currer	nt (Note 6)	ls	-0.39	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1	%)		I _{DM}	-2.5	Α

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	Steady State	PD	0.25	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RθJA	496	°C/W
Total Power Dissipation (Note 6)	Steady State	PD	0.32	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	RθJA	395	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

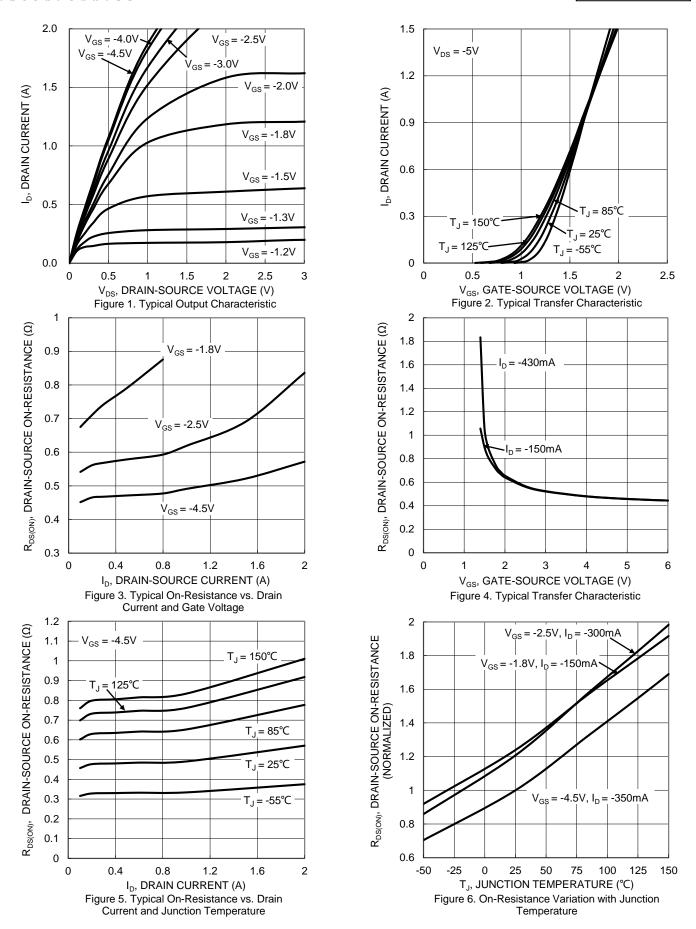
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	-100	nA	$V_{DS} = -20V, V_{GS} = 0V$	
Gate-Source Leakage		_	_	±2.0	μΑ	$V_{GS} = \pm 4.5V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(th)	-0.5	_	-1.0	٧	$V_{DS} = V_{GS}$, $I_D = -250\mu A$	
		_	0.4	0.7		$V_{GS} = -4.5V$, $I_{D} = -430mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	0.5	0.9	Ω	$V_{GS} = -2.5V, I_D = -300mA$	
		_	0.7	1.3		$V_{GS} = -1.8V, I_{D} = -150mA$	
Diode Forward Voltage (Note 7)	VsD	_	-0.7	-1.2	V	V _G S = 0V, I _S = -150mA	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	49	_	pF	101/1/	
Output Capacitance	Coss	_	12	-	рF	$V_{DS} = -16V, V_{GS} = 0V,$ - $f = 1.0MHz$	
Reverse Transfer Capacitance	Crss	_	3.4	_	pF	1 = 1:01/11/12	
Total Gate Charge	Qg	_	0.7	_	nC	\\ 45\\\\ 10\\\	
Gate-Source Charge	Qgs	_	0.1	-	nC	$V_{GS} = -4.5V, V_{DS} = -10V,$ $V_{DS} = -250 \text{mA}$	
Gate-Drain Charge	Qgd	_	0.1	_	nC	1D = -230IIIA	
Turn-On Delay Time	tD(ON)	_	5.3	_	ns	101/1/	
Turn-On Rise Time	tR	_	2.8	_	ns	V _{DD} = -10V, V _{GS} = -4.5V,	
Turn-Off Delay Time	t _{D(OFF)}	_	1247	_	ns	$R_L = 47\Omega$, $R_G = 10\Omega$, $R_D = -200$ mA	
Turn-Off Fall Time	tF	_	445	_	ns	TD = -200111A	
Reverse Recovery Time	trr		10.5	_	ns	I 1Adi/dt = 100A/us	
Reverse Recovery Charge	Q _{RR}	_	1.8	_	nC	$I_F = -1A$, di/dt = 100A/ μ s	

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect. Notes:

^{8.} Guaranteed by design. Not subject to production testing.







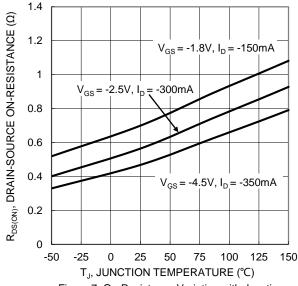


Figure 7. On-Resistance Variation with Junction Temperature

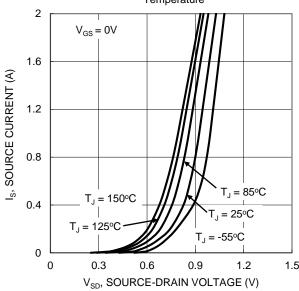
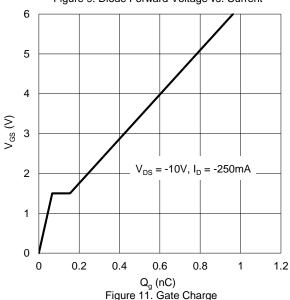


Figure 9. Diode Forward Voltage vs. Current



1.2 $V_{\text{GS(TH)}}$, GATE THRESHOLD VOLTAGE (V) 1 $I_D = -1mA$ 0.8 0.6 $I_{D} = -250 \mu A$ 0.4 0.2 -50 25 50 75 100 125 T_J, JUNCTION TEMPERATURE (°C)

Figure 8. Gate Threshold Variation vs. Junction Temperature

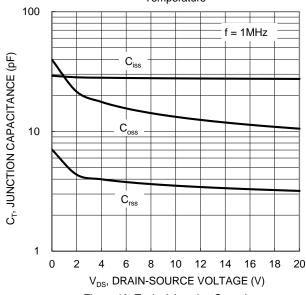
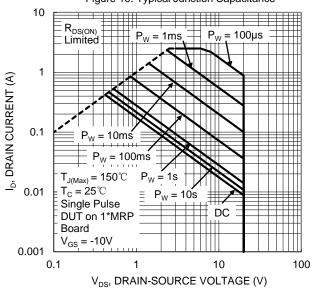


Figure 10. Typical Junction Capacitance



V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



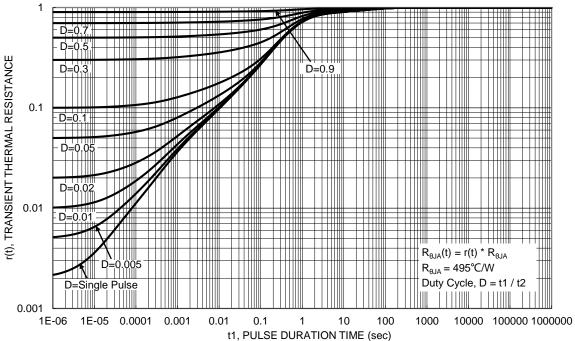


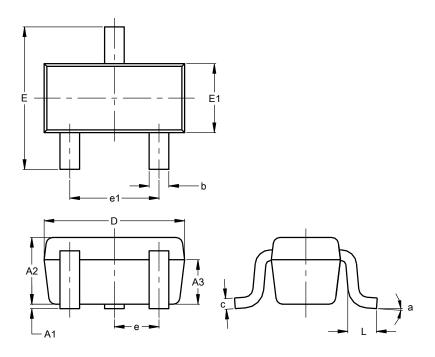
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT523

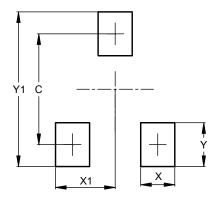


SOT523						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.60	0.80	0.75			
A3	0.45	0.65	0.50			
b	0.15	0.30	0.22			
С	0.10	0.20	0.12			
D	1.50	1.70	1.60			
Е	1.45	1.75	1.60			
E1	0.75	0.85	0.80			
е	0.50 BSC					
e1	0.90	1.10	1.00			
L	0.20	0.40	0.33			
а	0°		8°			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT523



Dimensions	Value (in mm)		
C	1.29		
Х	0.40		
X1	0.70		
Υ	0.51		
V1	1.90		



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