



DMP510DL

Product Summary

BV _{DSS}	RDS(ON) Max	I _D TA = +25°C
-50V	10Ω @ V _{GS} = -5V	-180mA

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDs(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch

P-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

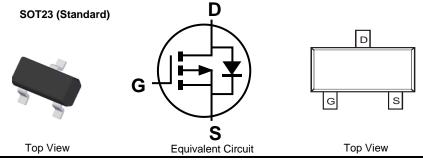
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
 - https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: SOT23
- Case Material: UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish (Lead Free Plating). Solderable per MIL-STD-202, Method 208(3)
- Terminal Connections: See Diagram
- Weight: 0.008 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMP510DL-7	SOT23 (Standard)	3,000/Tape & Reel
DMP510DL-13	SOT23 (Standard)	10,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

Notes:

	51D	MY	
I			
	2021	2022	

51D = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: I = 2021) M or \overline{M} = Month (ex: 9 = September)

Date Code Key												
Year	2015		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	С		I	J	K	L	М	N	0	Р	R	S
Manuth	Le.	F - 1	Maria	A			l. I	A	Sam	Oct	Nev	Dee
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		VDSS	-50	V	
Gate-Source Voltage	V _{GSS}	±30	V		
Continuous Drain Current (Note 6) V _{GS} = -5V	Steady State	T _A = +25°C T _A = +70°C	ID	-180 -130	mA
Maximum Continuous Body Diode Forward Currer	nt (Note 6)	ls	-180	mA	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1	%)	IDM	-1.2	А	

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	310	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	405	°C/W
Total Power Dissipation (Note 6)		PD	500	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	251	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

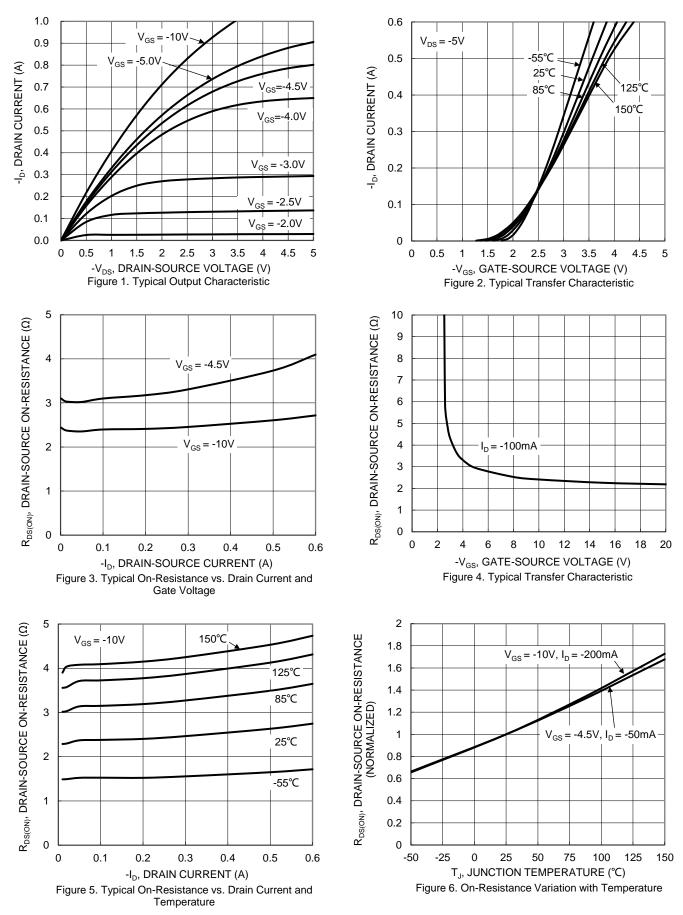
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						·
Drain-Source Breakdown Voltage	BVDSS	-50	_		V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current	IDSS	—	_	-1	μA	$V_{DS} = -50V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-0.8	_	-2.0	V	$V_{DS} = V_{GS}, I_D = -1mA$
Static Drain-Source On-Resistance	RDS(ON)	_	_	10	Ω	V _{GS} = -5V, I _D = -0.1A
Forward Transconductance	g FS	_	0.25		S	V _{DS} = -25V, I _D = -0.1A
DYNAMIC CHARACTERISTICS (Note 8)						·
Input Capacitance	Ciss	—	24.6	—	pF	
Output Capacitance	Coss	_	4.8		pF	V _{DS} = -25V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	2.8	_	pF	7
Turn-On Delay Time	tD(ON)	_	2.8		ns	
Turn-On Rise Time	tR		2.6		ns	$V_{DD} = -30V, I_D = -0.27A,$
Turn-Off Delay Time	tD(OFF)		11.1		ns	$R_{GEN} = 50\Omega$, $V_{GS} = -10V$
Turn-Off Fall Time	tF		7.2		ns	

Notes:

Device mounted on FR-4 PCB, with minimum recommended pad layout.
Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.



DMP510DL





DMP510DL

100

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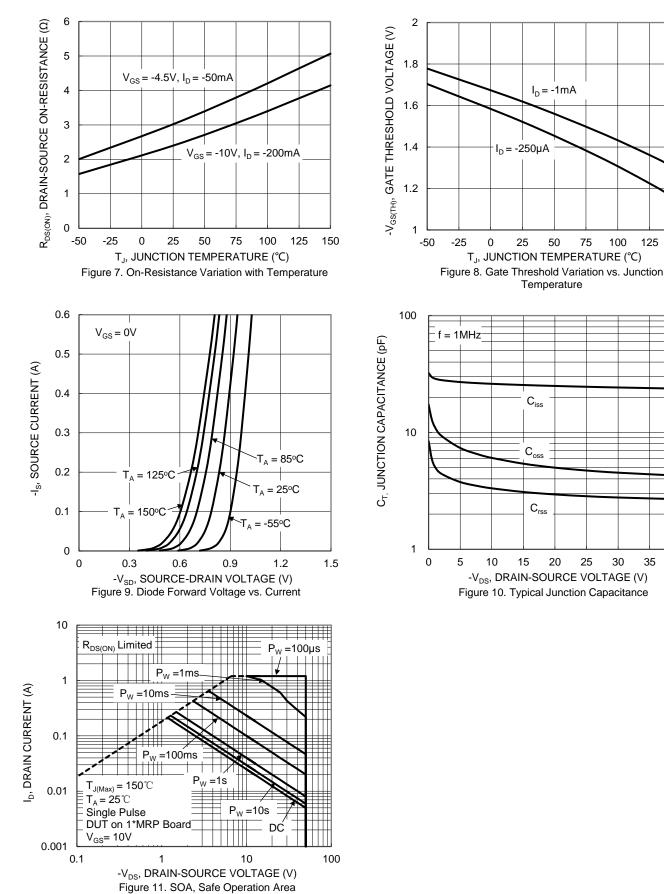
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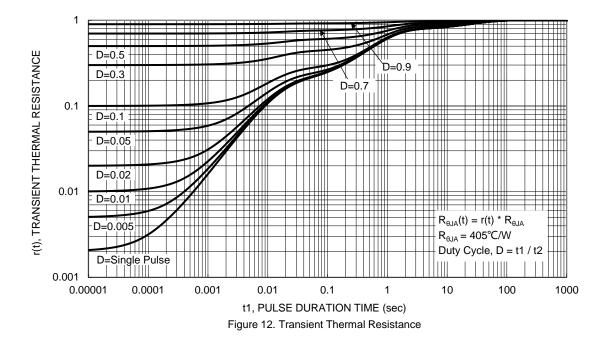
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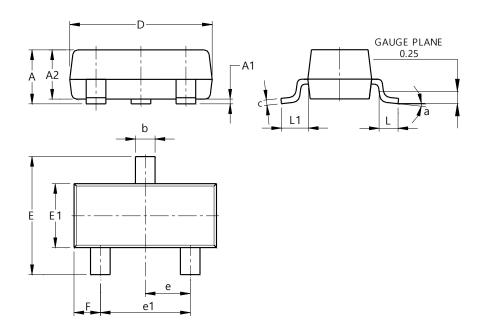






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

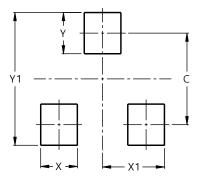


S	SOT23 (Standard)								
Dim	Min	Max	Тур						
Α	0.90	1.15	1.025						
A1	0.00	0.10	0.05						
A2	0.85	1.10	0.975						
b	0.30	0.51	0.40						
С	0.080	0.202	0.11						
D	2.80	3.00	2.90						
E	2.25	2.55	2.40						
E1	1.20	1.40	1.30						
е	0.89	1.03	0.915						
e1	1.78	2.05	1.83						
F	0.40	0.60	0.535						
L1	0.45	0.61	0.55						
L	0.25	0.55	0.40						
а	0°	8°							
All	All Dimensions in mm								

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23 (Standard)



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9

SOT23 (Standard)



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