



DMP510DLW

### **Product Summary**

BV <sub>DSS</sub>	Rds(on) Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
-50V	10.0Ω @ V <sub>GS</sub> = -5V	-174mA

# **Description and Applications**

This MOSFET has been designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Control
- Power Management Functions
- Backlighting

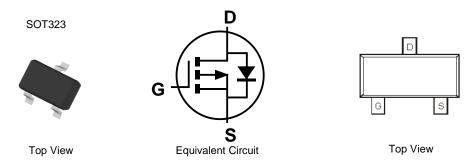
#### **50V P-CHANNEL ENHANCEMENT MODE MOSFET**

### **Features and Benefits**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

### **Mechanical Data**

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42
  Leadframe. Solderable per MIL-STD-202, Method 208 3
- Weight: 0.006 grams (Approximate)



#### Ordering Information (Note 4)

Part Number	Case	Packaging
DMP510DLW-7	SOT323	3000/Tape & Reel
DMP510DLW-13	SOT323	10000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# **Marking Information**

51D	۸M

51D= Product Type Marking Code  $\overline{Y}M = Date Code Marking$ Y or  $\overline{Y} = Year (ex: H = 2020)$ M = Month (ex: 9 = September)

#### Date Code Key

	-											
Year	2017		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	E		Н		J	K	L	М	N	0	Р	R
					1	1	1			_		
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage			V <sub>DSS</sub>	-50	V
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current (Note 6) $V_{GS} = -5V$ State State T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C			lD	-174 -139	mA
Maximum Continuous Body Diode Forward Curre	ls	-65	mA		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1	%) (Note 6	<b>)</b>	ldм	-1.0	А

# Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	320	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	398	°C/W
Total Power Dissipation (Note 6)		PD	470	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>0JA</sub>	273	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						·
Drain-Source Breakdown Voltage	BVDSS	-50		—	V	Vgs = 0V, Id = -250µA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	-1.0	μA	$V_{DS} = -50V, V_{GS} = 0V$
Gate-Source Leakage	lgss	—	_	±100	nA	$V_{GS} = \pm 16V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.8		-2.0	V	$V_{DS} = V_{GS}, I_D = -1mA$
Static Drain-Source On-Resistance	RDS(ON)	—		10	Ω	VGS = -5V, ID = -0.1A
Diode Forward Voltage	Vsd	—	-0.78	-1.5	V	VGS = 0V, IS = -100mA
DYNAMIC CHARACTERISTICS (Note 8)						·
Input Capacitance	Ciss	-	24.6	—	pF	
Output Capacitance	Coss	—	4.8	—	pF	V <sub>DS</sub> = -25V, V <sub>GS</sub> = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss	-	2.8	—	pF	
Gate Resistance	Rg	—	2000	—	Ω	$f = 1.0MHz$ , $V_{GS} = 0V$ , $V_{DS} = 0V$
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qg	—	280	—	рС	
Total Gate Charge (V <sub>GS</sub> = -10V)	Qg	—	560	—	рС	Vgs = -4.5V, Vds = -10V,
Gate-Source Charge	Qgs	—	90	—	рС	I <sub>D</sub> = -100mA
Gate-Drain Charge	Q <sub>gd</sub>	—	77	—	рС	
Turn-On Delay Time	t <sub>D(ON)</sub>	—	2.8	—	ns	
Turn-On Rise Time	tR	—	2.6	—	ns	V <sub>DD</sub> = -30V, I <sub>D</sub> = -0.27A,
Turn-Off Delay Time	tD(OFF)	—	11.1	—	ns	$R_{GEN} = 50\Omega$ , $V_{GS} = -10V$
Turn-Off Fall Time	tF	—	7.2	—	ns	1

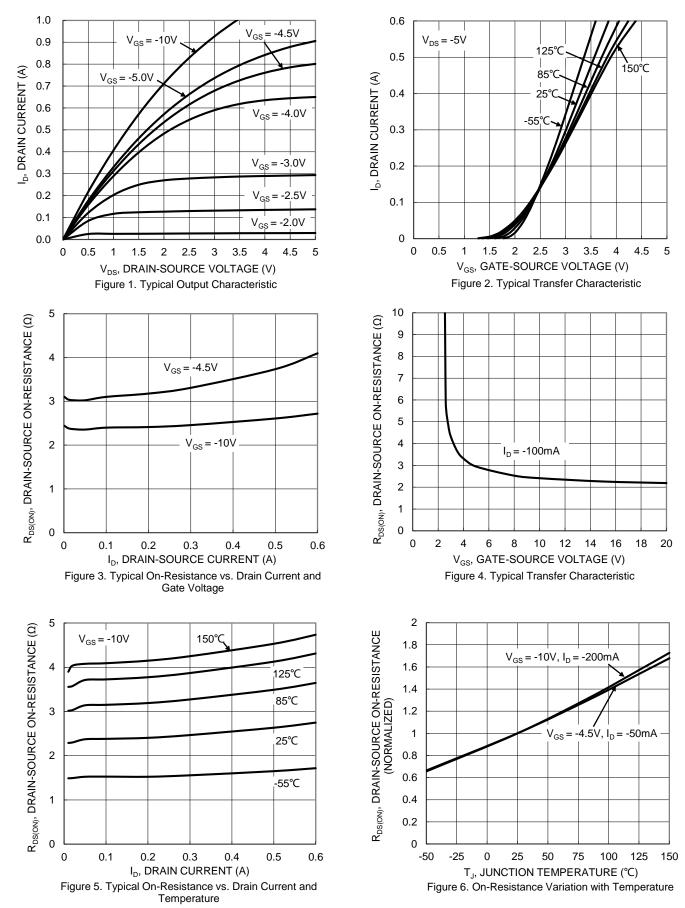
5. Device mounted on FR-4 PCB, with minimum recommended pad layout. Notes:

Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
 Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.



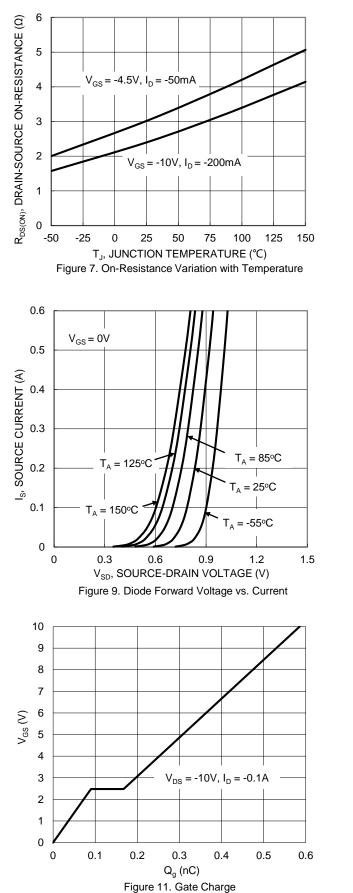
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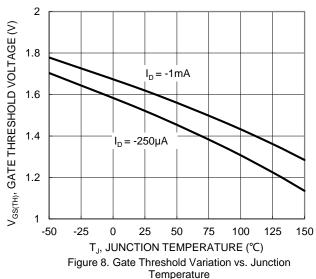


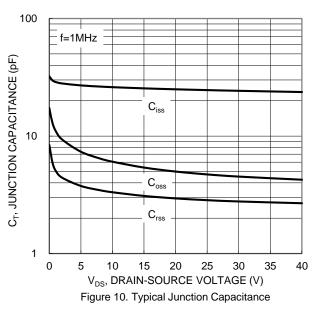
DMP510DLW Document number: DS40228 Rev. 2 - 2



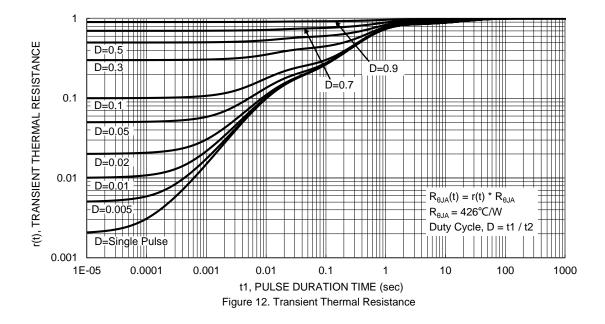
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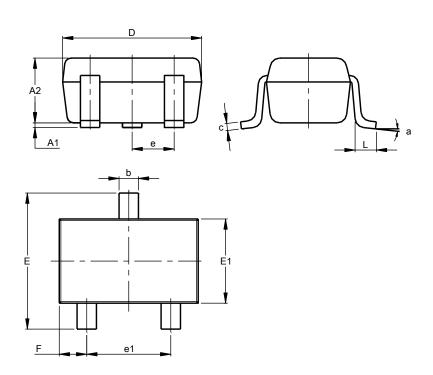






# **Package Outline Dimensions**

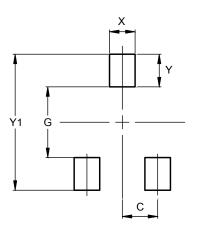
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT323								
Dim	Min	Max	Тур					
A1	0.00	0.10	0.05					
A2	0.90	1.00	0.95					
b	0.25	0.40	0.30					
c	0.10	0.18	0.11					
D	1.80	2.20	2.15					
Е	2.00	2.20	2.10					
E1	1.15	1.35	1.30					
е	C	).650 B	SC					
e1	1.20	1.40	1.30					
F	0.375	0.475	0.425					
L	0.25	0.40	0.30					
а	0°	8°						
All	Dimen	sions i	in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.470
Ŷ	0.600
Y1	2.500

SOT323

SOT323



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