

#### **60V P-CHANNEL ENHANCEMENT MODE MOSFET**

## **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
-60V	$28m\Omega @ V_{GS} = -10V$	-7A
	35mO @ Vcs = -4.5V	-6.2A

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Backlighting
- Power Management Functions
- DC-DC Converters

## **Features and Benefits**

- Low On-Resistance
- · Fast Switching Speed
- Low Threshold
- Low Gate Drive
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

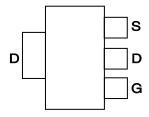
#### Mechanical Data

- Case: SOT223
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
  Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.112 grams (Approximate)

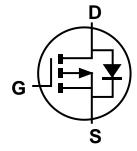




Top View



Pin Out - Top View



**Equivalent Circuit** 

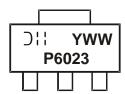
### **Ordering Information (Note 5)**

ĺ	Part Number	Case	Packaging
	DMP6023LEQ-13	SOT223	2,500/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product\_compliance\_definitions.html.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# **Marking Information**



Oll = Manufacturer's Marking P6023 = Marking Code YWW = Date Code Marking Y or Y = Year (ex: 7 = 2017) WW = Week (01 to 53)



# **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	$V_{DSS}$	-60	V	
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note EV)	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	-7 -5.6	А
Continuous Drain Current (Note 5) V <sub>GS</sub> = -10V	$T_C = +25$ °C $T_C = +70$ °C	I <sub>D</sub>	-18.2 -14.5	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	-50	А	
Maximum Continuous Body Diode Forward Current (Note 5)	Is	-2	А	
Avalanche Current, L = 0.1mH	I <sub>AS</sub>	-35.5	Α	
Avalanche Energy, L = 0.1mH	E <sub>AS</sub>	62.9	mJ	

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25$ °C $T_A = +70$ °C	P <sub>D</sub>	2 1.3	W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{ heta JA}$	60	°C/W
Total Power Dissipation (Note 5)	T <sub>C</sub> = +25°C	P <sub>D</sub>	17.3	W
Thermal Resistance, Junction to Case (Note 5)		$R_{\theta JC}$	7.2	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-60	-	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	_	-1	μA	V <sub>DS</sub> = -60V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	$V_{GS(TH)}$	-1	_	-3	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance	Dagger	_	1	28	mΩ	$V_{GS} = -10V, I_D = -5A$	
Static Dialit-Source Off-Resistance	R <sub>DS(ON)</sub>	_	-	35		$V_{GS} = -4.5V$ , $I_D = -4A$	
Diode Forward Voltage	$V_{SD}$	_	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C <sub>iss</sub>	_	2569	_	pF	)/ 20\/ )/ 0\/	
Output Capacitance	Coss	-	179	_	pF	$V_{DS} = -30V, V_{GS} = 0V,$ - f = 1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	-	143	_	pF	1 = 11VIDZ	
Gate Resistance	$R_g$		8	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = -4.5V)	$Q_g$	_	26.5	_	nC		
Total Gate Charge (V <sub>GS</sub> = -10V)	$Q_g$	-	53.1	_	nC	V <sub>DS</sub> = -30V. I <sub>D</sub> = -5A	
Gate-Source Charge	Q <sub>gs</sub>	_	7.1	_	nC	$V_{DS} = -30V, I_{D} = -5A$	
Gate-Drain Charge	$Q_{gd}$	_	12.6	_	nC		
Turn-On Delay Time	t <sub>D(ON)</sub>	_	6	_	ns		
Turn-On Rise Time	t <sub>R</sub>	_	7.1	_	ns	$V_{GS} = -10V, V_{DS} = -30V,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	110	_	ns	$R_g = 3\Omega$ , $I_D = -5A$	
Turn-Off Fall Time	t <sub>F</sub>	_	62	_	ns	7	
Body Diode Reverse Recovery Time	t <sub>RR</sub>	_	20	_	ns	L - 50 di/dt - 1000/up	
Body Diode Reverse Recovery Charge	$Q_{RR}$	_	14	_	nC	$I_F = -5A$ , di/dt = 100A/ $\mu$ s	

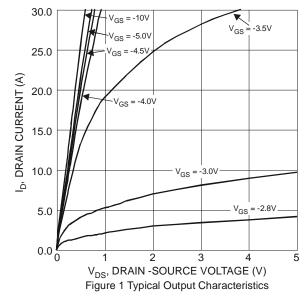
Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square pad layout.

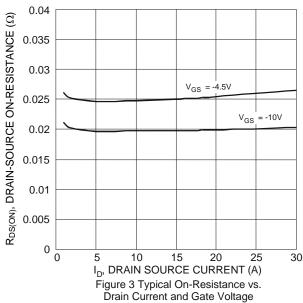
<sup>6.</sup> Short duration pulse test used to minimize self-heating effect.

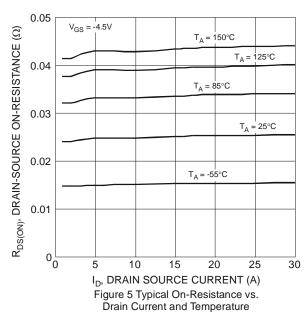
<sup>7.</sup> Guaranteed by design. Not subject to product testing.

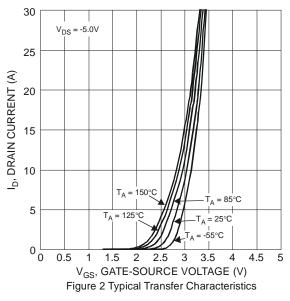


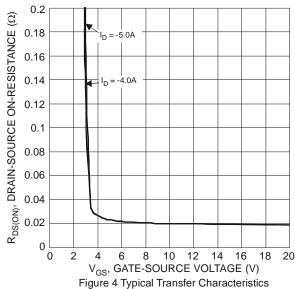


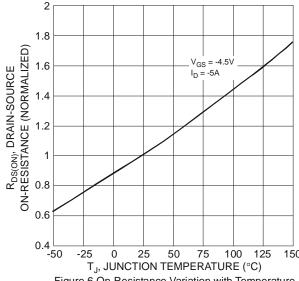




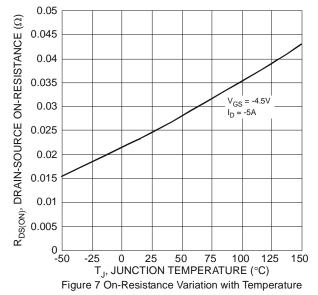


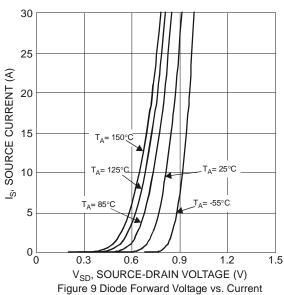


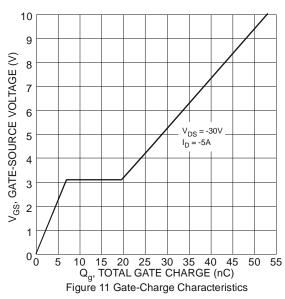












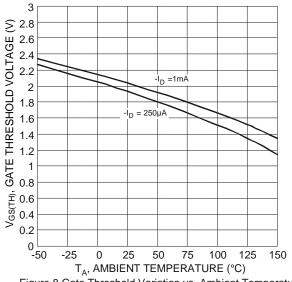
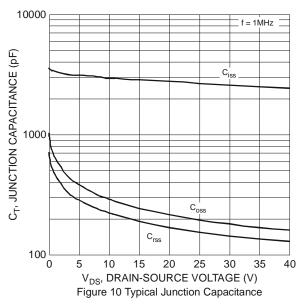
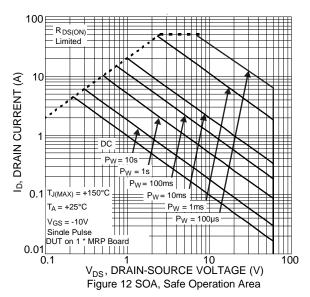


Figure 8 Gate Threshold Variation vs. Ambient Temperature

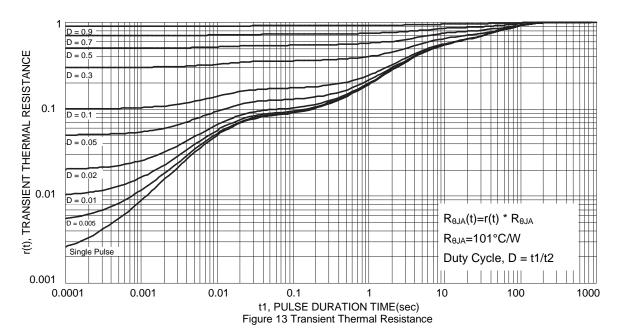




July 2017

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# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

# Gauge Plane 0.25 Seating Plane L

	e1 -	e ——	b b
A	— A1		>-
<u> </u>			
f	l		

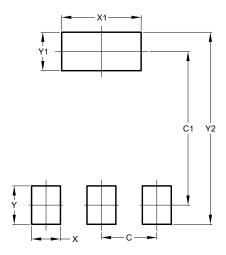
## SOT223

SOT223				
Dim	Min	Max	Тур	
Α	1.55	1.65	1.60	
<b>A</b> 1	0.010	0.15	0.05	
b	0.60	0.80	0.70	
b1	2.90	3.10	3.00	
C	0.20	0.30	0.25	
D	6.45	6.55	6.50	
Е	3.45	3.55	3.50	
E1	6.90	7.10	7.00	
е	-	-	4.60	
e1	-	-	2.30	
L	0.85	1.05	0.95	
q	0.84	0.94	0.89	
All Dimensions in mm				

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT223



Dimensions	Value (in mm)
С	2.30
C1	6.40
Х	1.20
X1	3.30
Υ	1.60
Y1	1.60
Y2	8 00



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