



65V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Rds(on) Max	I _{D Max} T _A = +25°С
-65V	8Ω @ V _{GS} = -5V	-318mA
	18Ω @ V _{GS} = -2.5V	-212mA

Features and Benefits

- Low On-Resistance
- Low Input Capacitance

Mechanical Data

208 **e4**)

Case: X1-DFN1006-3

- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

Case Material: Molded Plastic, "Green" Molding Compound;

Terminals: Finish - NiPdAu. Solderable per MIL-STD-202, Method

UL Flammability Classification Rating 94V-0

Moisture Sensitivity: Level 1 per J-STD-020

Weight: 0.001 grams (Approximate)

Description and Applications

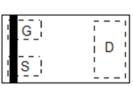
This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

X1-DFN1006-3



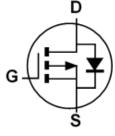
Bottom View



Top View

Pin-Out

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Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP68D0LFB-7B	X1-DFN1006-3	10,000 / Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

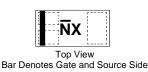
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

Notes:



X1-DFN1006-3

NX= Part Marking Code

DMP68D0LFB Document number: DS42866 Rev. 1 - 2



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	VDSS	-65	V		
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 5) V_{GS} = -5V	Steady State	T _A = +25°C T _A = +70°C	٥l	-192 -153	mA
Continuous Drain Current (Note 6) V _{GS} = -5V	Steady State	T _A = +25°C T _A = +70°C	١D	-318 -254	mA
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			IDM	-800	mA
Maximum Body Diode Continuous Current (Note 6)			ls	-318	mA

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 5)	PD	0.5	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 5)	Reja	251	°C/W
Power Dissipation (Note 6)	PD	1.21	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 6)	R _{0JA}	103	°C/W
Operating and Storage Temperature Range	Tj, Tstg	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	-65	—		V	$V_{GS} = 0V, I_D = -250 \mu A$
Zero Gate Voltage Drain Current	IDSS	—	—	-1.0	μA	$V_{DS} = -65V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-0.8		-2.1	V	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$
Static Drain-Source On-Resistance	Descent		2.0	8	Ω	$V_{GS} = -5V, I_{D} = -100 mA$
Static Drain-Source On-Resistance	RDS(ON)	—	4.7	18	Ω	V _{GS} = -2.5V, I _D = -10mA
Diode Forward Voltage	Vsd		-0.8	-1.5	V	V _{GS} = 0V, I _S = -100mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss		36			
Output Capacitance	Coss		3.9		pF	V _{DS} = -30V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss	—	2.1	—		
Gate Resistance	Rg	_	247	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge	Qg	—	4.1	—		
Gate-Source Charge	Qgs	_	1.3	_	nC	V _{GS} = -5V, V _{DS} = -30V, I _D = -100mA
Gate-Drain Charge	Q _{gd}	—	1.3	_		
Turn-On Delay Time	t _{D(ON)}	—	12.2	_		
Turn-On Rise Time	tR		10.6			VGS = -5V, VDS = -30V,
Turn-Off Delay Time	tD(OFF)		33.2		ns	$R_{G} = 50\Omega, I_{D} = -100 \text{mA}$
Turn-Off Fall Time	tF		18.9		1	

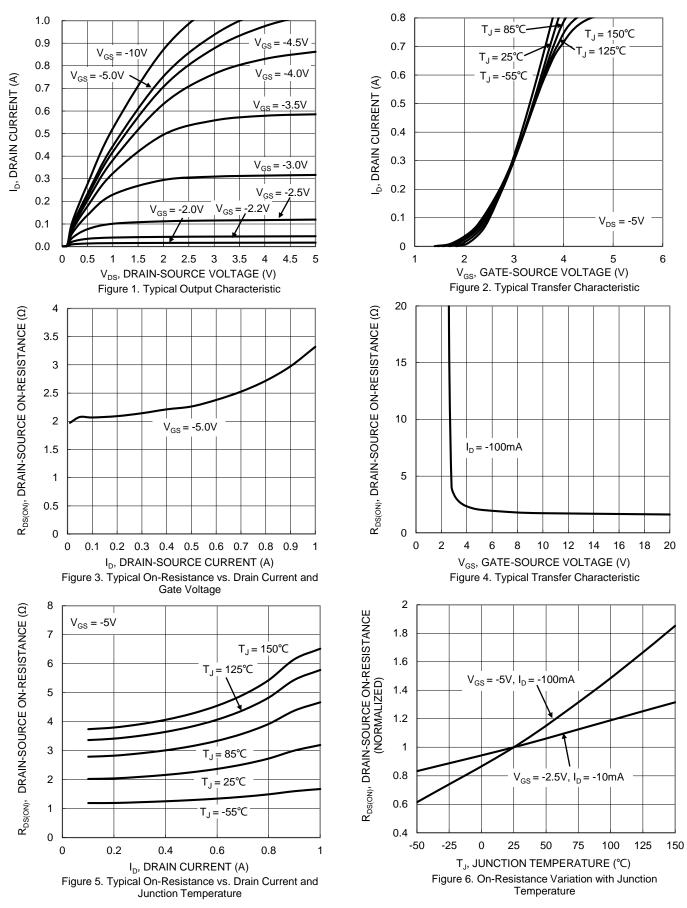
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. Notes:

Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.

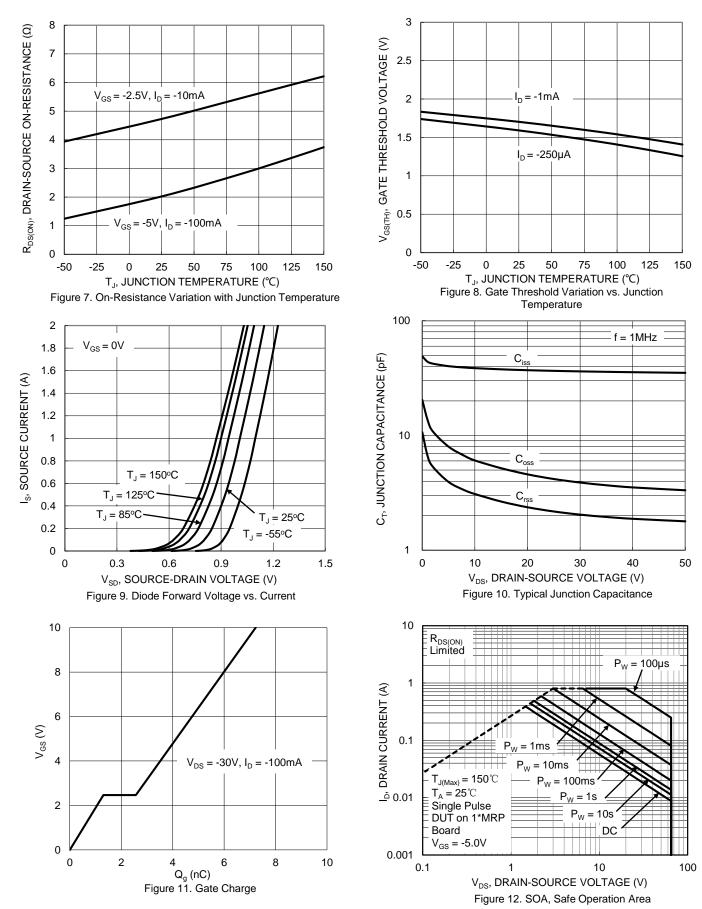
8. Guaranteed by design. Not subject to production testing.



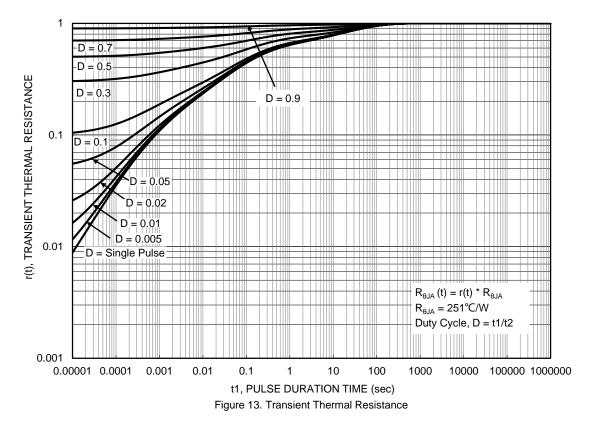
DMP68D0LFB









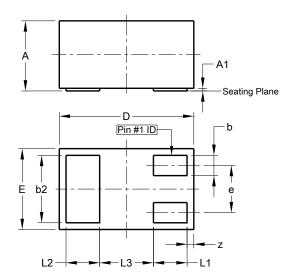




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



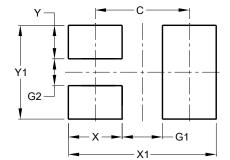


X1-DFN1006-3					
Dim	Min	Max	Тур		
Α	0.47	0.53	0.50		
A1	0.00	0.05	0.03		
b	0.10	0.20	0.15		
b2	0.45	0.55	0.50		
D	0.95	1.075	1.00		
Е	0.55	0.675	0.60		
e	1	-	0.35		
L1	0.20	0.30	0.25		
L2	0.20	0.30	0.25		
L3	-	-	0.40		
z	0.02	0.08	0.05		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1006-3



Dimensions	Value (in mm)
С	0.70
G1	0.30
G2	0.20
Х	0.40
X1	1.10
Y	0.25
Y1	0.70



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