



N-CHANNEL ENHANCEMENT MODE MOSFET WITH SCHOTTKY DIODE

Features

- DIOFET Utilizes a Unique Patented Process to Monolithically Integrate a MOSFET and a Schottky in a Single Die to Deliver:
 - Low R_{DS(ON)} Minimizes Conduction Losses
 - Low V_{SD} Reducing the Losses Due to Body Diode Conduction
 - Low Q_{rr} Lower Q_{rr} of the Integrated Schottky Reduces Body Diode Switching Losses
 - Low Gate Capacitance (Q_g/Q_{gs}) Ratio Reduces Risk of Shoot-Through or Cross Conduction Currents at High Frequencies
 - Avalanche Rugged I_{AR} and E_{AR} Rated
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below

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Top View

Internal Schematic

• Weight: 0.072 grams (Approximate)

Top View

Ordering Information (Note 4)

Part Number	Case	Packaging
DMS3016SSS-13	SO-8	2500 / Tape & Reel

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1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

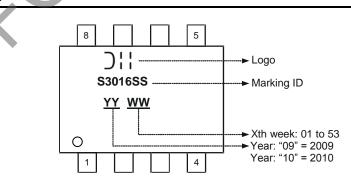
and Lead-free

Notes:

3. Halogen and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information





Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 5) V_{GS} = 4.5V	Steady State	T _A = +25°C T _A = +85°C	ID	9.8 6.3	A
Pulsed Drain Current (Note 6)			I _{DM}	90	A
Avalanche Current (Note 6) (Note 7)			I _{AR}	13	A
Repetitive Avalanche Energy (Note 6) (Note 7) L = 0.3mH			E _{AR}	25.4	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	1.54	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 5)	R _{θJA}	81	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	٥°

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

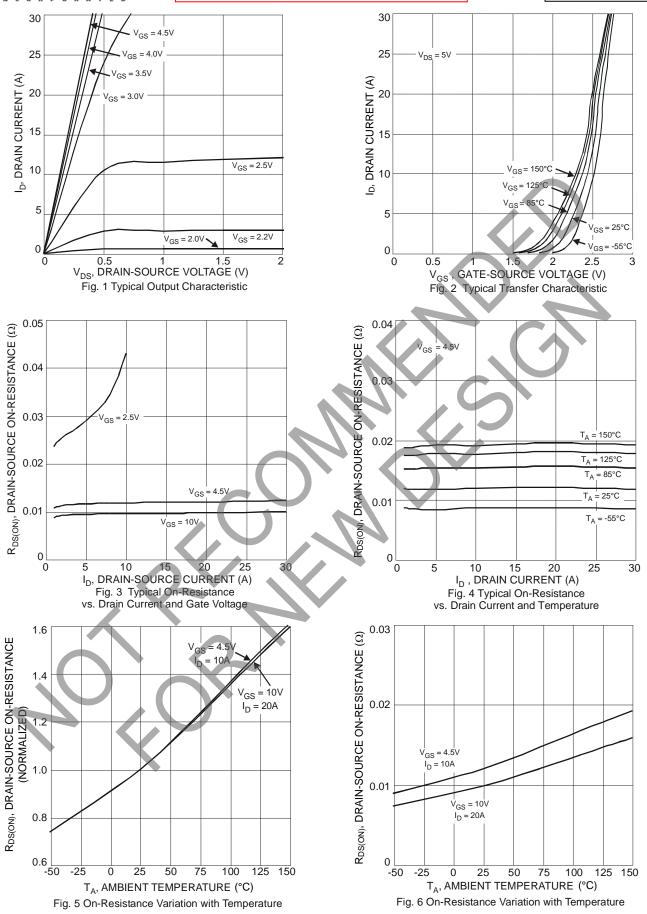
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	-		0.1	mA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS	-		±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	1.0	2	2.3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance			9	13	mΩ	V _{GS} = 10V, I _D = 9.8A	
	R _{DS(ON)}	-	11	16		V _{GS} = 4.5V, I _D = 9.8A	
Forward Transfer Admittance	Y _{fs}		5	-	S	$V_{DS} = 5V, I_{D} = 9.8A$	
Diode Forward Voltage	Vsd	-	0.4	1	V	$V_{GS} = 0V, I_{S} = 1A$	
Maximum Body-Diode + Schottky Continuous Current	ls	-	-	5	А	-	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss		1849	-	pF		
Output Capacitance	Coss	ł	158	-	pF	V _{DS} =15V, V _{GS} = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}		123	-	pF	1 - 1.00012	
Gate Resistance	Rg	0.53	2.68	4.82	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge V _{GS} = 4.5V	Qg	-	18.5	-	nC		
Total Gate Charge V _{GS} = 10V	Qq	-	43	-	nC	V _{DS} = 15V, V _{GS} = 10V,	
Gate-Source Charge	Qgs	-	4.7	-	nC	I _D = 9.8A	
Gate-Drain Charge	Qqd	-	4.0	-	nC	1	
Turn-On Delay Time	t _{D(ON)}	-	6.62	-	ns		
Turn-On Rise Time	tr	-	8.73	-	ns	$V_{GS} = 10V, V_{DS} = 10V,$	
Turn-Off Delay Time	t _{D(OFF)}	-	36.41	-	ns	$R_g = 3\Omega, R_L = 1.2\Omega$	
Turn-Off Fall Time	t _f	-	4.69	-	ns	1	

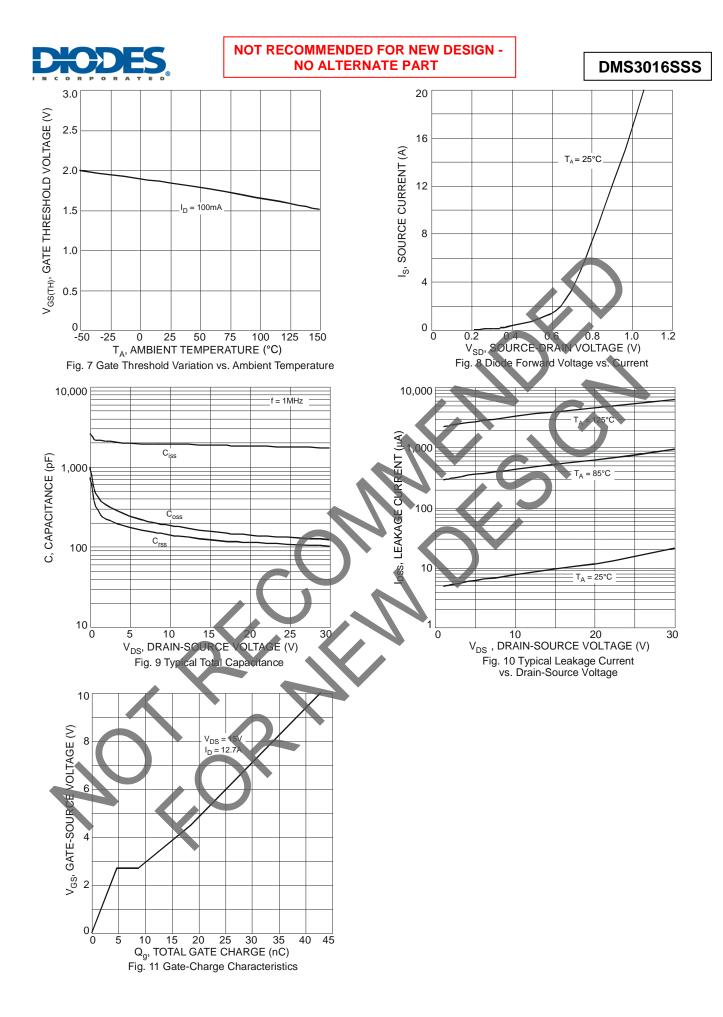
5. Device mounted on minimum recommended layout. The value in any given application depends on the user's specific board design. 6. Repetitive rating, pulse width limited by junction temperature. 7. I_{AR} and E_{AR} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$. 8. Short duration pulse test used to minimize self-heating effect. 9. Guaranteed by design. Not subject to production testing. Notes:



NOT RECOMMENDED FOR NEW DESIGN -NO ALTERNATE PART

DMS3016SSS

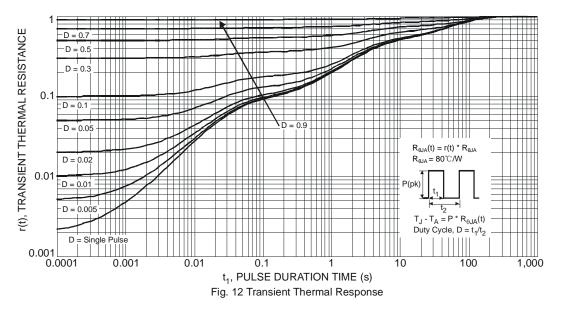






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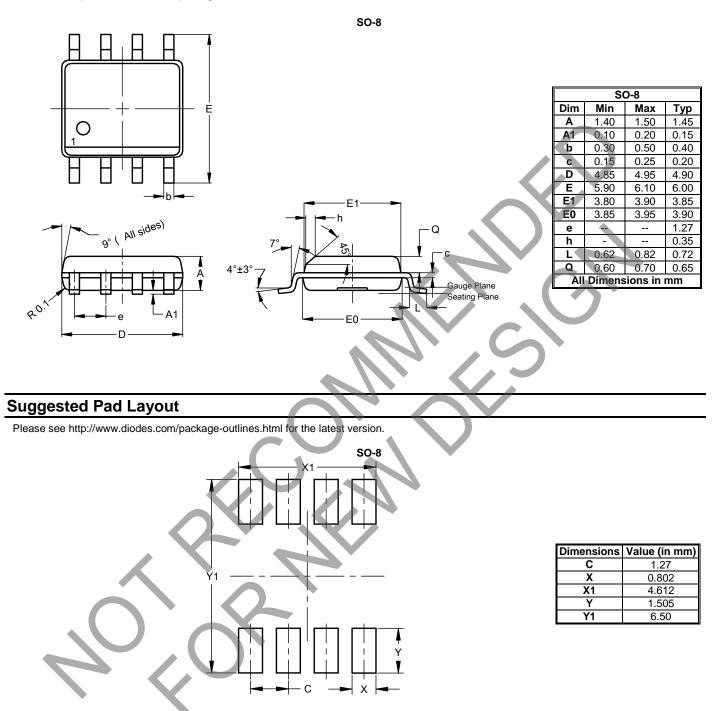
DMS3016SSS





Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.





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