



DMT4008LFV

40V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8 (Type UX)

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
40V	7.9mΩ @ V _{GS} = 10V	54.8A
	12mΩ @ V _{GS} = 4.5V	44.5A

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, which makes it ideal for high-efficiency power management applications.

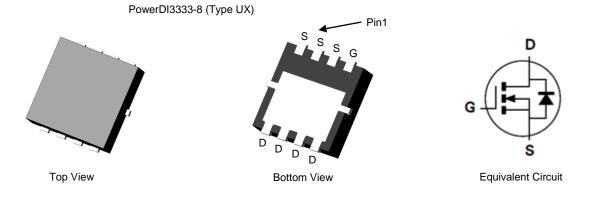
- Power Management Functions
- DC-DC Converters

Features

- 100% Unclamped Inductive Switching—Ensures More Reliable and Robust End Application
- Low On-Resistance
- Low Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: PowerDI[®]3333-8 (Type UX)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.072 grams (Approximate)



Ordering Information (Note 4)

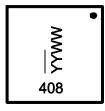
Part Number	Case	Packaging
DMT4008LFV-7	PowerDI3333-8 (Type UX)	2000/Tape & Reel
DMT4008LFV-13	PowerDI3333-8 (Type UX)	3000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>

4. For packaging details, see https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



408 = Product Type Marking Code $\underline{YY}WW$ = Date Code Marking \underline{YY} = Last Two Digits of Year (ex: 18 = 2018) WW = Week Code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	40	V	
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current, V_{GS} = 10V (Note 6)	T _C = +25°C T _C = +70°C	ID	54.8 43.9	А
Continuous Drain Current, V _{GS} = 10V (Note 5)	T _A = +25°C T _A = +70°C	ID	12.1 9.7	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	70	А
Maximum Continuous Body Diode Forward Current (Note 6)		ls	29.8	А
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle	= 1%)	I _{SM}	70	А
Avalanche Current, L = 0.3mH	I _{AS}	11.3	А	
Avalanche Energy, L = 0.3mH		E _{AS}	19.2	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	PD	1.9	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{ÐJA}	64	°C/W	
Total Power Dissipation (Note 6) T _C = +25°C		PD	35.7	W
Thermal Resistance, Junction to Case (Note 6)		Rejc	3.5	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)						-	
Drain-Source Breakdown Voltage	BV _{DSS}	40	—	—	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	IDSS		—	1	μA	$V_{DS} = 32V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}		—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	1	—	3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	D		6.5	7.9	mΩ	$V_{GS} = 10V, I_D = 12A$	
	R _{DS(ON)}		9.4	12	mu	V _{GS} = 4.5V, I _D = 10A	
Diode Forward Voltage	V _{SD}	_	_	1.2	V	$V_{GS} = 0V, I_{S} = 10A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}		1179	—	pF		
Output Capacitance	Coss		384	—	pF	$V_{DS} = 20V, V_{GS} = 0V,$ f = 1MHz	
Reverse Transfer Capacitance	C _{rss}	_	42		pF		
Gate Resistance	Rg	_	1.7		Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Qg	_	17.1	_	nC		
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	8.3		nC		
Gate-Source Charge	Q _{gs}	—	2.4	_	nC	$V_{DS} = 20V, I_D = 10A$	
Gate-Drain Charge	Q _{gd}	—	3.4	_	nC		
Turn-On Delay Time	t _{D(ON)}	_	3.5	_	ns		
Turn-On Rise Time	t _R	_	3.7	_	ns		
Turn-Off Delay Time	t _{D(OFF)}	_	17.1		ns	$R_G = 6\Omega, I_D = 10A$	
Turn-Off Fall Time	t _F	_	6.4	_	ns	7	
Body Diode Reverse Recovery Time	t _{RR}		19.8	_	ns		
Body Diode Reverse Recovery Charge	Q _{RR}	_	8.8	—	nC	I _F = 10A, di/dt = 400A/µs	

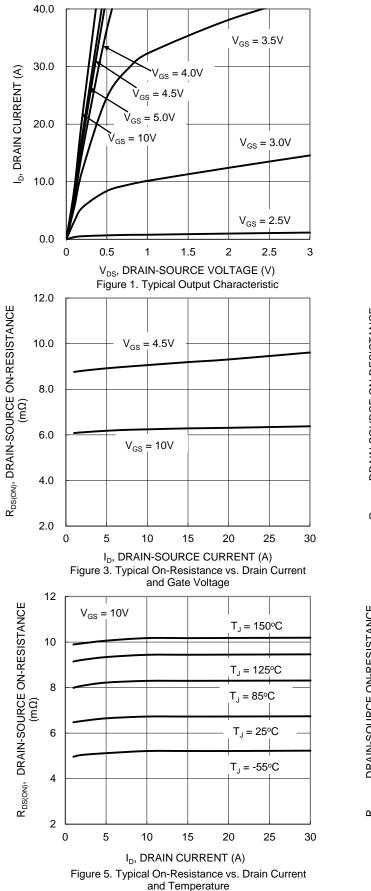
Notes:

5. Device mounted on FR-4 substrate PCB, 2oz copper, with thermal bias to bottom layer and 1inch square copper plate.

Thermal resistance from junction to soldering point (on the exposed drain pad).
Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.





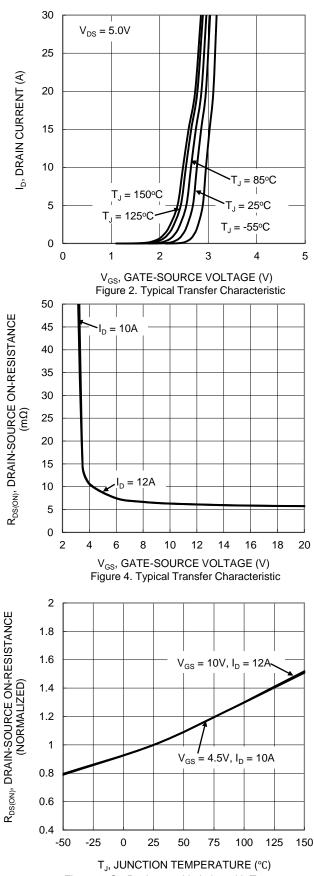
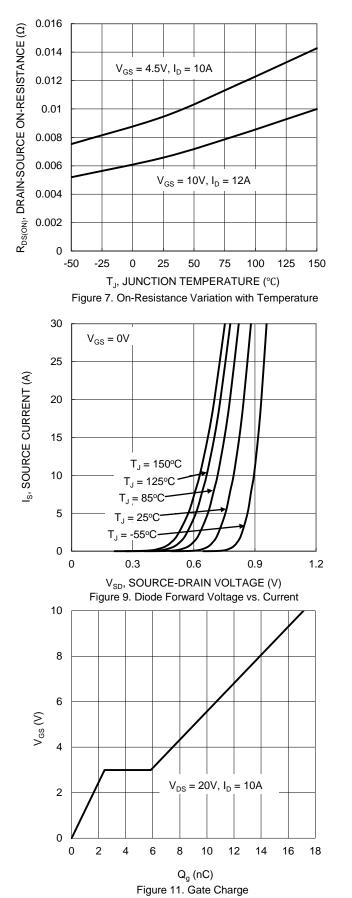


Figure 6. On-Resistance Variation with Temperature





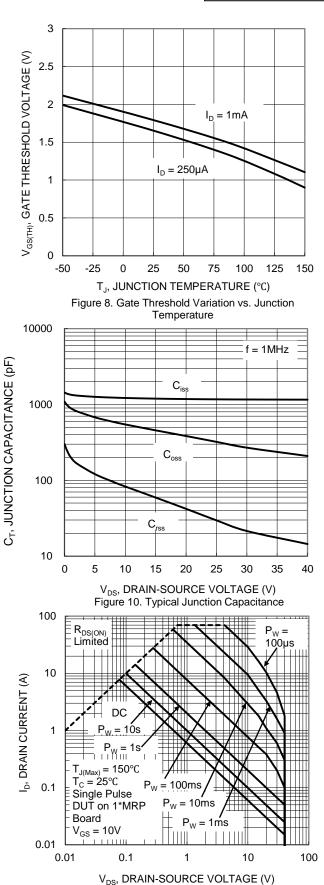
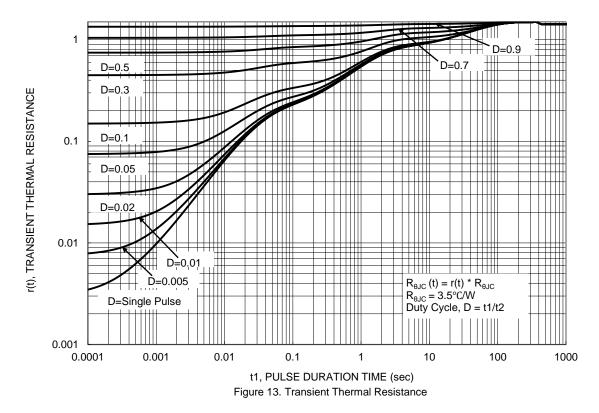


Figure 12. SOA, Safe Operation Area

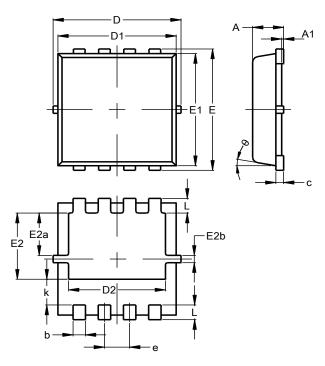






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

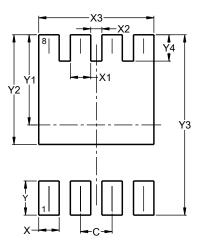


	PowerDI3333-8 (Type UX)					
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A1	0.00	0.05				
b	0.25	0.40	0.32			
С	0.10	0.25	0.15			
D	3.20	3.40	3.30			
D1	2.95	3.15	3.05			
D2	2.30	2.70	2.50			
Е	3.20	3.40	3.30			
E1	2.95	3.15	3.05			
E2	1.60	2.00	1.80			
E2a	0.95	1.35	1.15			
E2b	0.10	0.30	0.20			
e	0.65 BSC					
k	0.50	0.90	0.70			
L	0.30	0.50	0.40			
θ	0°	12°	10°			
All	All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (Type UX)



Dimensions	Value (in mm)		
C	0.650		
Х	0.420		
X1	0.420		
X2	0.230		
X3	2.370		
Y	0.700		
Y1	1.850		
Y2	2.250		
Y3	3.700		
Y4	0.540		

PowerDI3333-8 (Type UX)



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