



DMT6005LSS

60V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = 25°C
001/	6mΩ @ V _{GS} = 10V	13.5A
60V	8.9mΩ @ V _{GS} = 4.5V	11.3A

Description and Applications

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$, yet maintain superior switching performance, making it ideal for high efficiency power management applications.

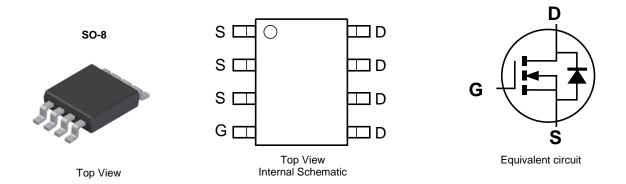
- High Frequency Switching
- Synchronous Rectification
- DC-DC Converters

Features and Benefits

- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (63)
- Weight: 0.074 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMT6005LSS-13	SO-8	2,500/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

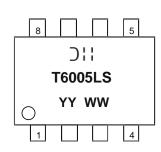
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

Notes:

and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



)|| = Manufacturer's Marking T6005LS = Product Type Marking Code YYWW = Date Code Marking YY or \overline{YY} = Year (ex: 15 = 2015) WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	ID	13.5 10.8	А
	t<10s	T _A = +25°C T _A = +70°C	ID	18.1 14.4	А
Maximum Continuous Body Diode Forward Current (Note 6)			I _S	3	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	80	А
Avalanche Current, L = 1mH			I _{AS}	14.8	А
Avalanche Energy, L = 1mH			E _{AS}	98	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	1.3	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	93	°C/W
	t<10s	$R_{ extsf{ heta}JA}$	53	°C/W
Total Power Dissipation (Note 6)		PD	1.7	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Devi	73	°C/W
mermai Resistance, Junction to Ambient (Note 6)	t<10s	R _{0JA}	41	°C/W
Thermal Resistance, Junction to Case (Note 6)	R _{θJC}	12.7	°C/W	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C	

Electrical Characteristics (T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)			- 71-				
Drain-Source Breakdown Voltage	BV _{DSS}	60	—	—	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}		—	1	μA	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	1	—	3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
		_	5	6	mΩ	V _{GS} = 10V, I _D = 20A	
Static Drain-Source On-Resistance	R _{DS(ON)}		5.7	7.2		$V_{GS} = 6V, I_D = 20A$	
		_	6.7	8.9		V _{GS} = 4.5V, I _D = 12.5A	
Diode Forward Voltage	V _{SD}	_	0.9	1.2	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)						÷	
Input Capacitance	Ciss	_	2962	—		$V_{DS} = 30V, V_{GS} = 0V,$ f = 1MHz	
Output Capacitance	C _{oss}	_	965	—	pF		
Reverse Transfer Capacitance	C _{rss}	_	60	—			
Gate Resistance	Rg	_	0.66	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Qq	_	47.1	—			
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	23.1	—	nC	$V_{DD}=30V,I_D=20A$	
Gate-Source Charge	Q _{gs}	_	10.2	—	nc		
Gate-Drain Charge	Q _{gd}	_	12.5	—			
Turn-On Delay Time	t _{D(ON)}	_	8.3	—			
Turn-On Rise Time	t _R	_	9.4	—	- 0	$V_{DD} = 30V, V_{GS} = 10V,$ $I_D = 20A, R_g = 3.3\Omega$	
Turn-Off Delay Time	tD(OFF)	_	22	—	nS		
Turn-Off Fall Time	t _F	_	8.9	—			
Body Diode Reverse Recovery Time	t _{RR}	_	40.4	—	nS		
Body Diode Reverse Recovery Charge	Q _{RR}		49.7	—	nC	— I _F = 20A, di/dt = 100A/μs	

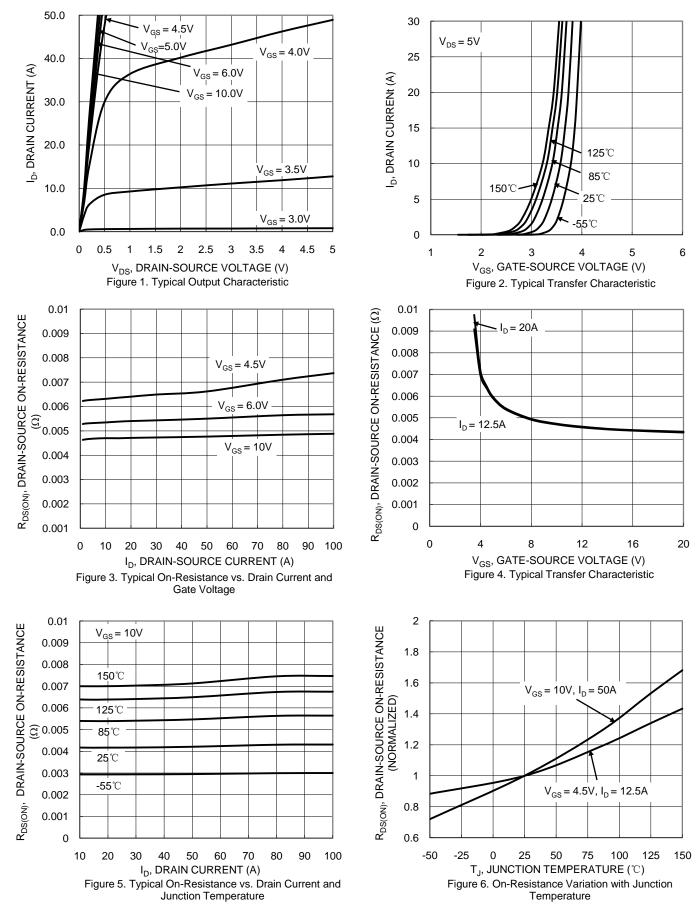
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. Notes:

6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

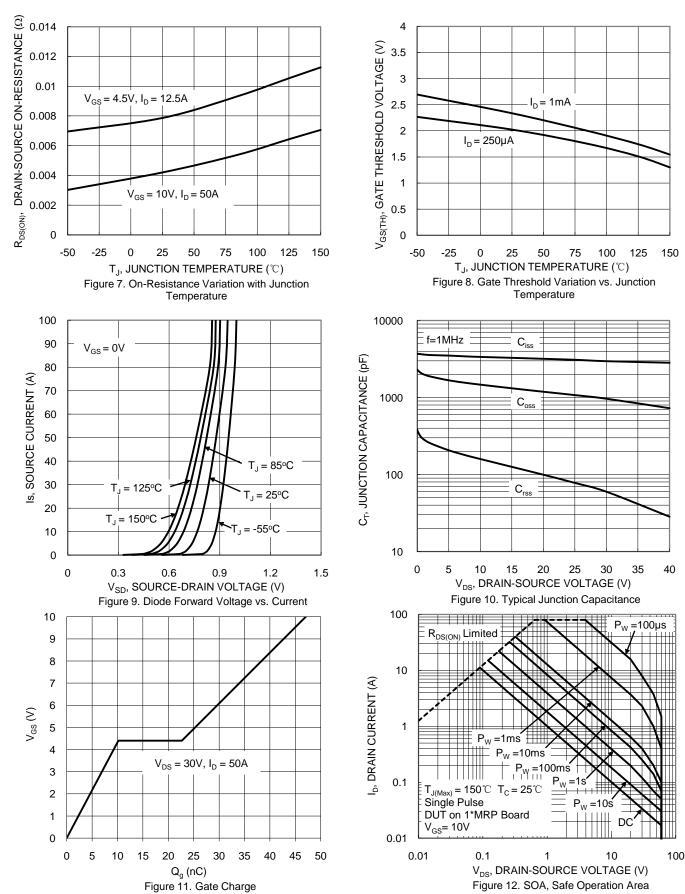
7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to product testing.



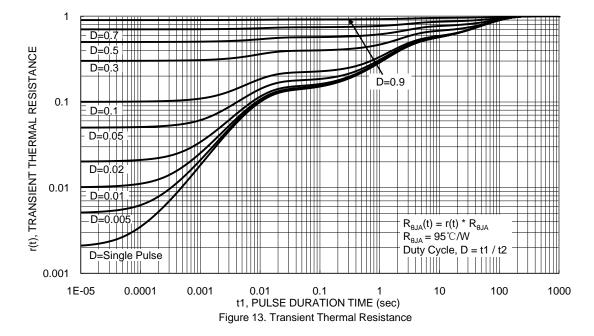
DMT6005LSS







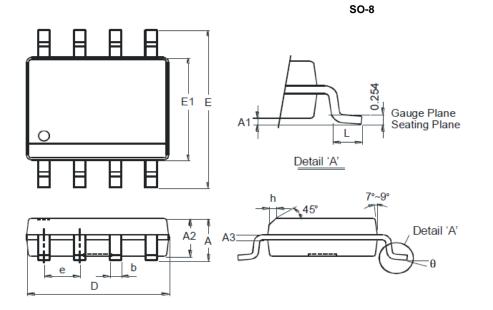






Package Outline Dimensions

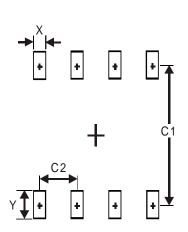
Please see http://www.diodes.com/package-outlines.html for the latest version.



SO-8			
Dim	Min	Max	
Α	-	1.75	
A1	0.10	0.20	
A2	1.30	1.50	
A3	0.15	0.25	
b	0.3	0.5	
D	4.85	4.95	
ш	5.90	6.10	
E1	3.85	3.95	
e	1.27	Тур	
h	-	0.35	
L	0.62	0.82	
θ	0°	8°	
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SO-8

Dimensions	Value (in mm)			
Х	0.60			
Y	1.55			
C1	5.4			
C2	1.27			



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