



60V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _C = +25°C		
60V	$10m\Omega$ @ $V_{GS} = 10V$	74.5A		
	12.8m Ω @ V _{GS} = 4.5V	65.8A		

Features and Benefits

- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low R_{DS(ON)}—Ensures On State Losses Are Minimized
- Excellent Q_{gd x} R_{DS(ON)} Product (FOM)
- Advanced Technology for DC-DC Converters
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

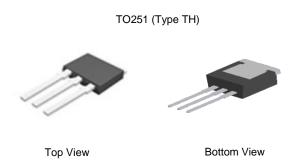
Description and Applications

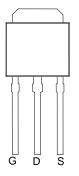
This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

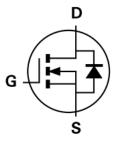
- Power Management Functions
- DC-DC Converters
- Backlighting

Mechanical Data

- Case: TO251 (Type TH)
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.33 grams (Approximate)







Top View Pin Configuration

Internal Schematic

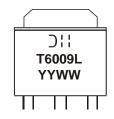
Ordering Information (Note 4)

Part Number Case		Packaging		
DMT6009LJ3	TO251 (Type TH)	75 Pieces / Tube		

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3).compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



The Manufacturer's Marking
T6009L = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 18 = 2018)
WW or WW = Week Code (01 to 53)



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	60	V	
Gate-Source Voltage			±16	V
Continuous Drain Current (Note 7)	$T_C = +25$ °C $T_C = +70$ °C	I _D	74.5 59.6	А
Maximum Body Diode Forward Current (Note 7)		Is	50	Α
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)	I _{DM}	280	Α	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)	I _{SM}	280	Α	
Avalanche Current, L=0.1mH	I _{AS}	28.2	Α	
Avalanche Energy, L=0.1mH	E _{AS}	39.8	mJ	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)	T _A = +25°C	P _D	2.9	W
Thermal Resistance, Junction to Ambient (Note 6)		R _{OJA}	43	°C/W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{\Theta JA}$	80	°C/W
Total Power Dissipation (Note 7)	T _C = +25°C	P _D	83.3	W
Thermal Resistance, Junction to Case (Note 7)		$R_{\Theta JC}$	1.5	°C/W
Operating and Storage Temperature Range		$T_{J,}T_{STG}$	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	Cymbol		1,76	Mux	Onic	Test condition	
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}		_	1	μΑ	V _{DS} = 48V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}		_	±100	nA	$V_{GS} = \pm 16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)				I	l		
Gate Threshold Voltage	V _{GS(TH)}	0.7		2	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance	D	_	8	10	mΩ	V _{GS} = 10V, I _D = 13.5A	
Static Diani-Source On-Resistance	R _{DS(ON)}	_	9.8	12.8	11122	V _{GS} = 4.5V, I _D = 11.5A	
Diode Forward Voltage	V _{SD}		0.8	1.2	V	$V_{GS} = 0V$, $I_S = 5A$	
DYNAMIC CHARACTERISTICS (Note 9)				•			
Input Capacitance	C _{iss}	_	1925	_		V _{DS} = 30V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	Coss	_	438	_	pF		
Reverse Transfer Capacitance	C _{rss}		41	_			
Gate Resistance	Rg		1.7	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg		15.6	_			
Total Gate Charge (V _{GS} = 10V)	Qg	_	33.5		nC	V _{DS} = 30V, I _D = 13.5A	
Gate-Source Charge	Q _{gs}	_	4.7	_	IIC		
Gate-Drain Charge	Q _{gd}		5.3	_			
Turn-On Delay Time	t _{D(ON)}		4.5	_		$V_{DD} = 30V, V_{GS} = 10V,$ $R_g = 6\Omega, I_D = 13.5A$	
Turn-On Rise Time	t _R	_	8.6	_	1		
Turn-Off Delay Time	t _{D(OFF)}	_	35.9	_	ns		
Turn-Off Fall Time	t _F	_	15.7	_	1		
Body Diode Reverse Recovery Time	t _{RR}	_	18.2	_	ns		
Body Diode Reverse Recovery Charge	Q _{RR}	_	33.1	_	nC	$I_F = 13.5A$, di/dt = 400A/ μ s	

Notes:

- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
 Thermal resistance from junction to soldering point (on the exposed drain pad).
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to production testing.



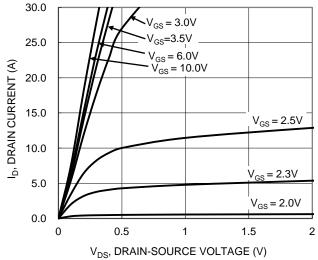


Figure 1. Typical Output Characteristic

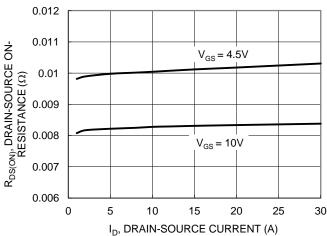


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

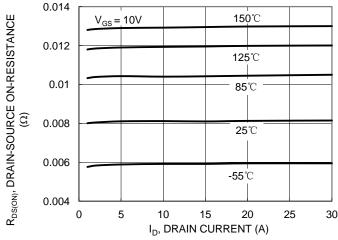


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

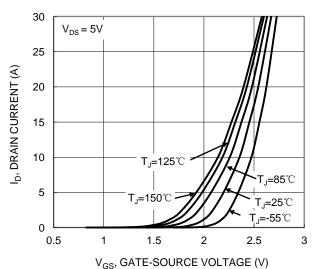


Figure 2. Typical Transfer Characteristic

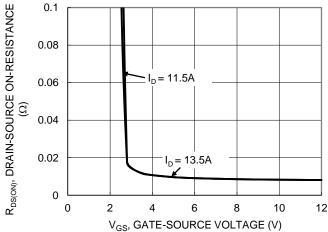


Figure 4. Typical Transfer Characteristic

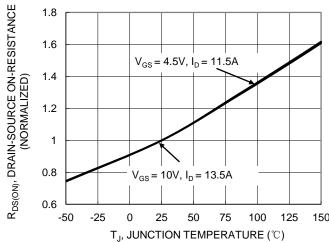
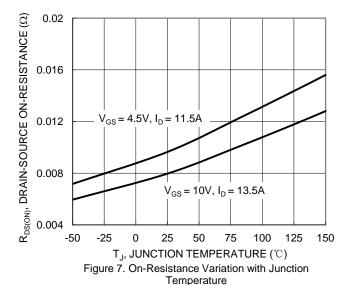
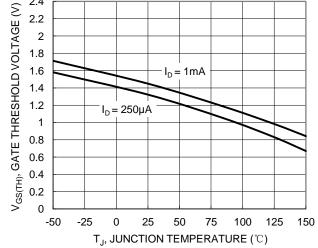


Figure 6. On-Resistance Variation with Junction Temperature





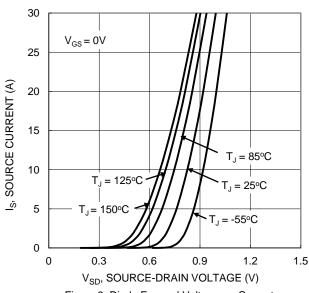


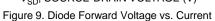
2.4

2.2

1000

Figure 8. Gate Threshold Variation vs. Junction Temperature





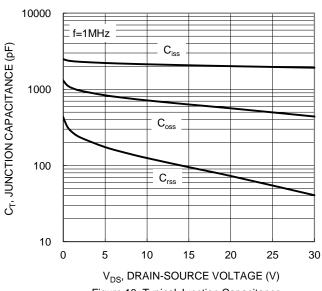
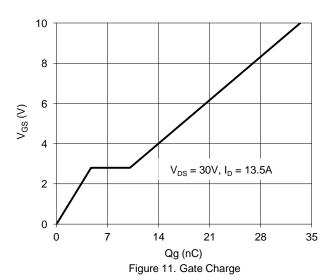


Figure 10. Typical Junction Capacitance



 $R_{DS(ON)}$ Limited 100 ID, DRAIN CURRENT (A) 10 $T_{J(Max)} = 150$ °C $P_W = 10 ms$ T_C = 25°C Single Pulse $P_W = 100 ms$ DUT on infinite heatsink DC V_{GS}= 10V 0.1 0.1 100 V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



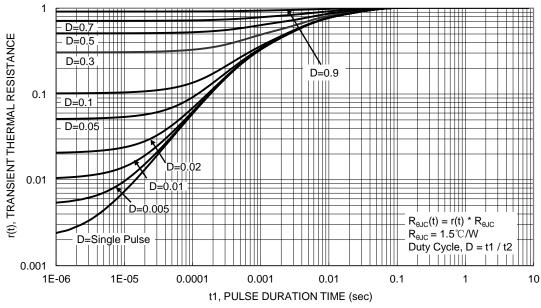
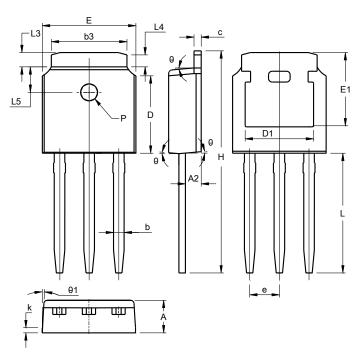


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO251 (Type TH)



TO251 (Type TH)					
Dim	Min	Max	Тур		
Α	2.20	2.40	2.30		
A2	0.97	1.17	1.07		
b	0.68	0.90	0.78		
b3	5.20	5.50	5.33		
C	0.43	0.63	0.53		
D	5.98	6.22	6.10		
D1	5	5.30 REF	=		
е	2	.286 BS	С		
Е	6.40	6.80	6.60		
E1	4.63	5.03	4.83		
Н	16.22	16.82	16.52		
k	0.40REF				
L	9.15	9.65	9.40		
L3	0.88	1.28	1.02		
L4	0.75 REF				
L5	1.65	1.95	1.80		
ΡØ	1.20				
θ	5°	9°	7°		
θ1	5°	9°	7°		
All Dimensions in mm					



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