



60V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BVDSS	R _{DS(ON)} Max	I _D Max T _A = +25°C
COV/	11mΩ @ V _{GS} = 10V	10.4A
60V	$14m\Omega @ V_{GS} = 4.5V$	9.3A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- High Frequency Switching
- Synchronous Rectification
- DC-DC Converters

Features and Benefits

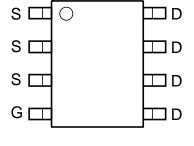
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low RDS(ON) Minimizes On-State Losses
- Low Input Capacitance
- · Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

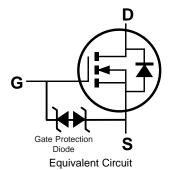
Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (§3)
- Weight: 0.074 grams (Approximate)









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Top View

Top View Pin-Out

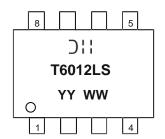
Ordering Information (Note 4)

Part Number	Case	Packaging
DMT6012LSS-13	SO-8	2,500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



⊃¦¦ = Manufacturer's Marking T6012LS = Product Type Marking Code YYWW = Date Code Marking YY or YY = Year (ex: 20 = 2020) WW = Week (01 to 53)



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	60	V
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	I _D	10.4 8.4	А
Maximum Continuous Body Diode Forward Current (Note 6)			ls	10	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	65	А
Avalanche Current, L = 0.3mH			las	15.8	Α
Avalanche Energy, L = 0.3mH			Eas	37.5	mJ

Thermal Characteristics ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	1.2	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RθJA	106	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	PD	1.84	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	68	°C/W
Thermal Resistance, Junction to Case (Note 6)		Rejc	9.2	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

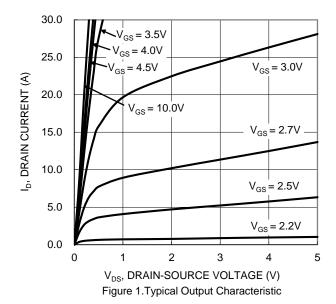
Electrical Characteristics (T_A = +25°C, unless otherwise specified.)

			1 _				
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	60	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS		_	1	μΑ	V _{DS} = 48V, V _{GS} = 0V	
Gate-Source Leakage	Igss		_	±10	μΑ	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	0.7	_	2	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	D	_	8.4	11	•	V _{GS} = 10V, I _D = 10A	
Static Drain-Source Off-Resistance	RDS(ON)	_	11.5	14	mΩ	V _G S = 4.5V, I _D = 5A	
Diode Forward Voltage	VsD	_	0.8	1.2	V	V _G S = 0V, I _S = 20A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	1522	_		V _{DS} = 30V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	Coss	l	352	-	pF		
Reverse Transfer Capacitance	Crss		27.5				
Gate Resistance	R_g		1.4		Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (VGS = 4.5V)	Qg		10.7			V _{DS} = 30V, I _D = 10A	
Total Gate Charge (V _{GS} = 10V)	Q_g	l	22.2	l	nC		
Gate-Source Charge	Qgs	l	3.3	l	IIC		
Gate-Drain Charge	Q_{gd}		4.2				
Turn-On Delay Time	t _{D(ON)}	l	4.4	l		$V_{GS} = 10V, V_{DS} = 30V,$ $R_{G} = 6\Omega, I_{D} = 10A$	
Turn-On Rise Time	tR		6.7	_	ns		
Turn-Off Delay Time	tD(OFF)	_	25.5	_	ns		
Turn-Off Fall Time	tF		12.5	_			
Body Diode Reverse Recovery Time	trr	_	25.8	_	ns	I 400 di/dh 4000//	
Body Diode Reverse Recovery Charge	Q _{RR}	_	15.1	_	nC IF = 10A, di/dt = 100A/µs		

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- $\label{eq:continuous} \textbf{7. Short duration pulse test used to minimize self-heating effect.}$
- 8. Guaranteed by design. Not subject to product testing.





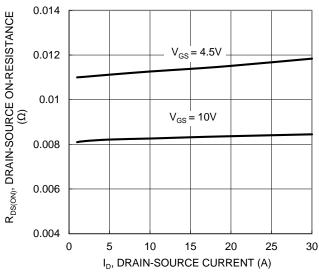


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

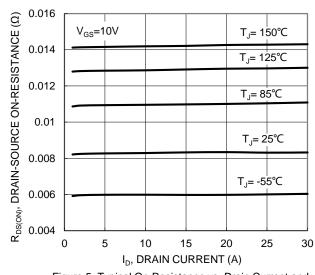
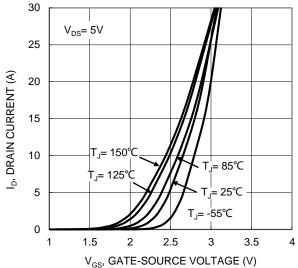


Figure 5. Typical On-Resistance vs. Drain Current and Temperature



V_{GS}, GATE-SOURCE VOLTAGE (V) Figure 2. Typical Transfer Characteristic

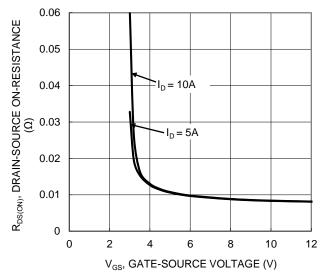


Figure 4. Typical Transfer Characteristic

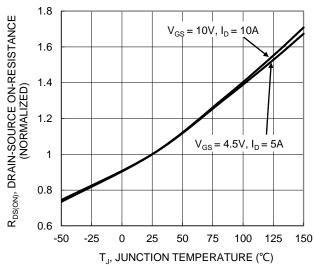


Figure 6. On-Resistance Variation with Temperature



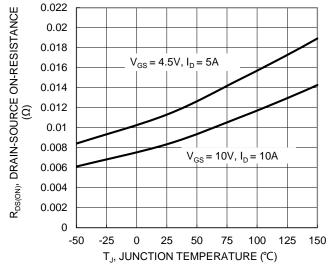


Figure 7. On-Resistance Variation with Temperature

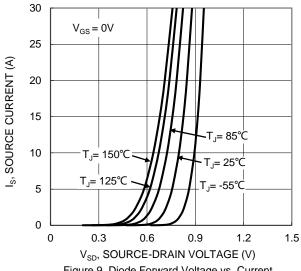


Figure 9. Diode Forward Voltage vs. Current

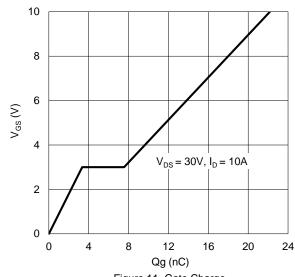


Figure 11. Gate Charge

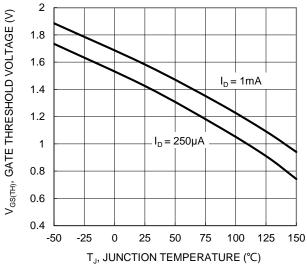
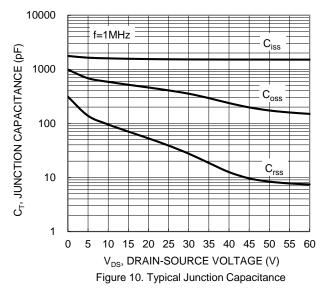


Figure 8. Gate Threshold Variation vs. JunctionTemperature



100 ID, DRAIN CURRENT (A) 10 =10ms T_{J(MAX)}=150°C P_W=100ms T_C=25°C 0.1 Single Pulse DUT on 1*MRP board V_{GS}=10V 0.01 0.01 0.1 10 100 V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



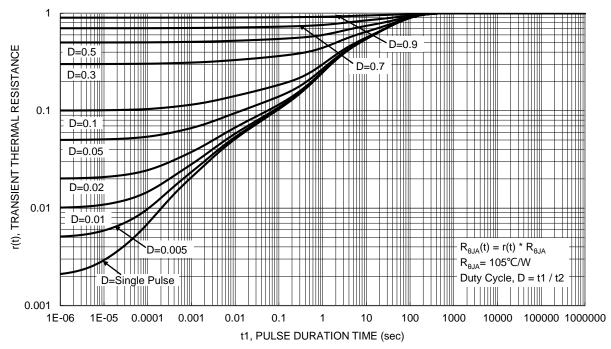


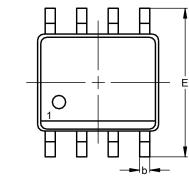
Figure 13. Transient Thermal Resistance

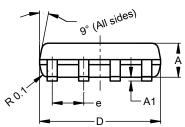


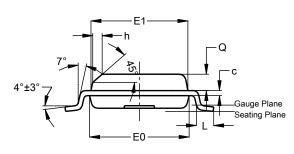
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.







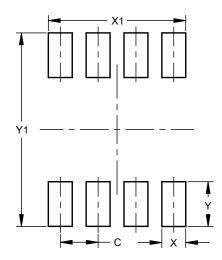


SO-8						
Dim	Min	Max	Тур			
Α	1.40	1.50	1.45			
A1	0.10	0.20	0.15			
q	0.30	0.50	0.40			
C	0.15	0.25	0.20			
D	4.85	4.95	4.90			
Е	5.90	6.10	6.00			
E1	3.80	3.90	3.85			
E0	3.85	3.95	3.90			
е			1.27			
h			0.35			
٦	0.62	0.82	0.72			
Ø	0.60	0.70	0.65			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8



Dimensions	Value (in mm)				
С	1.27				
Х	0.802				
X1	4.612				
Y	1.505				
Y1	6.50				



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