



DMTH4011SPD

40V 175°C DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _C = +25°C
40V	15mΩ @ V _{GS} = 10V	42A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) and vet maintain superior switching performance, making it ideal for high efficiency power management applications.

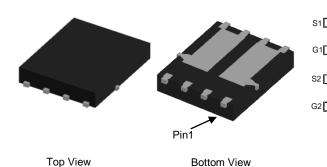
- Backlighting
- Power Management Functions
- DC-DC Converters

Features and Benefits

- Rated to +175°C Ideal for High Ambient Temperature • Environments
- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low RDS(ON) Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMTH4011SPDQ)

Mechanical Data

- Case: PowerDI[®]5060-8 (Type C)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.097 grams (Approximate)



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	S1	S2

Pin Out Top View

Equivalent Circuit

Ordering Information (Note 4)

	Part Number	Case	Packaging			
	DMTH4011SPD-13	PowerDI5060-8 (Type C)	2,500/Tape & Reel			
Notes:	Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.					

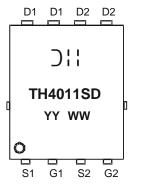
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2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



⊃¦¦ = Manufacturer's Marking TH4011SD = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 17 = 2017) WW = Week (01 to 53)

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	40	V	
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current (Note 6)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	I _D	42 29.7	A
Continuous Drain Current (Note 5)	$T_A = +25^{\circ}C$ $T_A = +100^{\circ}C$	ID	11.1 7.8	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	60	А	
Maximum Continuous Body Diode Forward Current (Note 6)	ls	3.3	А	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)	I _{SM}	60	A	
Avalanche Current, L = 0.3mH		I _{AS}	11.9	A
Avalanche Energy, L = 0.3mH		E _{AS}	21.4	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	2.6	W
Thermal Resistance, Junction to Ambient (Note 5)		R _{0JA}	57	°C/W
Total Power Dissipation (Note 6)	T _C = +25°C	PD	37.5	W
Thermal Resistance, Junction to Case (Note 6)		R _{θJC}	4	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)			•			•	
Drain-Source Breakdown Voltage	BV _{DSS}	40		_	V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	_		1	μA	$V_{DS} = 32V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_		±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)			•				
Gate Threshold Voltage	V _{GS(TH)}	2		4	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	11.6	15	mΩ	$V_{GS} = 10V, I_D = 20A$	
Diode Forward Voltage	V _{SD}	_	_	1.2	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)			•				
Input Capacitance	C _{iss}	_	805	_	pF		
Output Capacitance	Coss	_	208	_	pF	= V _{DS} = 20V, V _{GS} = 0V, f = 1MHz	
Reverse Transfer Capacitance	C _{rss}		15	_	pF		
Gate Resistance	Rg		2.76	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge	Qg		10.6	_	nC		
Gate-Source Charge	Q _{gs}	_	2.2	_	nC	$-V_{DS} = 20V, I_D = 20A,$ $-V_{GS} = 10V$	
Gate-Drain Charge	Q _{gd}	_	2.7	_	nC		
Turn-On Delay Time	t _{D(ON)}	_	4.1	_	ns		
Turn-On Rise Time	t _R	_	3.8	_	ns	$V_{DD} = 20V, V_{GS} = 10V,$ $R_G = 1.6\Omega, I_D = 20A$	
Turn-Off Delay Time	t _{D(OFF)}		8.6	—	ns		
Turn-Off Fall Time	tF	_	1.9		ns		
Body Diode Reverse Recovery Time	t _{RR}	—	10.2	—	ns		
Body Diode Reverse Recovery Charge	Q _{RR}	—	9.6	—	nC	− I _F = 15A, di/dt = 400A/µs	

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz. copper, with thermal bias to bottom layer 1inch square copper plate.

Thermal resistance from junction to soldering point (on the exposed drain pad).
Short duration pulse test used to minimize self-heating effect.

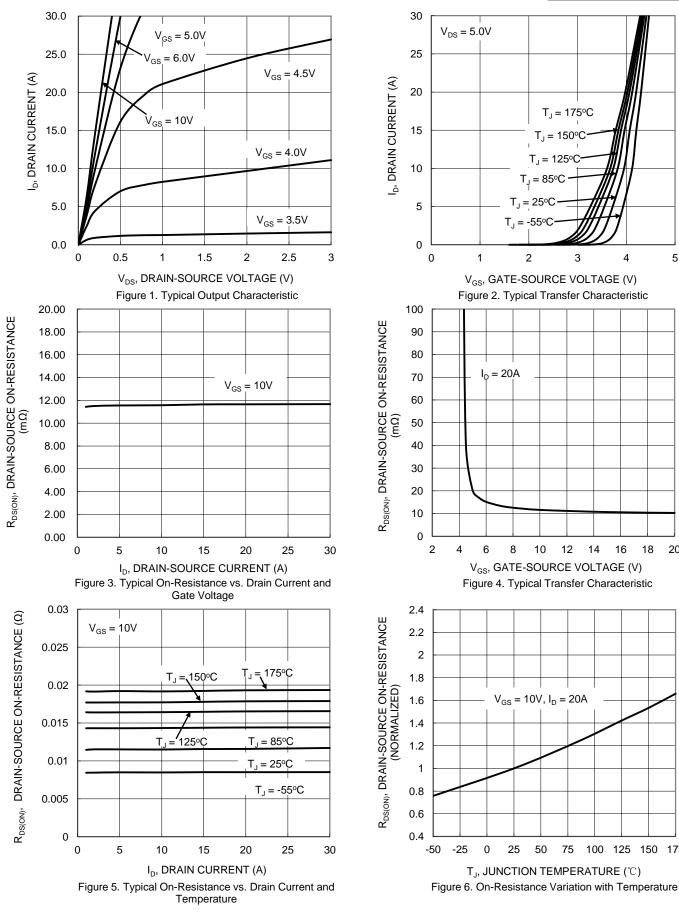
8. Guaranteed by design. Not subject to product testing.



DMTH4011SPD

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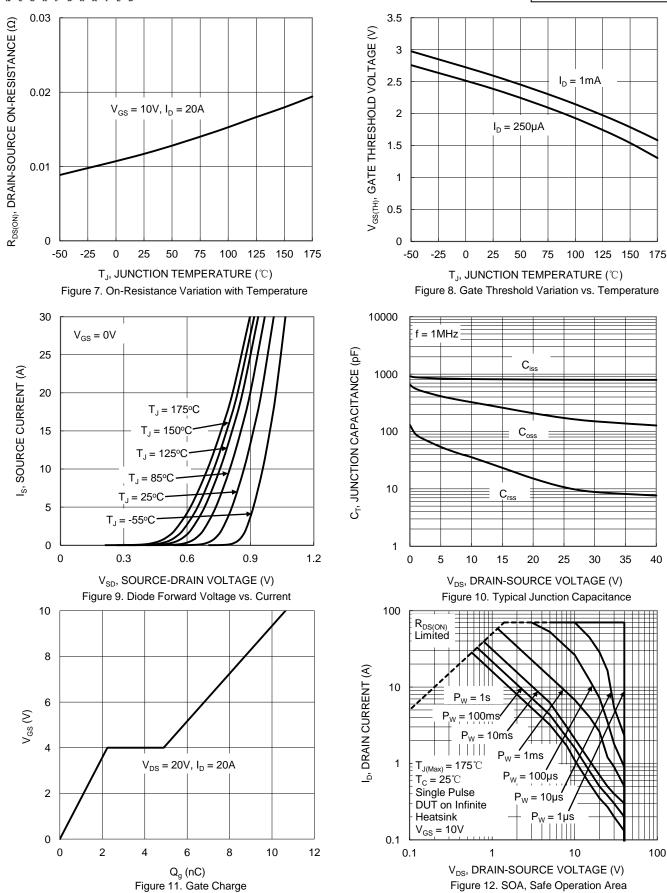
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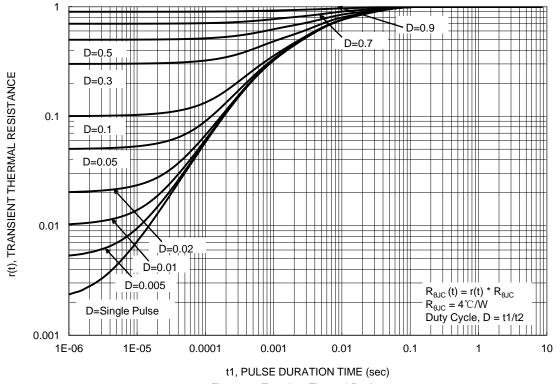
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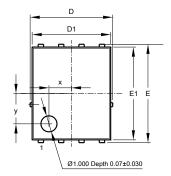


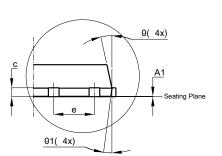


Package Outline Dimensions

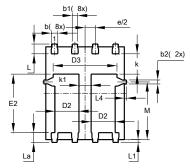
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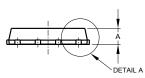
PowerDI5060-8 (Type C)





DETAIL A



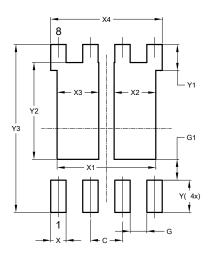


Po	PowerDI5060-8 (Type C)					
Dim	Min	Тур				
Α	0.90	1.10	1.00			
A1	0	0.05	0.02			
b	0.33	0.51	0.41			
b1	0.300	0.366	0.333			
b2	0.20	0.35	0.25			
С	0.23	0.33	0.277			
D	ļ	5.15 BSC)			
D1	4.85	4.95	4.90			
D2	1.40	1.60	1.50			
D3	-	-	3.98			
Е	(6.15 BSC)			
E1	5.75	5.85	5.80			
E2	3.56	3.76	3.66			
е		1.27BSC	;			
k	-	-	1.27			
k1	0.56	-	-			
L	0.51	0.71	0.61			
La	0.51	0.71	0.61			
L1	0.05	0.20	0.175			
L4	-	-	0.125			
М	3.50	3.71	3.605			
х	-	-	1.400			
У	-	-	1.900			
θ	10°	12°	11°			
θ1	6°	8°	7°			
AI	All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (Type C)



Dimensions	Value
Dimensions	(in mm)
С	1.270
G	0.660
G1	0.820
Х	0.610
X1	3.910
X2	1.650
X3	1.650
X4	4.420
Y	1.270
Y1	1.020
Y2	3.810
Y3	6.610



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