



DMTH6005LK3Q

Product Summary

BV _{DSS}	R _{DS(ON)} max	Ι _D T _C = +25°C
60V	5.6mΩ @ V _{GS} = 10V	90A

Description and Applications

This MOSFET has been designed to meet the stringent requirements of Automotive applications. It is qualified to AECQ101, supported by a PPAP and is ideal for use in:

- Engine Management Systems
- Body Control Electronics
- DCDC Converters

60V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET

Features

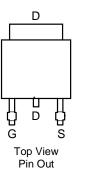
- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching ensures more reliable and robust end application
- Low R_{DS(ON)} minimizes power losses
- Low Q_g minimizes switching losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

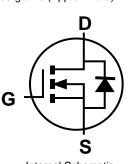
Mechanical Data

- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ⁽³⁾
- Weight: 0.33 grams (Approximate)



Top View





Internal Schematic

Ordering Information (Note 5)

Case	Packaging
TO252 (DPAK)	2,500/Tape & Reel

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html

Marking Information

Notes:



DII = Manufacturer's Marking H6005L = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 15 = 2015) WW = Week Code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage	V _{DSS}	60	V	
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current (Note 7)	T _C = +25°C (Note 10)	ID	90	A
· · · · · ·	$T_{C} = +100^{\circ}C$	2	70	
Maximum Body Diode Forward Current (Note 7)	T _C = +25°C	ls	90	A
Pulsed Drain Current (10µs pulse, duty cycle = 1%)		I _{DM}	150	A
Avalanche Current, L=1mH	I _{AS}	14.8	A	
Avalanche Energy, L=1mH		E _{AS}	98	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 6)	T _A = +25°C	PD	2.1	W
Thermal Resistance, Junction to Ambient (Note 6)		R _{0JA}	38	°C/W
Total Power Dissipation (Note 7)	$T_{C} = +25^{\circ}C$	PD	100	W
Thermal Resistance, Junction to Case (Note 7)		R _{θJC}	1.5	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +175	°C

Electrical Characteristics ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)					1		
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)						·	
Gate Threshold Voltage	V _{GS(TH)}	1	_	3	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
		_	4.5	5.6		$V_{GS} = 10V, I_D = 50A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	5.6	7.2	mΩ	$V_{GS} = 6V, I_D = 20A$	
	. ,	_	7.9	10	1	V _{GS} = 4.5V, I _D = 12.5A	
Diode Forward Voltage	V _{SD}	_	_	1.2	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	_	2962	_			
Output Capacitance	Coss	_	965.2	_	pF	$V_{DS} = 30V, V_{GS} = 0V,$ f = 1MHz	
Reverse Transfer Capacitance	Crss	_	59.8	_			
Gate Resistance	R _G	_	0.66	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Qg	_	47.1	_			
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	23.1	_	nC	N/ 20)/ L 50A	
Gate-Source Charge	Q _{gs}	_	10.2	_	nc	$V_{DD} = 30V, I_D = 50A$	
Gate-Drain Charge	Q _{gd}	_	12.5	_			
Turn-On Delay Time	t _{D(ON)}		8.3				
Turn-On Rise Time	t _R	_	9.4	_		$V_{DD} = 30V, V_{GS} = 10V,$	
Turn-Off Delay Time	t _{D(OFF)}		22		ns	$I_{D} = 30A, R_{G} = 3.3\Omega$	
Turn-Off Fall Time	tF		8.9	—			
Body Diode Reverse Recovery Time	t _{RR}	_	40.4	_	ns	1 204 di/dt 1004/up	
Body Diode Reverse Recovery Charge	Q _{RR}	_	49.7	_	nC	-I _F = 30A, di/dt = 100A/μs	

6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout. Notes:

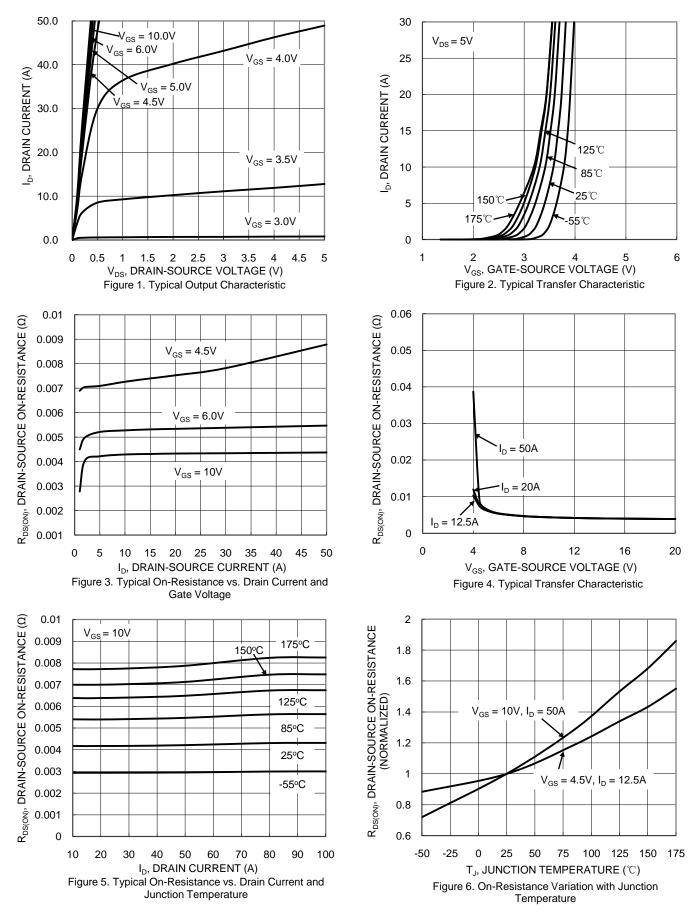
Thermal resistance from junction to soldering point (on the exposed drain pad).
Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to production testing.

10. Package limited.



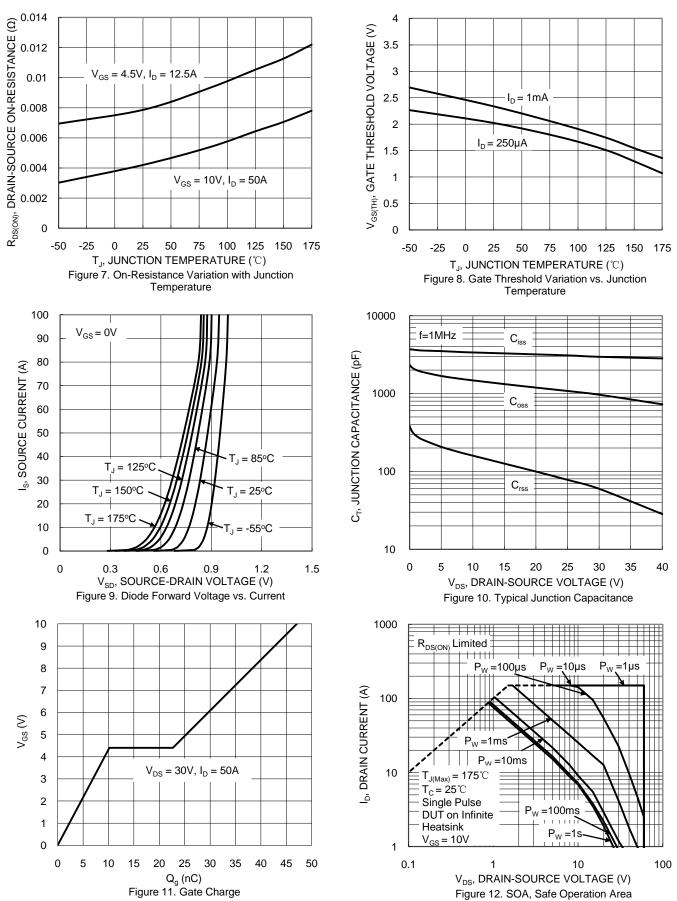
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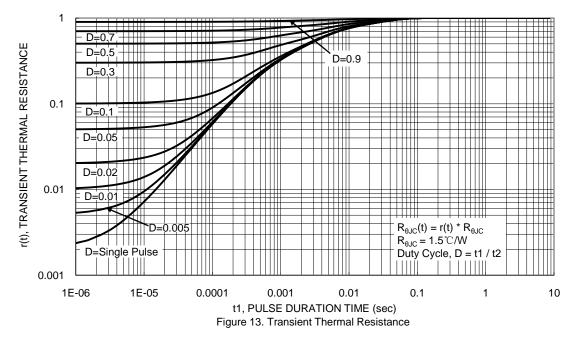
DMTH6005LK3Q Document number: DS38659 Rev. 1 - 2



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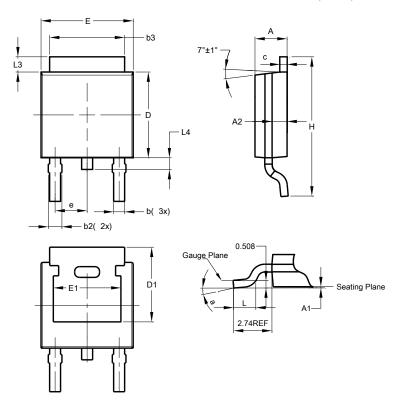




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO252 (DPAK)

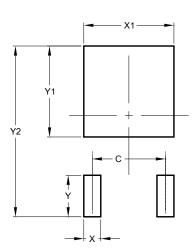


TO252 (DPAK)					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
A1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
p	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
c	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е	-	-	2.286		
Е	6.45	6.70	6.58		
E1	4.32	-	-		
Η	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	-		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO252 (DPAK)



Dimensions	Value (in mm)
С	4.572
Х	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700



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