

## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C
60V	19mΩ @ V <sub>GS</sub> = 10V	33.2A
	28mΩ @ V <sub>GS</sub> = 4.5V	28A

## Features and Benefits

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low Input Capacitance
- Fast Switching Speed
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

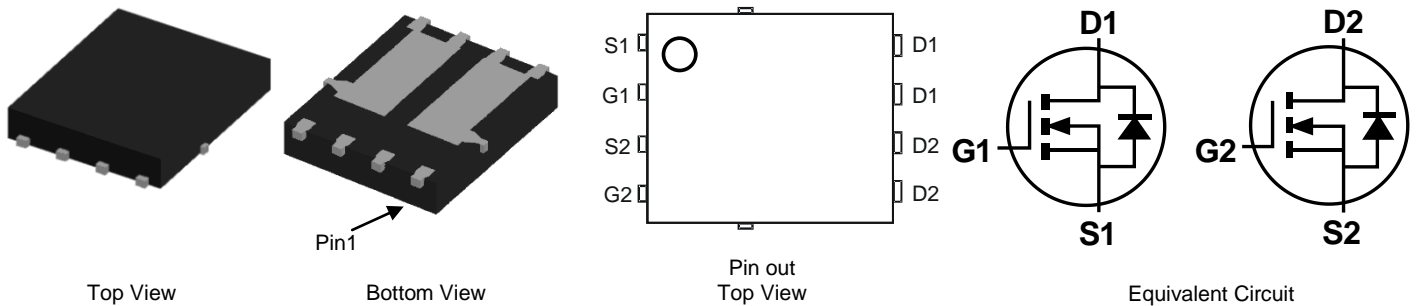
## Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Engine Management Systems
- Body Control Electronics
- DC-DC Converters

## Mechanical Data

- Case: PowerDI<sup>®</sup> 5060-8 (Type C)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 <sup>(e3)</sup>
- Weight: 0.097 grams (Approximate)

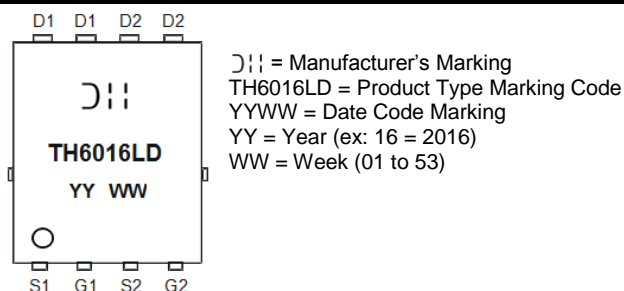


## Ordering Information (Note 5)

Part Number	Case	Packaging
DMTH6016LPDQ-13	PowerDI5060-8 (Type C)	2,500/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to [http://www.diodes.com/product\\_compliance\\_definitions.html](http://www.diodes.com/product_compliance_definitions.html).
  5. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

## Marking Information



**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V <sub>DSS</sub>	60	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 7)	I <sub>D</sub>	T <sub>C</sub> = +25°C	33.2
		T <sub>C</sub> = +100°C	23.7
Continuous Drain Current (Note 6)	I <sub>D</sub>	T <sub>A</sub> = +25°C	9.2
		T <sub>A</sub> = +100°C	6.5
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	50	A
Maximum Continuous Body Diode Forward Current (Note 6)	I <sub>S</sub>	31	A
Pulsed Body Diode Forward Current (Note 6)	I <sub>SM</sub>	50	A
Avalanche Current, L = 0.1mH	I <sub>AS</sub>	15.3	A
Avalanche Energy, L = 0.1mH	E <sub>AS</sub>	11.7	mJ

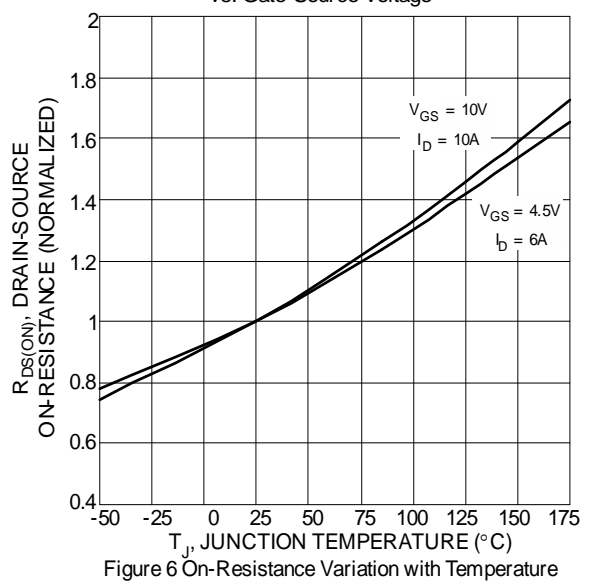
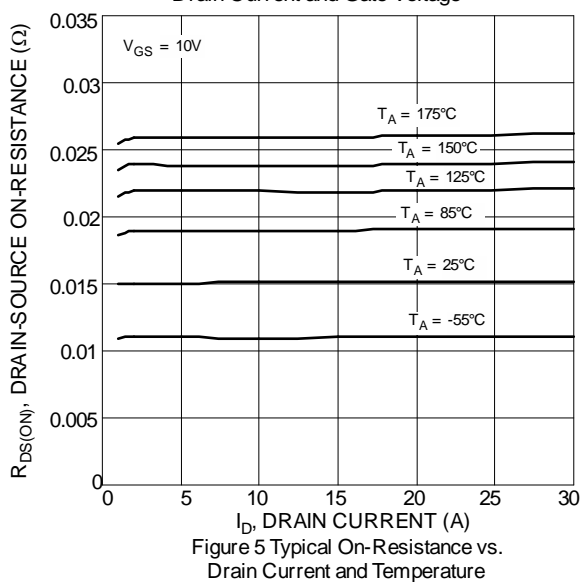
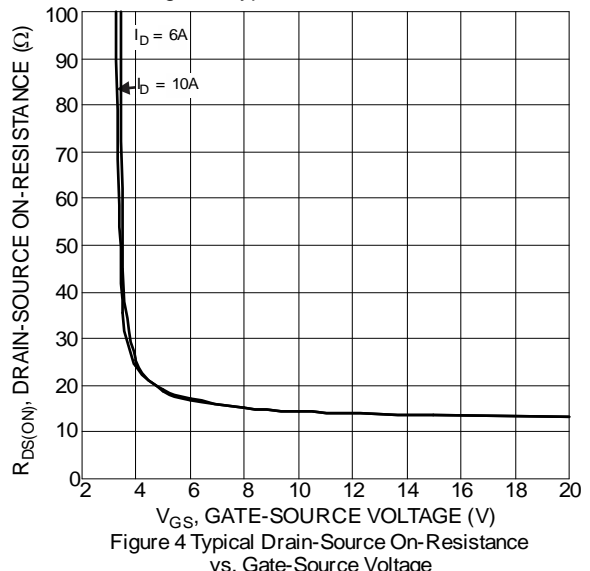
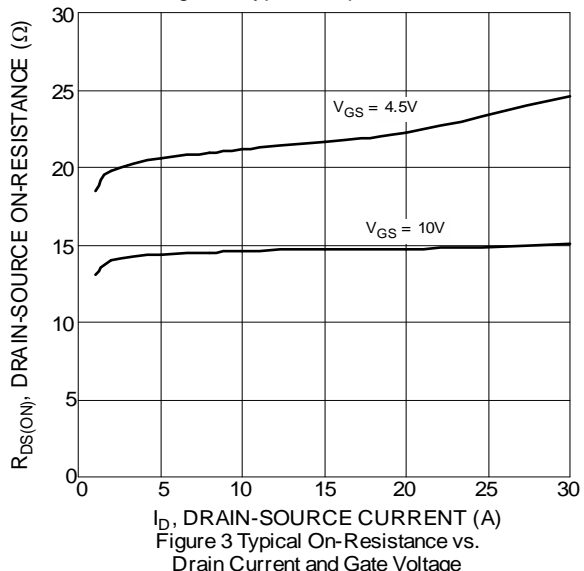
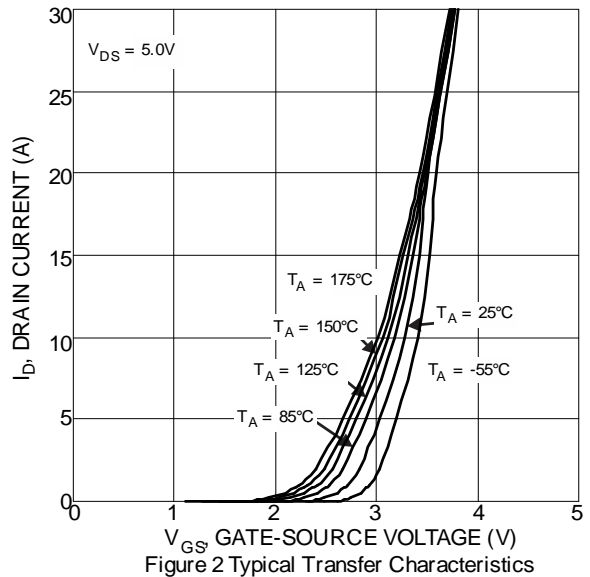
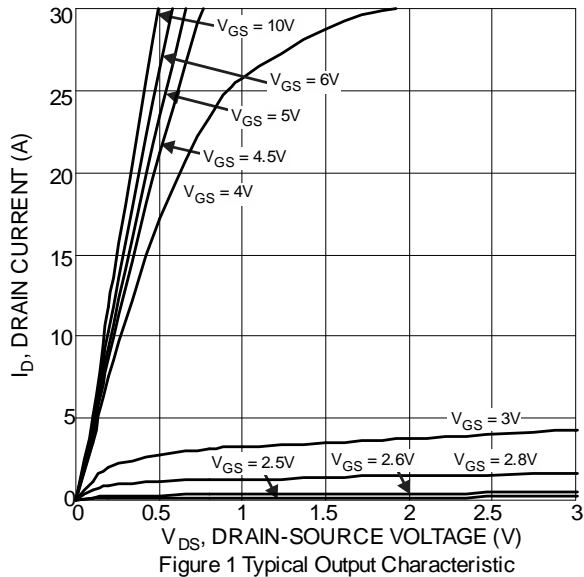
**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	P <sub>D</sub>	2.5	W
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>ΘJA</sub>	58	°C/W
Total Power Dissipation (Note 7)	P <sub>D</sub>	37.5	W
Thermal Resistance, Junction to Case (Note 7)	R <sub>ΘJC</sub>	4	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +175	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b> (Note 8)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	µA	V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS</b> (Note 8)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	—	2.5	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250µA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	14.5	19	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 10A
		—	20.9	28		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 6A
Diode Forward Voltage	V <sub>SD</sub>	—	0.7	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A
<b>DYNAMIC CHARACTERISTICS</b> (Note 9)						
Input Capacitance	C <sub>ISS</sub>	—	864	—	pF	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1MHz
Output Capacitance	C <sub>OSS</sub>	—	282	—	pF	
Reverse Transfer Capacitance	C <sub>RSS</sub>	—	27	—	pF	
Gate Resistance	R <sub>G</sub>	—	1.3	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>G</sub>	—	8.4	—	nC	V <sub>DS</sub> = 30V, I <sub>D</sub> = 10A
Total Gate Charge (V <sub>GS</sub> = 10V)	Q <sub>G</sub>	—	17	—	nC	
Gate-Source Charge	Q <sub>GS</sub>	—	3.1	—	nC	
Gate-Drain Charge	Q <sub>GD</sub>	—	4.3	—	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	—	3.4	—	ns	V <sub>DD</sub> = 30V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 10A, R <sub>G</sub> = 6Ω
Turn-On Rise Time	t <sub>R</sub>	—	5.2	—	ns	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	13	—	ns	
Turn-Off Fall Time	t <sub>F</sub>	—	7	—	ns	
Body Diode Reverse Recovery Time	t <sub>RR</sub>	—	22	—	ns	I <sub>F</sub> = 10A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	—	11	—	nC	

- Notes:
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1 inch square copper plate.
  7. Thermal resistance from junction to soldering point (on the exposed drain pad).
  8. Short duration pulse test used to minimize self-heating effect.
  9. Guaranteed by design. Not subject to product testing.



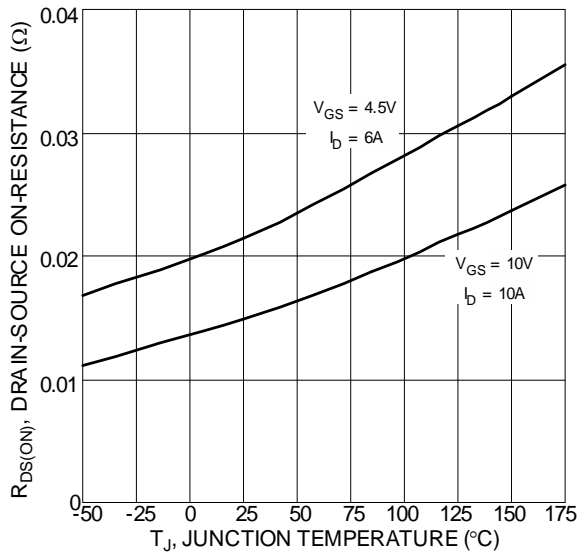


Figure 7 On-Resistance Variation with Temperature

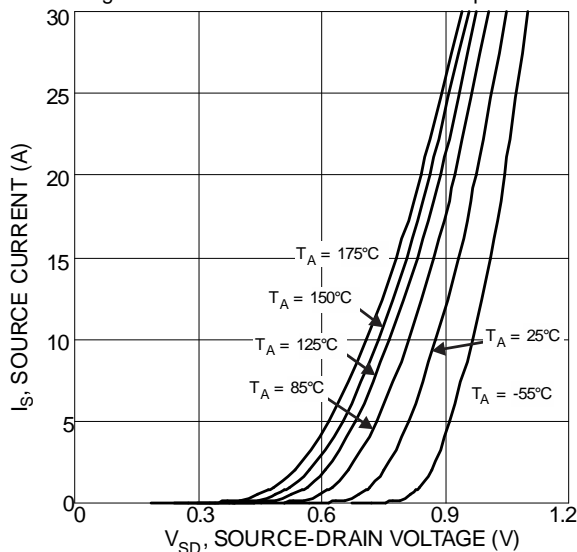


Figure 9 Diode Forward Voltage vs. Current

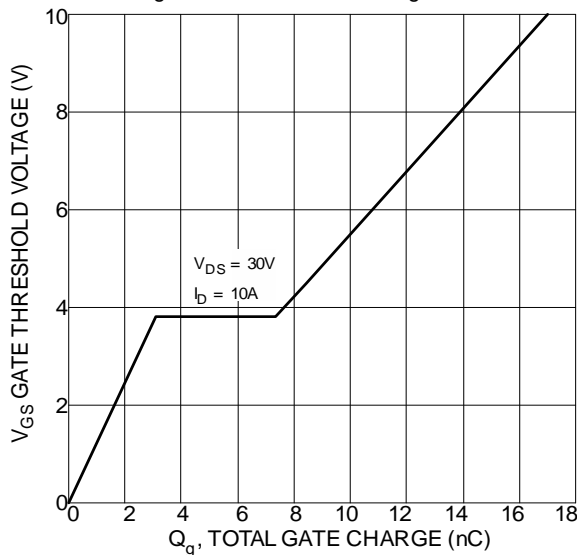


Figure 11 Gate Charge

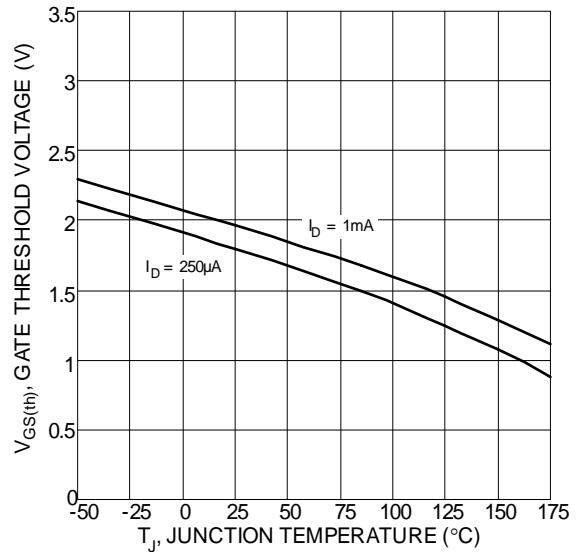


Figure 8 Gate Threshold Variation vs. Ambient Temperature

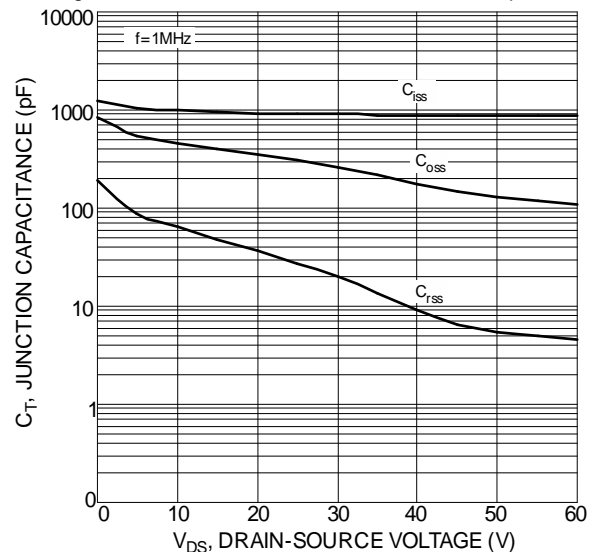


Figure 10 Typical Junction Capacitance

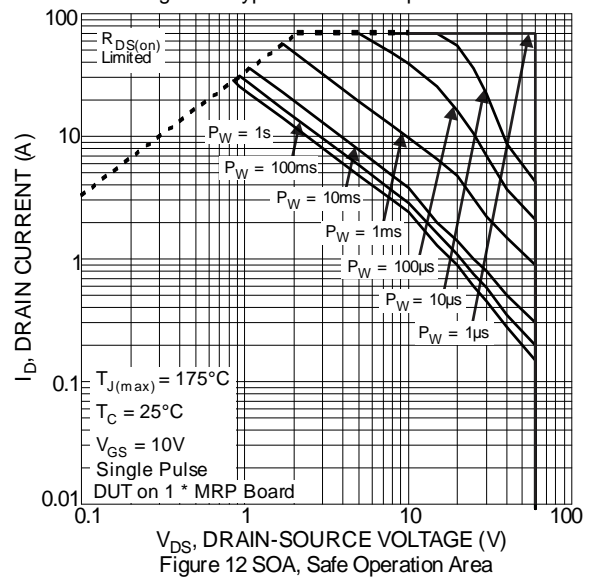
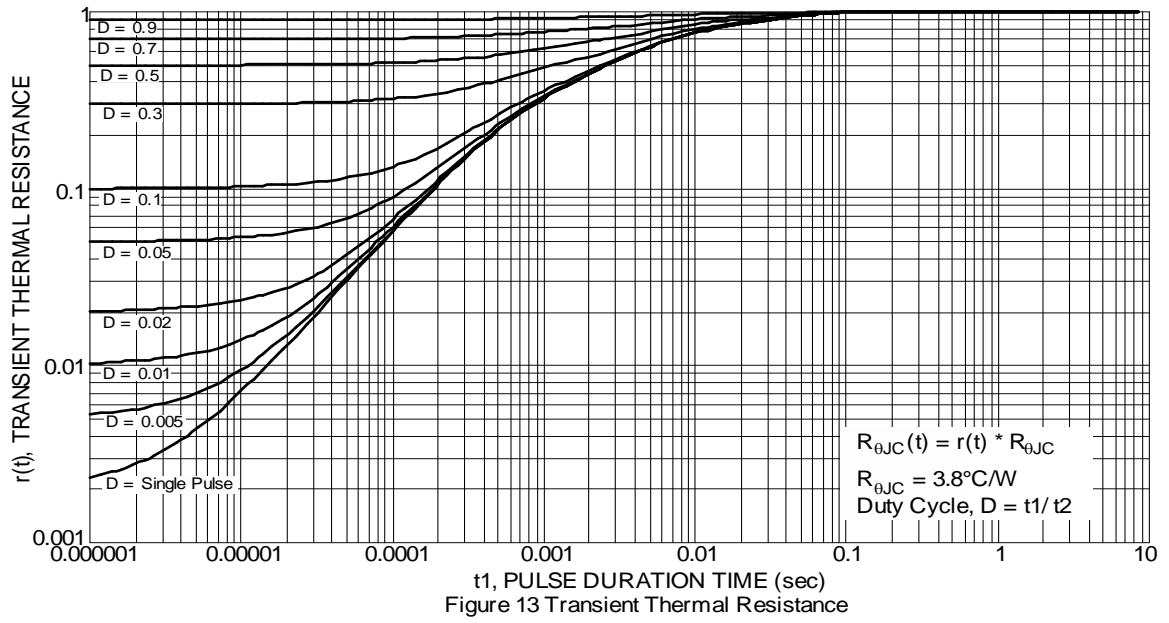


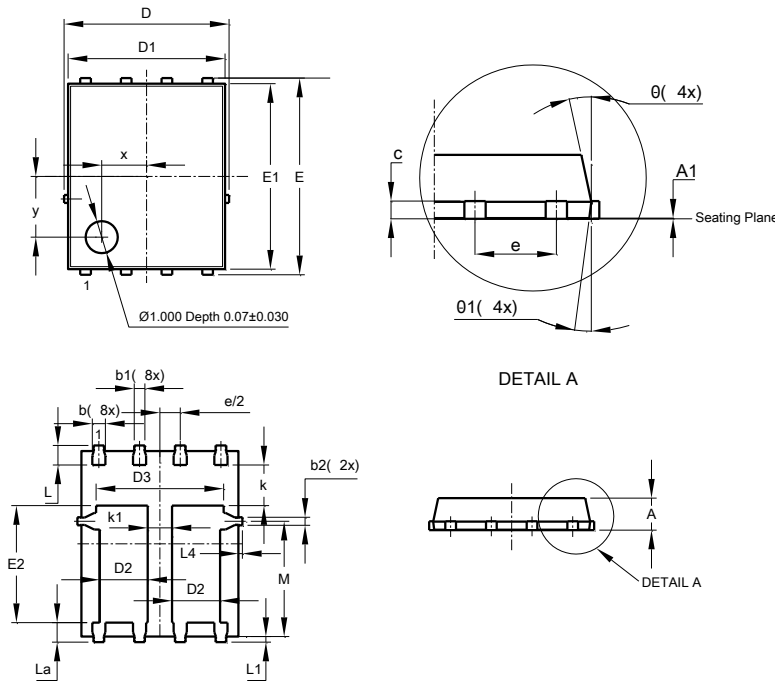
Figure 12 SOA, Safe Operation Area



**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**PowerDI5060-8 (Type C)**

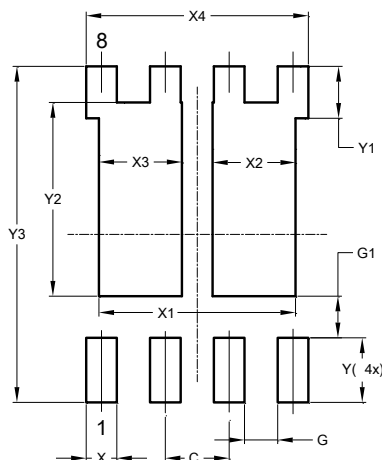


PowerDI5060-8 (Type C)			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0	0.05	0.02
b	0.33	0.51	0.41
b1	0.300	0.366	0.333
b2	0.20	0.35	0.25
c	0.23	0.33	0.277
D	5.15 BSC		
D1	4.85	4.95	4.90
D2	1.40	1.60	1.50
D3	-	-	3.98
E	6.15 BSC		
E1	5.75	5.85	5.80
E2	3.56	3.76	3.66
e	1.27BSC		
k	-	-	1.27
k1	0.56	-	-
L	0.51	0.71	0.61
La	0.51	0.71	0.61
L1	0.05	0.20	0.175
L4	-	-	0.125
M	3.50	3.71	3.605
x	-	-	1.400
y	-	-	1.900
θ	10°	12°	11°
θ1	6°	8°	7°
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**PowerDI5060-8 (Type C)**



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	3.910
X2	1.650
X3	1.650
X4	4.420
Y	1.270
Y1	1.020
Y2	3.810
Y3	6.610

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