



#### DMWSH120H90SM4

#### 1200V N-CHANNEL SILICON CARBIDE **POWER MOSFET**

### **Product Summary**

BV <sub>DSS</sub>	RDS(ON) Max	I <sub>D</sub> T <sub>C</sub> = +25°C
1200V	97.5mΩ $@V_{GS} = 15V$	40A

### **Description and Applications**

This SiC MOSFET is designed to minimize the on-state resistance yet maintain superior switching performance, making it ideal for highefficiency power-management applications.

- EV high-power DC-DC converters
- EV charging systems
- AC-DC traction inverters
- Automotive motor drivers

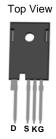
### Low Input Capacitance

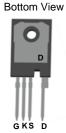
Low On-Resistance

**Features and Benefits** 

- High BV<sub>DSS</sub> Rating for Power Application
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- An automotive-compliant part is available under separate datasheet (DMWSH120H90SM4Q)

# TO247-4 Standard

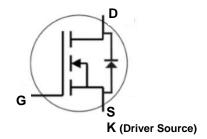




Pin Configuration

#### **Mechanical Data**

- Package: TO247-4
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 6.6 grams (Approximate)



Internal Schematic

#### **Ordering Information** (Note 4)

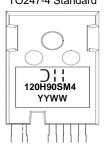
Part Number	Port Number Poekage		Packing			
Part Number	Package	Qty. Carrier				
DMWSH120H90SM4	TO247-4 Standard	30 Pieces	Tube			

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

### **Marking Information**

TO247-4 Standard



D : : = Manufacturer's Marking 120H90SM4 = Product Type Marking Code YYWW or YYWW = Date Code Marking  $\overline{YY}$  or  $\overline{YY}$  = Last Two Digits of Year (ex. 24 = 2024) WW or WW = Week Code (01 to 53)



## **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	1200	V	
Gate-Source Voltage (Dynamic)	Vgss	+19/-8	V	
Gate-Source Voltage (Static)	Vgss	+15/-4	V	
Continuous Drain Current (Notes 5, 9)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	I <sub>D</sub>	40.0 28.3	А
Continuous Diode Forward Current (Note 5)	Is	43	А	
Pulsed Source Current (Pulse Width tp Limited by TJ Max) (Note 5)	Isм	88	А	
Pulsed Drain Current (Pulse Width t <sub>P</sub> Limited by T <sub>J Max</sub> ) (Note 5)	I <sub>DM</sub>	88	А	

#### Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Total Power Dissipation (Note 5)	Tc = +25°C	Do	235	W	
Total Power Dissipation (Note 5)	Tc = +100°C	PD	117	1 vv	
Thermal Resistance, Junction to Ambient (Note 6)	Reja	30	°C/W		
Thermal Resistance, Junction to Case (Note 5)	Rejc	0.64	C/VV		
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C	

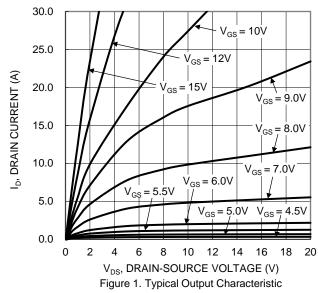
#### Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BVDSS	1200	_	_	V	$V_{GS} = 0$ , $I_{D} = 100 \mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	100	μA	V <sub>DS</sub> = 1200V, V <sub>GS</sub> = 0	
Gate-Source Leakage	Igss	_	_	±200	nA	$V_{GS} = +15/-4V, V_{DS} = 0$	
ON CHARACTERISTICS (Note 8)	ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	Vgs(TH)	1.7	2.5	3.5	V	$V_{DS} = V_{GS}$ , $I_D = 5mA$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	-	75	97.5	mΩ	$V_{GS} = 15V, I_D = 20A$	
Diode Forward Voltage	VsD	_	4.3	_	V	V <sub>G</sub> S = -4V, I <sub>S</sub> = 10A	
Transconductance	gfs	_	4.7	_	S	VDS = 20V, ID = 20A	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C <sub>iss</sub>	_	1112	_			
Output Capacitance	Coss	_	64	_	pF	$V_{GS} = 0$ , $V_{DS} = 1000V$ , $V_{AC} = 25mV$ , $f = 1MHz$	
Reverse Transfer Capacitance	Crss	_	4.42	_			
Coss Stored Energy	Eoss	_	39.5	_	μJ	]	
Turn-On Switching Energy (Body Diode Forward)	Eon	_	172	_	μJ	$V_{GS} = -4V/+15V$ , $V_{DS} = 800V$ ,	
Turn-Off Switching Energy (Body Diode Forward)	Eoff	_	71	_	μυ	$Rg = 5\Omega$ , $I_D = 20A$ , $L = 156\mu H$	
Gate Resistance	Rg	_	2.5	_	Ω	V <sub>AC</sub> = 100mV, f = 1MHz	
Total Gate Charge	Qg	_	51.1	_		V <sub>GS</sub> = -4V/+15V, V <sub>DS</sub> = 800V, I <sub>D</sub> = 20A	
Gate-Source Charge	Qgs	_	15.9	_	nC		
Gate-Drain Charge	Qgd	_	18.8	_		ID = 20A	
Turn-On Delay Time	td(on)	_	9.1	_		$V_{GS} = -4V/+15V$ , $V_{DD} = 800V$ , $Rg = 5\Omega$ , $I_{D} = 20A$ , $I_{D} = 100$	
Turn-On Rise Time	t <sub>R</sub>	_	21.3	_			
Turn-Off Delay Time	tD(OFF)	_	17.4	_	ns		
Turn-Off Fall Time	tF	_	6.4	_			
Body Diode Reverse-Recovery Time	trr	_	12.1	_	ns	V 4V V 900V	
Body Diode Reverse-Recovery Charge	Q <sub>RR</sub>	-	145	_	nC	$V_{GS} = -4V$ , $V_{DS} = 800V$ ,	
Body Diode Reverse-Recovery Current	I <sub>RRM</sub>	_	20.7	_	Α	IF = 20A, di/dt = 3600A/µs	

5. Device mounted on an infinite heatsink. Notes:

- 6. Device mounted on FR-4 substrate PC board, 2oz. copper, with minimum recommended pad layout.
- Guaranteed by design. Not subject to production testing.
   Short duration pulse test used to minimize self-heating effect.
- 9. Drain current limited by maximum junction temperature.





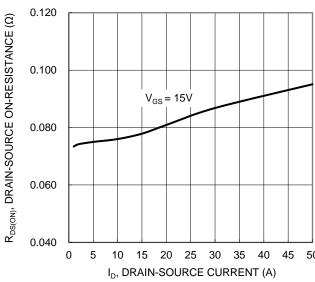


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

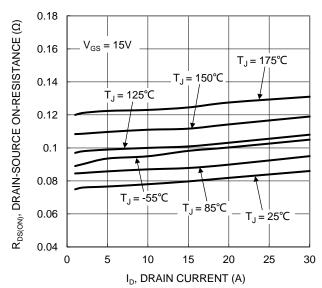


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

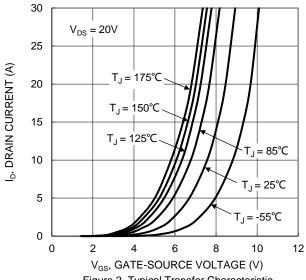


Figure 2. Typical Transfer Characteristic

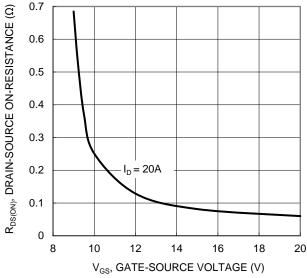


Figure 4. Typical Transfer Characteristic

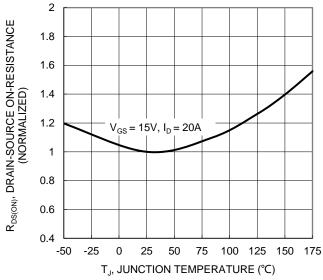


Figure 6. On-Resistance Variation with Temperature





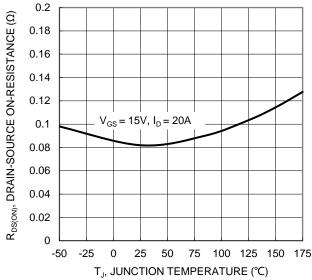


Figure 7. On-Resistance Variation with Temperature

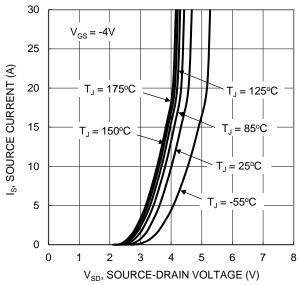


Figure 9. Diode Forward Voltage vs. Current

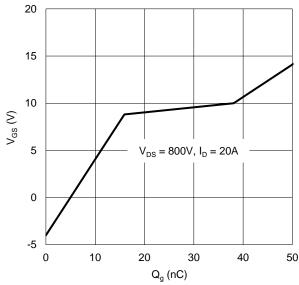


Figure 11. Gate Charge

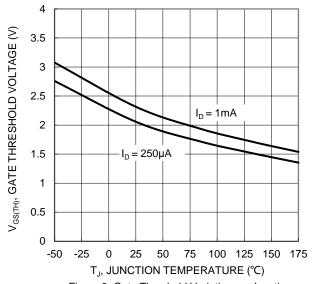


Figure 8. Gate Threshold Variation vs. Junction Temperature

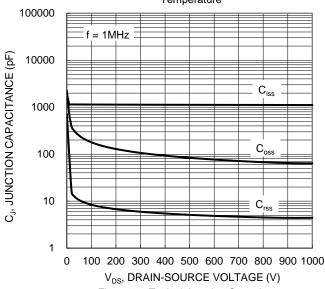


Figure 10. Typical Junction Capacitance

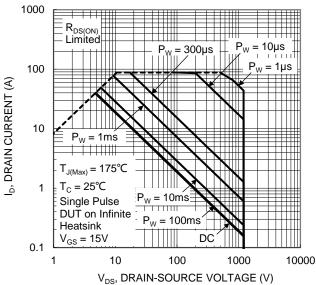


Figure 12. SOA, Safe Operation Area



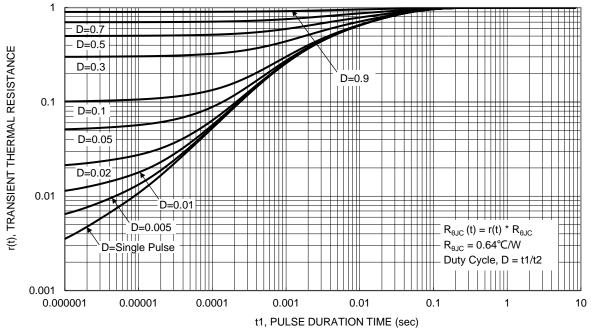


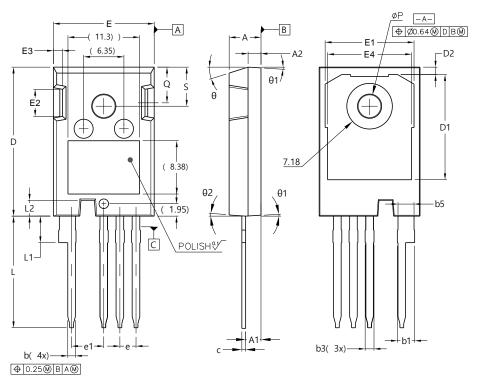
Figure 13. Transient Thermal Resistance



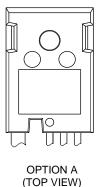
### **Package Outline Dimensions**

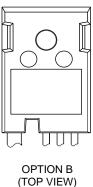
Please see http://www.diodes.com/package-outlines.html for the latest version.

#### TO247-4 Standard



TO247-4 Standard				
Dim	Min	Max		
Α	4.83	5.21		
A1	2.29	2.54		
A2	1.91	2.16		
b	1.07	1.33		
b1	2.39	2.94		
b3	1.07	1.60		
b5	2.39	2.69		
С	0.55	0.68		
D	23.30	23.60		
D1	16.25	17.65		
D2	0.95	1.25		
Ε	15.75	16.30		
E1	13.10	14.15		
E2	3.68	5.10		
E3	1.00	1.90		
E4	12.38	13.43		
е	2.54 BSC			
e1	5.08 BSC			
L L1	17.31	17.82		
L1	3.97	4.37		
L2	2.35	2.65		
ØΡ	3.51	3.65		
Q	5.49	6.00		
S	6.04	6.30		
θ	17.5°- 20° REF			
θ1	3.5°- 5° REF			
θ2	4°- 5° REF			
All Dimensions in mm				







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C3M0045065K E3M0120090J C3M0065090J-TR C3M0120100J C3M0075120J DMWS120H100SM4 DMWSH120H28SM4
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G3R20MT12K G3R20MT12N G3R20MT17K G3R20MT17N G3R30MT12J-TR G3R30MT12K G3R350MT12D G3R40MT12D
G3R40MT12J