

NOT RECOMMENDED FOR NEW DESIGN **USE FZT788B**

DPLS315E



LOW V_{CE(SAT)} PNP SURFACE MOUNT TRANSISTOR

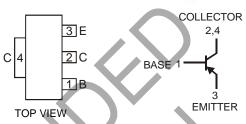
Features

- **Epitaxial Planar Die Construction**
- Low Collector-Emitter Saturation Resistance $R_{CE(SAT)} = 70 \text{m}\Omega$ at 3A
- High DC Current Gain hFE > 300 at IC = 2A
- Complementary NPN Type Available (DNLS412E)
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification Applications
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

Mechanical Data

- Case: SOT-223
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Copper Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.112 grams (approximate)





Schematic and Pin Configuration

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit
Collector-Base Voltage		10	V _{CBO}	-15	V
Collector-Emitter Voltage			V_{CEO}	-15	V
Emitter-Base Voltage		-	V _{EBO}	-5	V
Continuous Collector Current			lc	-3	А
Peak Pulse Current	_ \		I _{CM}	-8	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation @ T _A = 25°C (Note 3)	PD	1	W
Thermal Resistance, Junction to Ambient Air (Note 1) @T _A = 25°C	$R_{\theta JA}$	125	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

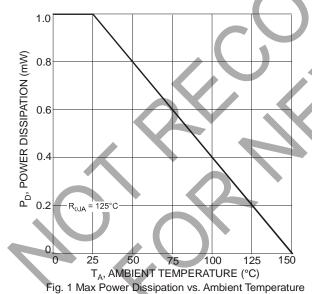
Notes:

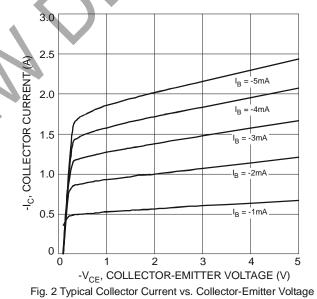
- 1. No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
- 3. Device mounted on FR-4 PCB, pad layout as shown on page 4 or in Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

Electrical Characteristics $@T_A = 25^{\circ}C$ unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 4)							
Collector-Base Breakdown Voltage	V _{(BR)CBO}	-15	_	_	V	$I_C = -100 \mu A, I_E = 0$	
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	-15		_	V	$I_C = -10 \text{mA}, I_B = 0$	
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	-5		_	V	$I_E = -100 \mu A, I_C = 0$	
Collector Cutoff Current	I _{CBO}	_	_	-0.1	μΑ	$V_{CB} = -10V, I_{E} = 0$	
Emitter Cutoff Current	I _{EBO}	_	_	-0.1	μΑ	$V_{EB} = -4V, I_C = 0$	
ON CHARACTERISTICS (Note 4)							
Collector-Emitter Saturation Voltage	V _{CE(SAT)}		-0.08 -0.12 -0.22 -0.21	-0.15 -0.25 -0.45 -0.5	٧	I _C = -0.5A, I _B = -2.5mA I _C = -1A, I _B = -5mA I _C = -2A, I _B = -10mA I _C = -3A, I _B = -50mA	
Base-Emitter Saturation Voltage	V _{BE(SAT)}		_	-0.9	V	$I_C = -1A$, $I_B = -5mA$	
Base-Emitter Turn-On Voltage	V _{BE(ON)}		-0.75	_	V	$V_{CE} = -2V, I_{C} = -1A$	
DC Current Gain	h _{FE}	500 400 300 150		1500		V _{CE} = -2V, I _C = -10mA V _{CE} = -2V, I _C = -1A V _{CE} = -2V, I _C = -2A V _{CE} = -2V, I _C = -6A	
AC CHARACTERISTICS							
Transition Frequency	f⊤	100		=	MHz	$V_{CE} = -5V$, $I_{C} = -50$ mA, $f = 50$ MHz	
Input Capacitance	C _{ibo}	_ 4	245		MHz	V _{EB} = -0.5V, f = 1MHz	
Output Capacitance	C _{obo}	-	45	<u></u>	pF	V _{CB} = -10V, f = 1MHz	
Switching Times	t _{on}		35 200		ns ns	$V_{CC} = -10V$, $I_{C} = -500$ mA, $I_{B1} = -I_{B2} = -50$ mA	

4. Pulse Test: Pulse width ≤300μs. Duty cycle ≤2.0%. Notes:





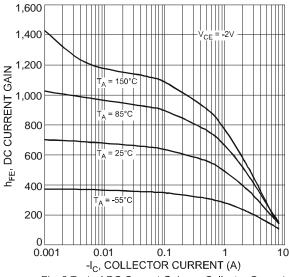


Fig. 3 Typical DC Current Gain vs. Collector Current

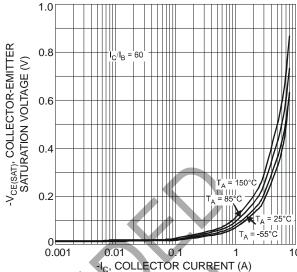


Fig. 4 Typical Collector-Emitter Saturation Voltage vs. Collector Current

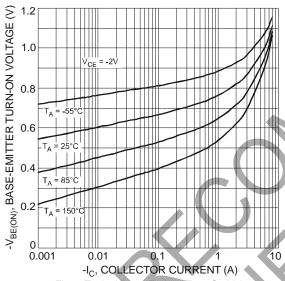


Fig. 5 Typical Base-Emitter Turn-On Voltage vs. Collector Current

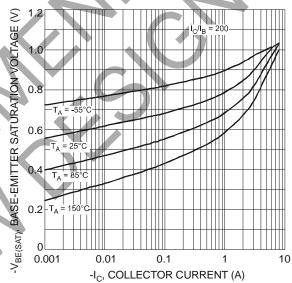


Fig. 6 Typical Base-Emitter Saturation Voltage

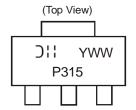
vs. Collector Current

Ordering Information (Note 5)

Device	Packaging	Shipping
DPLS315E-13	SOT-223	2500/Tape & Reel

Notes: 5. For packaging details, please go to our website at http://www.diodes.com/ap02007.pdf.

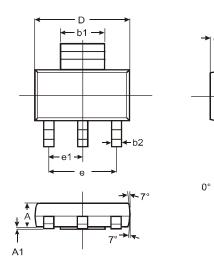
Marking Information



P315 = Product Type Marking Code YWW = Date Code Marking Y = Last digit of year ex: 7 = 2007 WW = Week code 01 - 52

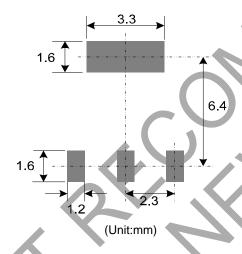


Package Outline Dimensions



SOT-223					
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
A 1	0.010	0.15	0.05		
b1	2.90	3.10	3.00		
b2	0.60	0.80	0.70		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
E	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
е	<u>_</u>	/ -\	4.60		
e1	Y		2.30		
٦	0.85	1.05	0.95		
ø	0.84	0.94	0.89		
All Dimensions in mm					

Suggested Pad Layout



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