- **Epitaxial Planar Die Construction**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen, Antimony and Beryllium Free. "Green" Device
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

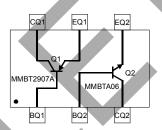
Sub-Component P/N	Reference	Device Type
MMBT2907A_DIE	Q1	PNP Transistor
MMBTA06_DIE	Q2	NPN Transistor

Mechanical Data

- Case: SOT-363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: See Schematic & Pin Configuration
- Terminals: Finish—Matte Tin Annealed over Alloy 42 Lead-Frame. Solderable per MIL-STD-202, Method 208 (3)
- Marking Information: See Page 6
- Ordering Information: See Page 6
- Weight: 0.016 grams (Approximate)



Existing Product



Device Schematic

Please support the datasheet update to Discontinued status 4 datasheet such as background of the datasheet.

Ordering Information (Note 4)

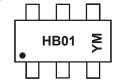
Part Number	Compliance	Case	Packaging
HBDM60V600W-7	Standard	SOT-363	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen, Antimony and Beryllium-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI), <1000ppm antimony compounds and <1000ppm Beryllium.
- 4. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



HB01 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: G = 2019) M = Month (ex: 9 = September)

Date Code Key

Year	2006	2007	 2019	2020	2021	2022	2023	2024	2025
Code	Т	U	 G	Н	1	J	K	L	М

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings: Total Device @TA = 25°C (unless otherwise specified)

Characteristic	Symbol	Value	Unit	
Operating and Storage Temperature Range	T_{OP},T_{stg}	-55 to +150	°C	

Thermal Characteristics: Total Device

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	200	mW
Thermal Resistance, Junction to Ambient Air (Note 5)	R _{OJA}	625	°C/W

Maximum Ratings: Sub-Component Devices @TA = 25°C unless otherwise specified

Characteristic	Symbol	Q1-PNP Transistor (MMBT2907A)	Q2-NPN Transistor (MMBTA06)	Unit
Collector-Base Voltage	V_{CBO}	-60	80	V
Collector-Emitter Voltage	V_{CEO}	-60	65	V
Emitter-Base Voltage	V_{EBO}	-5.5	6	V
Collector Current - Continuous (Note 5)	Ic	-600	500	mA

Note: 5. Device mounted on FR-4 substrate printed circuit board with 1 inch square 2oz copper pad area

Electrical Characteristics: PNP (MMBT2907A) Transistor (Q1) @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)					
Collector-Base Breakdown Voltage	V _{(BR)CBO}	-60		V	$I_C = -10\mu A, I_E = 0$
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	-60	ı	V	$I_C = -10 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	-5.5	ı	V	$I_E = -10\mu A, I_C = 0$
Collector Cutoff Current	I _{CBO}	-	-10	nA	$V_{CB} = -50V, I_{E} = 0$
Collector Cutoff Current	I _{CEX}	_	-50	nA	$V_{CE} = -30V, V_{EB(OFF)} = -0.5V$
Base Cutoff Current	I_{BL}		-50	nA	$V_{CE} = -30V, V_{EB(OFF)} = -0.5V$
ON CHARACTERISTICS (Note 6)					
		100	_	_	$I_C = -100\mu A$, $V_{CE} = -10V$
		100	_		$I_C = -1.0 \text{mA}, V_{CE} = -10 \text{V}$
DC Current Gain	h _{FE}	100	_		$I_C = -10 \text{mA}, V_{CE} = -10 \text{V}$
		100	300		$I_C = -150 \text{mA}, V_{CE} = -10 \text{V}$
		50	_	_	$I_C = -500 \text{mA}, V_{CE} = -10 \text{V}$
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	_	-0.3	V	$I_C = -150 \text{mA}, I_B = -15 \text{mA}$
Obligator Ethilian Cataration Vallage	V CE(SAT)		-0.5	•	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$
Base-Emitter Saturation Voltage	V _{BE(SAT)}	_	-0.95	V	$I_C = -150 \text{mA}, I_B = -15 \text{mA}$
0			-1.3	•	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$
SMALL SIGNAL CHARACTERISTICS				1	
Current Gain-Bandwidth Product	f⊤	100	-	MHz	$V_{CE} = -2.0V, I_{C} = -10mA,$ f = 100MHz
SWITCHING CHARACTERISTICS					
Turn-On Time	t _{on}	_	45	ns	V _{CE} = -30V, I _C = -150mA,
Delay Time	t _d	_	10	ns	I _{B1} = -15mA
Rise Time	tr	_	40	ns	IBI — - IOIIIA
Turn-Off Time	t _{off}	_	100	ns	V _{CC} = -6.0V, I _C = -150mA,
Storage Time	ts	_	80	ns	$I_{B1} = I_{B2} = -15 \text{mA}$
Fall Time	t _f		30	ns	181 - 182 IOITIA



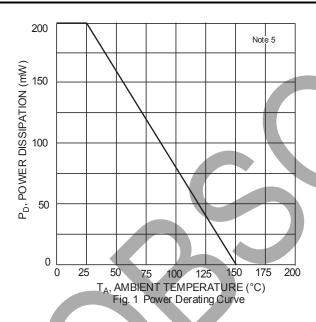
Electrical Characteristics: NPN (MMBTA06) Transistor (Q2) @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Collector-Base Breakdown Voltage	V _{(BR)CBO}	80	_	_	V	$I_C = 100 \mu A, I_E = 0$
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	65	_	_	V	$I_{C} = 1 \text{mA}, I_{B} = 0$
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	6	_	_	V	$I_E = 100 \mu A, I_C = 0$
Collector-Base Cutoff Current	I _{CBO}	_	_	100	nA	$V_{CB} = 80V, I_{E} = 0$
Collector Cutoff Current	I _{CES}	_	_	100	nA	$V_{CE} = 90V, V_{BE} = 0$
Emitter-Base Cutoff Current	I _{EBO}	_	_	100	nA	$V_{EB} = 5V, I_{C} = 0$
ON CHARACTERISTICS (Note 6)						
DC Current Gain	b	250	_	_	_	$V_{CE} = 1V$, $I_{C} = 10mA$
DC Current Gain	h _{FE}	100	_	_	_	V _{CE} = 1V, I _C = 100mA
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	_	0.2	0.4	V	I _C = 100mA, I _B = 10mA
Base-Emitter Turn-on Voltage	V _{BE(ON)}	0.7	0.75	0.8	V	$V_{CE} = 1V, I_{C} = 100mA$
Base-Emitter Saturation Voltage	V _{BE(SAT)}	_	_	0.95	V	I _C = 100mA, I _B = 5mA
SMALL SIGNAL CHARACTERISTICS						
Current Gain-Bandwidth Product	f _T	100		_	MHz	$V_{CE} = 20V, I_{C} = 10mA,$ f = 100MHz

Notes:

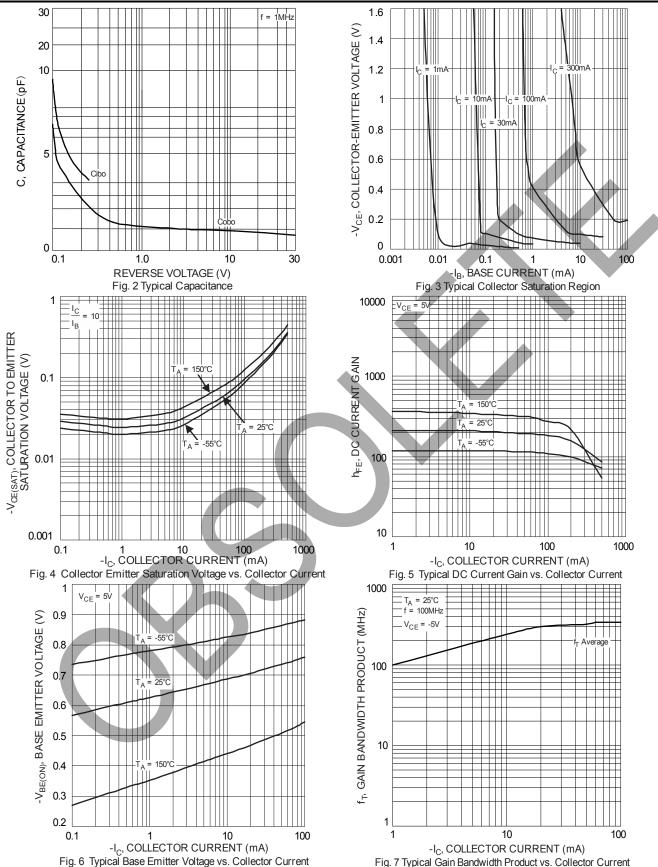
6. Short duration pulse test used to minimize self-heating effect.

Typical Characteristics @TA = 25°C unless otherwise specified





PNP (MMBT2907A) Transistor (Q1) Plots





NPN (MMBTA06) Transistor (Q2) Plots

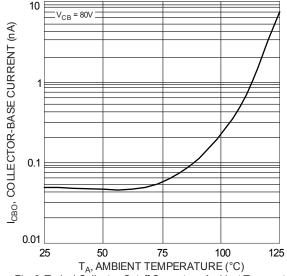


Fig. 8 Typical Collector-Cutoff Current vs. Ambient Temperature

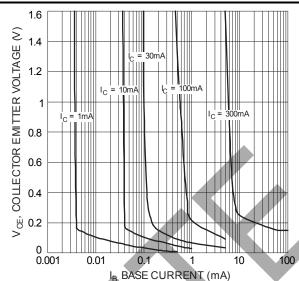
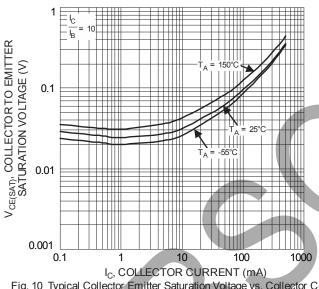
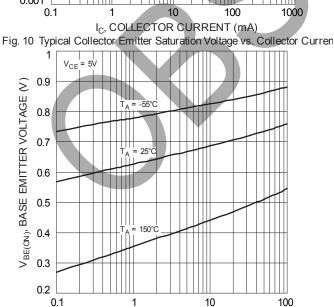


Fig. 9 Typical Collector Saturation Region





 $I_{\rm C}$, COLLECTOR CURRENT (mA) Fig. 12 Typical Base Emitter Voltage vs. Collector Current

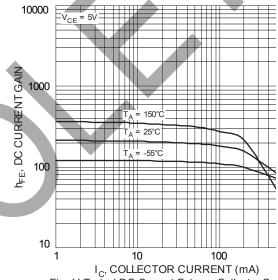


Fig. 11 Typical DC Current Gain vs. Collector Cu

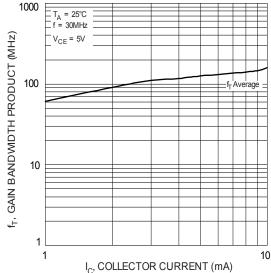
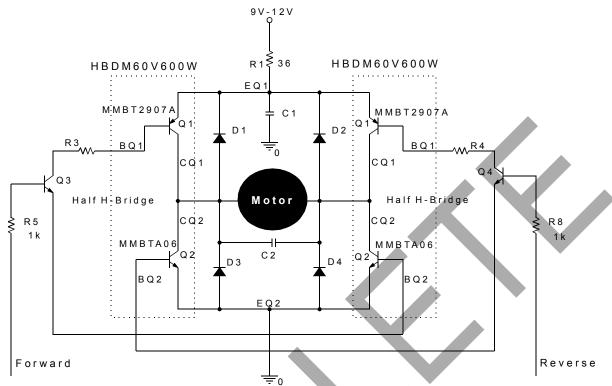


Fig. 13 Typical Gain Bandwidth Product vs. Collector Current

V_{BE(ON)}, BASE EMITTER VOLTAGE (V)



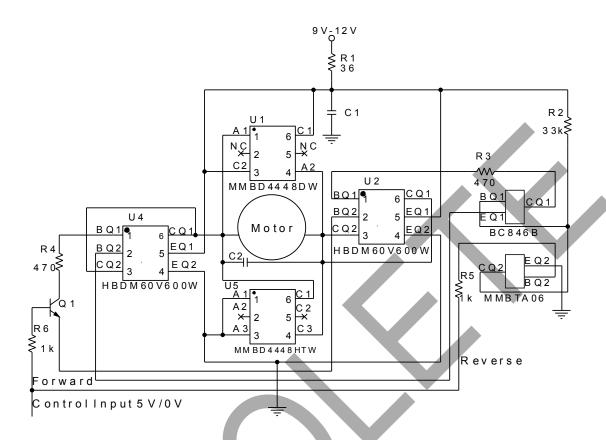
Current Schematic with Application Example



Note: D1, D2, D3, D4: Switching Diodes (MMBD4448) Q3, Q4: NPN Transistors (MMBTA06)



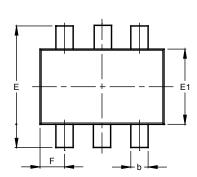
Application Example Schematic (with Package Pinouts)

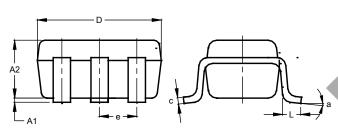




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.





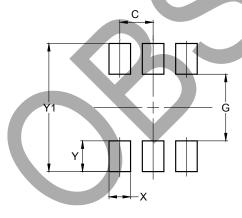
	SO	T363	
Dim	Min	Max	Тур
A1	0.00	0.10	0.05
A2	0.90	1.00	0.95
b	0.10	0.30	0.25
C	0.10	0.22	0.11
D	1.80	2.20	2.15
Е	2.00	2.20	2.10
E1	1.15	1.35	1.30
е	C	.650 E	SC
F	0.40	0.45	0.425
L	0.25	0.40	0.30
а	0°	8°	
All I	Dimen	sions	in mm

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363

SOT363



Dimensions	Value
Dillielisions	(in mm)
С	0.650
G	1.300
Х	0.420
Υ	0.600
Y1	2.500



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