



DUAL PNP SMALL SIGNAL SURFACE MOUNT TRANSISTOR

Features

- **Epitaxial Planar Die Construction**
- Small Surface Mount Package
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

Mechanical Data

- Case: SOT-26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed Over Copper Leadframe. • Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3

Device Schematic

Weight: 0.016 grams (approximate)



Top View

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-60	V
Collector-Emitter Voltage	V _{CEO}	-50	V
Emitter-Base Voltage	V _{EBO}	-5.0	V
Continuous Collector Current	Ic	-500	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @T _A = 25°C	PD	300	mW
Thermal Resistance, Junction to Ambient Air (Note 3) @T _A = 25°C	R _{0JA}	417	°C /W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

			-			T (0)
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)					-	
Collector-Base Breakdown Voltage	V _{(BR)CBO}	-60	_	_	V	I _C = -100μA
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	-50	_		V	I _C = -1.0mA
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	-5.0		—	V	I _E = -100μA
Collector Cutoff Current	ICBO			-0.1	μA	V _{CB} = -30V
Emitter Cutoff Current	I _{EBO}		_	-0.1	μA	$V_{EB} = -4.0V$
ON CHARACTERISTICS (Note 4)						
DC Current Gain	h _{FE}	120	_	390	—	V _{CE} = -3.0V, I _C = -100mA
Collector-Emitter Saturation Voltage (Note 3)	V _{CE(SAT)}		_	-0.6	V	I _C = -500mA, I _B = -50mA
SMALL SIGNAL CHARACTERISTICS						
Gain Bandwidth Product	f⊤	—	200	—	MHz	V _{CE} = -5V, I _E = 20mA, f = 100MHz
Output Capacitance	Cob	_	7	_	pF	V _{CB} = -10V, I _E = 0, f = 1MHz

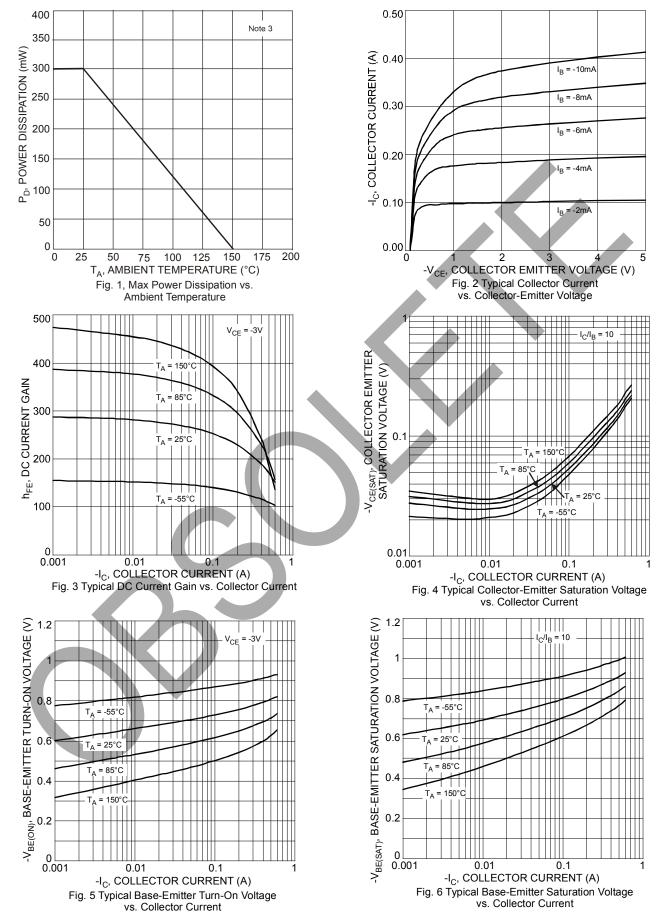
Notes: 1. No purposefully added lead.

2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

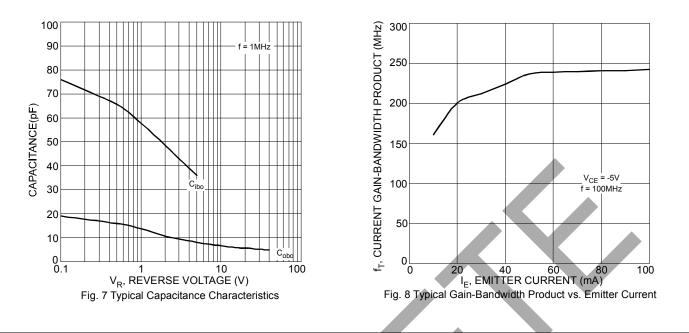
Device mounted on FR-4 PCB; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on page 4 or on our 3. website at http://www.diodes.com/datasheets/ap02001.pdf.

4. Short duration pulse test used to minimize self-heating effect.

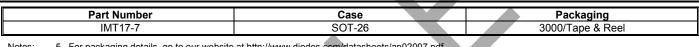


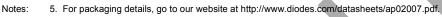


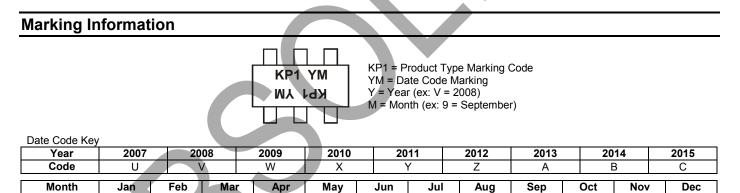




Ordering Information (Note 5)







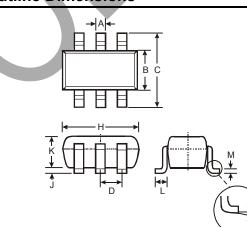
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Package Outline Dimensions

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Code



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4

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SOT-26				
Dim	Min	Max	Тур	
Α	0.35	0.50	0.38	
в	1.50	1.70	1.60	
С	2.70	3.00	2.80	
D			0.95	
Н	2.90	3.10	3.00	
ر	0.013	0.10	0.05	
κ	1.00	1.30	1.10	
L	0.35	0.55	0.40	
М	0.10	0.20	0.15	
α	0°	8°		
All Dimensions in mm				

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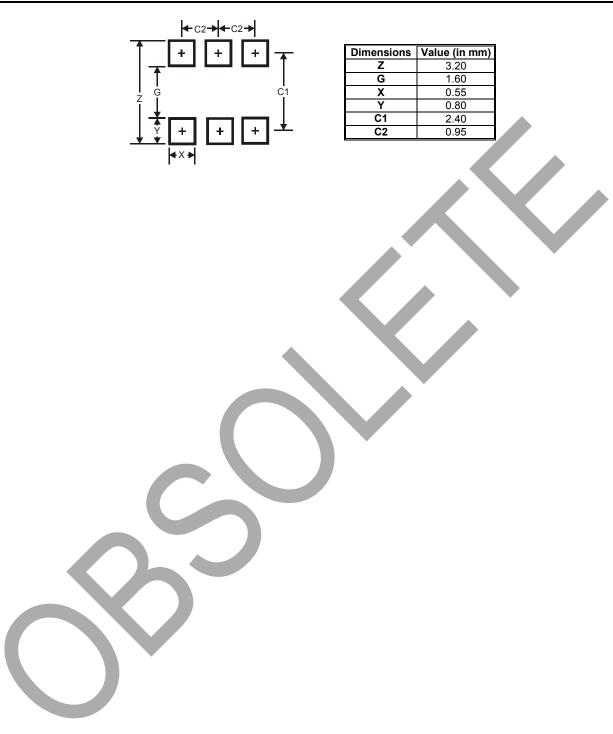
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Suggested Pad Layout





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