

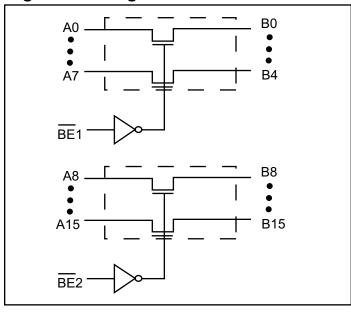


3.3V, Hot Insertion 16-Bit, 2-Port Bus Switch

Features

- → Fast Switching Speed: 4.5ns max.
- → Permits Hot Insertion
- → Near-zero propagation delay
- \rightarrow 5 Ω switches connect inputs to outputs
- → Direct bus connection when switches are ON
- → Ultra-low quiescent power (1.0µA typical)
 - Ideally suited for notebook applications
- → Industrial operating temperature: -40°C to +85°C
- → TTL compatible control of input levels
- → Packages (Pb-free & Green available):
 - 40-pin, 150-mil wide plastic BQSOP (B)

Logic Block Diagram



Description

The PI3B32X245 is a 3.3V,16-bit, 2-port bus switch. Two enable signals ($\overline{\text{BE}}\text{n}$) turn the switches on. The bus switch creates no additional propagational delay or additional ground bounce noise.

Pin Configuration

in Configuration			
NC E	10	40 🛘 Vcc	
A ₀	2	39 ☐ BE₁	
A ₁ [3	38 🛘 B ₀	
A ₂	4	37 🛘 B1	
A3 [5	36 🛘 B ₂	
A4 [6	35 🛘 B ₃	
A5 [7	34 🛘 B4	
A6 🗆	8	33 🛘 B ₅	
A7 [9	32 🛘 B ₆	
GND [10	31 🛘 B ₇	
NC [11	30 🕽 Vcc	
A8 🗆	12	29 BE ₂	
A9 🗆	13	28 🕽 B ₈	
A10 🗆	14	27 🛘 B ₉	
A11 🗆	15	26 D B ₁₀	
A ₁₂ [16	25 D B ₁₁	
A13 🗆	17	24 D B ₁₂	
A14 🗆	18	23 🛘 B13	
A ₁₅ [19	22 D B ₁₄	
GND [20	21 B ₁₅	
· ·			

Truth Table⁽¹⁾

Function	BEn	A0-15
Disconnect	Н	Hi-Z
Connect	L	B0-15

Notes:

H = High Voltage Level
 L = Low Voltage Level
 Hi-Z = High Impedance

Pin Description

Pin Name	I/O	Description
BEn	I	Bus Enable Input (Active LOW)
A0-A15	I/O	Bus A
B0-B15	I/O	Bus B





Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	65°C to +150°C
Ambient Temperature with Power Applied	40°C to +85°C
Supply Voltage to Ground Potential (Inputs & $V_{\rm CC}$ (Only)0.5V to +4.6V
Supply Voltage to Ground Potential (Outputs & D/O	Only) -0.5V to +4.6V
DC Input Voltage	0.5V to +7.0V
DC Output Current	120mA
Power Dissipation	0.5W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40$ °C to +85°C, $V_{CC} = 3.3$ V ± 10 %)

Parameters	Description	Test Conditions(1)	Min.	Typ. ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level			0.8	V
I_{IH}	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$				
I _{IL}	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$			±1	μΑ
I _{OZH}	High Impedence Output Current	0 A, B V _{CC}				m
V _{IK}	Clamp Diode Voltage	$V_{CC} = Min., I_{NN} = -18mA$			-1.2	V
D	Switch On Resistance ⁽³⁾	$V_{CC} = Min., V_{IN} = 0.0V, I_{ON} = 48mA \text{ or } 64mA$		5	8	W
R _{ON}	Switch On Resistance	$V_{CC} = Min., V_{IN} = 2.4V, I_{ON} = 15mA$		10	17	vv

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at $V_{CC} = 3.3V$, $T_A = 25^{\circ}C$ ambient and maximum loading.
- 3. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.

Capacitance $(T_A = 25^{\circ}C, f = 1 \text{ MHz})$

Parameters ⁽¹⁾	Description	Test Conditions	Тур.	Units
$C_{_{\mathrm{IN}}}$	Input Capacitance	$V_{IN} = 0V$	3.5	pF
C_{OFF}	A/B Capacitance, Switch off	$V_{IN} = 0V$	8.0	pF
C _{on}	A/B Capacitance, Switch On	$V_{IN} = 0V$	16.0	pF

Notes:

1. This parameter is determined by device characterization but is not production tested.





Power Supply Characteristics

Parameters	Description	Test Conditi	ons	Min.	Typ.(2)	Max.	Units
I_{cc}	1	00	$V_{IN} = GND \text{ or } V_{CC}$		1.0	5.0	μА
ΔI_{CC}	Supply Current per Input @ TTL HIGH	V _{CC} = Max.	$V_{IN} = 3.0V^{(3)}$			750	μА

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at $V_{CC} = 3.3V$, $+25^{\circ}C$ ambient.
- 3. Per TTL driven input ($V_{IN} = 3.4V$, control inputs only); A and B pins do not contribute to I_{CC} .

Switching Characteristics over Operating Range

			PI3B3	2X245	
			Co	om.	
Parameters	Description	Test Conditions	Min.	Max.	Units
t _{plh}	Propagation Delay ^(1,2) Ax to Bx, Bx to Ax			0.25	
t _{PZH}	Bus Enable Time $\overline{BE}x$ to Ax or Bx	$C_{L} = 50 pF$ $R_{L} = 500 \Omega$	1.0	4.0	ns
t _{PHZ}	Bus Disable Time BEx to Ax or Bx	-	1.0	4.5	

Notes:

- 1. This parameter is guaranteed but not tested on Propagation Delays.
- 2. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Part Marking

B Package



YY: Year

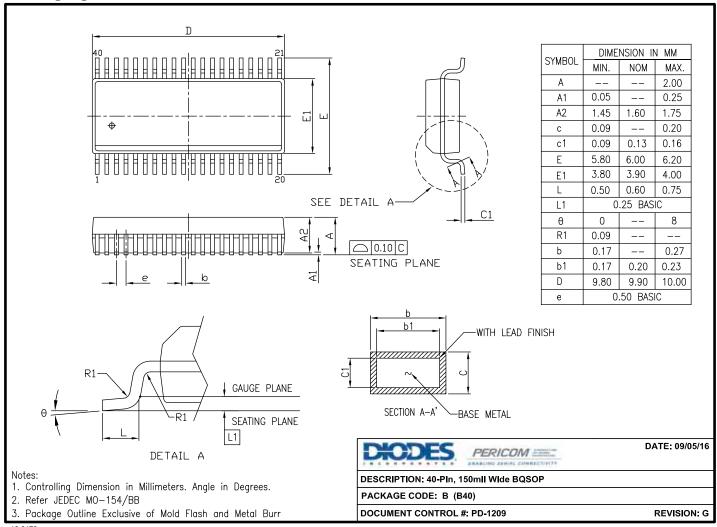
WW: Workweek

1st X: Assembly Code 2nd X: Fab Code





Packaging Mechanical: 40-BQSOP(B)



16-0176

For latest package info.

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Ordering Information

Ordering Code	Package Code	Description
PI3B32X245BEX	В	40-Pin, 150-mil Wide (BQSOP)

Notes

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. Thermal characteristics can be found on the company web site at www.diodes.com/design/support/packaging/
- 3. E = Pb-free and Green
- 4. X suffix = Tape/Reel





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QS3L384QG