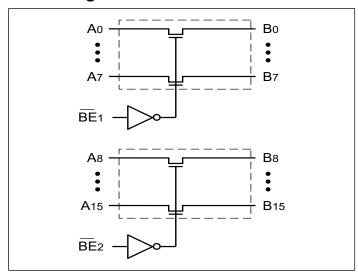


# 2.5V/3.3V, High Bandwidth, Hot Insertion 16-Bit, 2-Port, Bus Switch

#### **Features**

- → Near-Zero propagation delay
- → 5-ohm switches connect inputs to outputs
- → Permits Hot Insertion
- → Capable of Hot-Swapping
- → 5V I/O Tolerant
- → 2.5V Supply Voltage Operation
- → Rail-to-Rail 3.3V or 2.5V Switching
- → High Bandwidth Operation (>400 MHz)
- → Packaging (Pb-free & Green):
  - □ 40-pin, 150-mil-wide plastic BQSOP (B)

## **Block Diagram**



#### Truth Table(1)

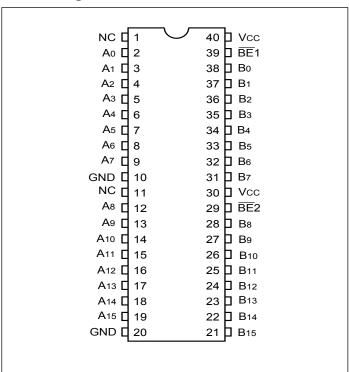
Function	$\overline{ m BE}_{ m N}$	A0-15
Disconnect	Н	Hi-Z
Connect	L	B0-15

Note: H = High Voltage Level L = Low Voltage Level Hi-Z = High Impedance

## **Description**

Pericom Semiconductor's PI3C32X245 is a 2.5V or 3.3V 16-bit, 2-port bus switch designed with a low On-Resistance (5-ohm) allowing inputs to be connected directly to outputs. The bus switch creates no additional propagational delay or additional ground bounce noise. The switches are turned ON by the Bus Enable  $(\overline{BE}_N)$  input signal. It is very useful in switching signals that have high bandwidth (>400 MHz).

## **Pin Configuration**



## **Pin Description**

Pin Name	Description	
$\overline{\mathrm{BE}}_{\mathrm{N}}$	Bus Enable Input (Active LOW)	
A0-15	Bus A	
B0-15	Bus B	

11-0008



## **Absolute Maximum Ratings**

Parameter	Min.	Max.	Units
Storage Temperature	-65	150	°C
Ambient Temperature with Power Applied	-40	85	°C
Supply Voltage to Ground Potential (Inputs & V <sub>CC</sub> Only)	-0.5	4.6	V
Supply Voltage to Ground Potential (Outputs & D/O Only)	-0.5	4.6	V
DC Input Voltage	-0.5	5.5	V
DC Output Current	-	120	mA
Power Dissipation	-	0.5	W

Stress beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device.

## **DC Electrical Characteristics** (Over the Operating Range, $T_A = -40$ °C to +85°C, $V_{CC} = 3.3V \pm 10$ %)

Parameters	Description	Test Conditions <sup>(1)</sup>	Min	Typ (2)	Max	Units
V <sub>IH</sub>	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V <sub>IL</sub>	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	V
I <sub>IH</sub>	Input HIGH Current	V <sub>CC</sub> = Max., V <sub>IN</sub> = V <sub>CC</sub>			±1	
$I_{IL}$	Input LOW Current	V <sub>CC</sub> = Max., V <sub>IN</sub> = GND			±1	μΑ
I <sub>OZH</sub>	High Impedance Output Current	$0 \le A, B \le V_{CC}$			±1	
$V_{IK}$	Clamp Diode Voltage	$V_{CC} = Min., I_{IN} = -18 \text{ mA}$		-0.73	-1.2	V
R <sub>ON</sub> Switch On Resistance <sup>(4)</sup>	C : 1 O P : (4)	$V_{CC} = Min., V_{IN} = 0.0V, I_{ON} = 48mA$		5	7	0
	$V_{CC} = Min, V_{IN} = 2.4V, I_{ON} = 15mA$		8	15	Ω	

### Capacitance ( $T_A = 25^{\circ}C$ , f = 1 MHz)

Parameters <sup>(5)</sup>	Description	<b>Test Conditions</b>	Тур	Units
C <sub>IN</sub>	Input Capacitance	$V_{IN} = 0V$	3.5	pF
C <sub>OFF</sub>	A/B Capacitance, Switch Off	$V_{IN} = 0V$	5.0	pF
Con	A/B Capacitance, Switch On	$V_{IN} = 0V$	10.0	pF

#### Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at  $V_{CC}$  = 3.3V,  $T_A$  = 25°C ambient and maximum loading.
- 3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- 4. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.

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## **Power Supply Characteristics**

Parameters	Description	Test Conditions(1)		Min	Typ (2)	Max	Units
I <sub>CC</sub>	Quiescent Power Supply Current	V <sub>CC</sub> = Max.	$V_{IN} = GND \text{ or } V_{CC}$		0.5	1.0	A
$\Delta I_{CC}$	Supply Current per Input HIGH	V <sub>CC</sub> = Max.	$V_{\rm IN} = 3.0 V^{(3)}$			2.5	mA

#### Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at  $V_{CC} = 3.3V$ ,  $+25^{\circ}C$  ambient.
- 3. Per TTL driven input (control input only); A and B pins do not contribute to  $\Delta I_{CC}$ .

## Switching Characteristics over 3.3V Operating Range

			P13C3	2X245	
			Co	m.	
Parameters	Description	Test Conditions	Min	Max	Units
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay <sup>(2,3)</sup> Ax to Bx	$C_L = 50 \text{ pF}$ $R_L = 500\Omega$		0.25	
t <sub>PZH</sub> t <sub>PZL</sub>	Bus Enable Time BE to Ax or Bx	$C_L = 50 \text{ pF}$	1.5	6.5	ns
t <sub>PHZ</sub>	Bus Disable Time BE to Ax or Bx	$ R_{L} = 500\Omega $ $ R = 500\Omega $	1.5	5.5	

## Switching Characteristics over 2.5V Operating Range

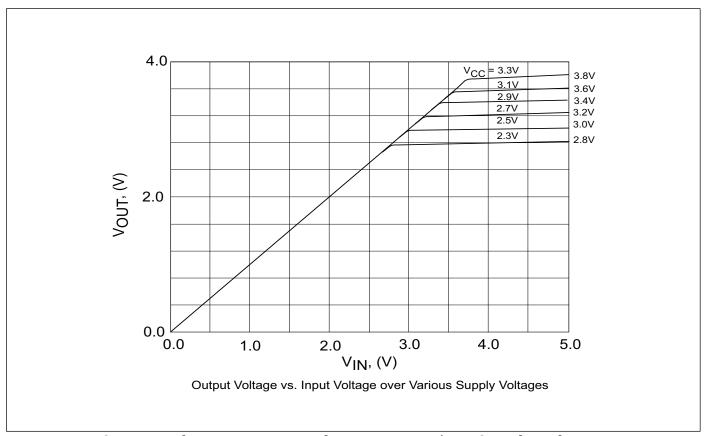
			P13C3	2X245	
			Co	m.	
Parameters	Description	Test Conditions	Min	Max	Units
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay <sup>(2,3)</sup> Ax to Bx	$C_L = 50 \text{ pF}$ $R_L = 500\Omega$		0.25	
t <sub>PZH</sub> t <sub>PZL</sub>	Bus Enable Time BE to Ax or Bx	$C_L = 50 \text{ pF}$	1.5	9.8	ns
t <sub>PHZ</sub>	Bus Disable Time BE to Ax or Bx	$ R_{L} = 500\Omega $ $ R = 500\Omega $	1.5	8.3	

#### Notes:

- 1. See test circuit and waveforms.
- 2. This parameter is guaranteed but not tested on Propagation Delays.
- 3. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

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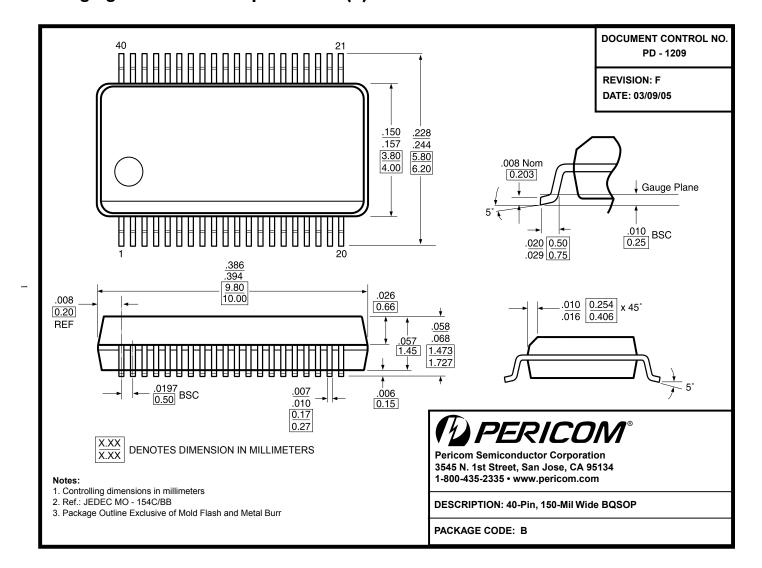




Output Voltage vs. Input Voltage over Various Supply Voltages



## Packaging Mechanical: 40-pin BQSOP (B)



# **Ordering Information**

Ordering Code	Package Code	Package Type
PI3C32X245BE	В	Pb-free & Green, 40-pin BQSOP

<sup>1.</sup> Thermal characteristics can be found on the company web site at www.pericom.com/packaging/

www.pericom.com

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QS3L384QG