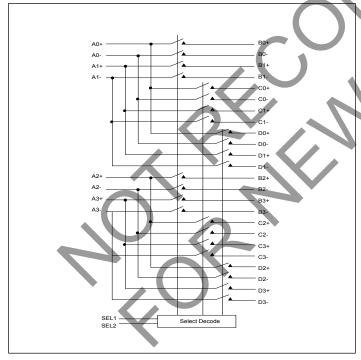


3.3V, PCI Express® 3.0 2-Lane, 3:1 Mux/DeMux Switch, with Power Down Feature

Features

- 4 Differential Channel, 3:1 Mux/DeMux
- PCI Express® 3.0 Performance, 8.0Gbps
- Bi-directional operation
- -3dB Bandwidth: 8GHz
- Low Bit-to-Bit Skew, 10ps max
- Low Crosstalk: -40dB@4 GHz
- Low Insertion Loss: -2.1dB@4 GHz (8.0Gbps)
- Low Return Loss:-10dB@4GHz (8.0Gbps)
- Low Off Isolation:-19dB @4GHz (8Gbps)
- Power Down option
- Supply Voltage 3.3V
- Packaging (Pb-free & Green):
 - 42-contact, TQFN (ZH42)

Block Diagram



Truth Table

| SEL1 | SEL2 | Function | | | | |
|------|------|-------------------------------|--|--|--|--|
| 0 | 0 | Power down all switch hi-z | | | | |
| 0 | 1 | A→B | | | | |
| 1 | 0 | A→C | | | | |
| 1 | 1 | A→D | | | | |

Description

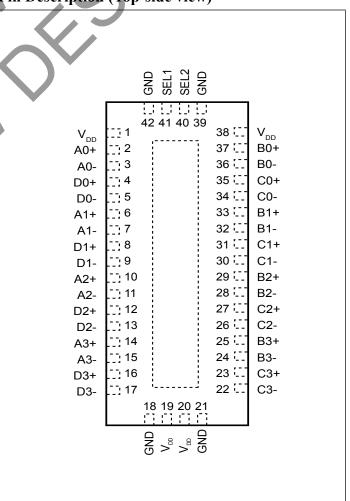
Pericom Semiconductor's PI3PCIE3413 is an 12 to 4 differential channel multiplexer/demultiplexer switch. This solution can switch 2 full PCI Express® 3.0, lanes to one of three locations. Using a unique design technique, Pericom has been able to minimize the impedance of the switch such that the attenuation observed through the switch is mininal. The unique design technique also offers a layout targeted for PCI Express signals, which minimizes the channel to channel skew as well as channel to channel crosstalk as required by the PCI Express specification.

Application

1

Routing of PCI Express 3.0, signals with low signal attenuation.

Pin Description (Top-side view)







PI3PCIE3413

Pin Description

| Pin # | Pin Name | I/O | Description |
|----------------------|-----------------|------|--|
| 2 | A0+ | I/O | C: IVO CI IO D (A |
| 3 | A0- | I/O | Signal I/O, Channel 0, Port A |
| 6 | A1+ | I/O | Signal I/O, Channel 1, Port A |
| 7 | A1- | 1/0 | Signal 1/0, Chainlei 1, Fort A |
| 10 | A2+ | I/O | Signal I/O, Channel 2, Port A |
| 11 | A2- | 170 | orginal I/O, Chamici 2, Fort II |
| 14 | A3+ | I/O | Signal I/O, Channel 3, Port A |
| 15 | A3- | 1,0 | organization of contract of the contract of th |
| 37 | B0+ | I/O | Signal I/O, Channel 0, Port B |
| 36 | В0- | -, - | |
| 33 | B1+ | I/O | Signal I/O, Channel 1, Port B |
| 32 | B1- | | |
| 29 | B2+ | I/O | Signal I/O, Channel 2, Port B |
| 28 | B2- | | 10.0 |
| 25 | B3+ | I/O | Signal I/O, Channel 3, Port B |
| 24 | В3- | | |
| 35 | C0+ | I/O | Signal I/O, Channel 0, Port C |
| 34 | C0- | | |
| 31 | C1+ | I/O | Signal I/O, Channel 1, Port C |
| 30 | C1- | | |
| 27 | C2+ | I/O | Signal I/O, Channel 2, Port C |
| 26 | C2- | , (X | |
| 23 | C3+ | I/O | Signal I/O, Channel 3, Port C |
| 22 | C3- | | |
| 4 | D0+ | I/O | Signal I/O, Channel 0, Port D |
| 5 | D0- | | |
| 8 | D1+ | I/O | Signal I/O, Channel 1, Port D |
| - | D1- | | |
| 12 | D2+ | I/O | Signal I/O, Channel 2, Port D |
| 13 | D2- | | |
| 16 17 | D3+ D3- | I/O | Signal I/O, Channel 3, Port D |
| 40, 41 | SEL2, SEL1 | I | Operation mode Select. Please see Truth Table on Page 1 |
| 1, 19, 20, 38 | V _{DD} | Pwr | 3.3V ±10% Positive Supply Voltage |
| 18,21, 39, 42 Center | | 1 W1 | |
| Pad | GND | Pwr | Power ground |



PI3PCIE3413

Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

| Storage Temperature | 65°C to +150°C |
|------------------------------------|----------------|
| Supply Voltage to Ground Potential | 0.5V to +4.6V |
| Channel DC Input Voltage | 0.5V to 1.5V |
| DC Output Current | 120mA |
| Power Dissipation | 0.5W |
| SEL DC Input Voltage | 0.5V to 4.6V |
| | |

Note: Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Electrical Characteristics

Recommended Operating Conditions

| Symbol | Parameter | Conditions | Min | Тур | Max | Units |
|-------------------|---|-----------------------|-----|------|-----|---------|
| V _{DD} | 3.3V Power Supply | | 3.0 | 3.3 | 3.6 | V |
| I_{DD} | Total current from V _{DD} 3.3V supply | SEL1,2 = 01, 10 or 11 | | 0.15 | 1 | mA |
| I_{DDQ} | Standby I _{DD} | SEL1,2 = 00 | | 0.1 | | |
| T _{CASE} | Case temperature range for operation within spec. | | -40 | | 85 | Celsius |

DC Electrical Characteristics for Switching over Operating Range

| Parameters | Description | Test Conditions ¹ | Min | Typ ⁽¹⁾ | Max | Units |
|-----------------------|---|--------------------------------------|-----|--------------------|------|-------|
| V _{IH} - SEL | Input HIGH Voltage, SEL Input | | 2 | | 3.6 | |
| V _{IL} - SEL | Input LOW Voltage, SEL Input | | 0 | | 0.8 | V |
| V_{IK} | Clamp Diode Voltage | V_{DD} = Max., I_{IN} = $-18mA$ | | -0.7 | -1.2 | |
| IIH | Input HIGH Current, SEL | V_{DD} = Max., V_{IN} = V_{DD} | | | ±5 | |
| IIL | Input LOW Current, SEL | $V_{DD} = Max., V_{IN} = 0V$ | | | ±5 | μΑ |
| IIH | Input HIGH Current, A _X , B _X , C _X , D _x | $V_{DD} = Max., V_{IN} = 1.5V$ | -10 | | +10 | |
| IIL | Input LOW Current, A _X , B _X , C _X , D _x | $V_{DD} = Max., V_{IN} = 0V$ | -10 | | +10 | μΑ |
| IOZH | HighZ HIGH Current, B_X , C_X , D_x | $V_{DD} = Max., V_{IN} = 1.5V$ | -10 | | +10 | μΑ |
| IOZL | HighZ LOW Current, B _X , C _X , D _x | $V_{DD} = Max., V_{IN} = 0V$ | -10 | | +10 | μΑ |

^{1.} Typical values are at $V_{DD} = 3.3 \text{V}$, $T_A = 25^{\circ}\text{C}$ ambient and maximum loading.

Switching Characteristics

| Param- eters | Description | Test Conditions | Min. | Тур. | Max. | Units |
|-------------------------------------|---|------------------------|------|------|------|-------|
| tpZH, tpZL | Line Enable Time - SEL to A _N , B _N , C _N , D _N | | 0.5 | 41 | 55 | |
| t _{PHZ} , t _{PLZ} | Line Disable Time - SEL to A_N , B_{N, C_N} , D_N | | 0.5 | 5 | 25 | ns |
| t _{b-b} | Bit-to-bit skew within the same differential pair | | | 5 | 10 | ps |

Notes:

1. Guaranteed by design. Typical values are at $V^{DD} = 3.3V$, $T^A = 25^{\circ}C$ ambient and maximum loading.

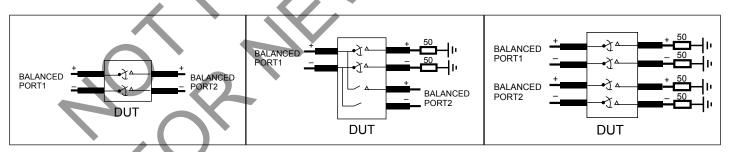


Dynamic Electrical Characteristics

| Param- eter | Description | Test Conditions | Min. | Typ.(1) | Max. | Units | |
|---------------------------|-------------------------------|---------------------|-------|---------|-------|-------|--|
| | | f= 50MHz - 1.25GHz | -0.84 | -0.7 | | JD. | |
| DDII | Differential Insertion Loss | f= 1.25GHz - 2.5GHz | -1.44 | -1.2 | | | |
| DDIL | $(V_{IN} = -10 dBm, DC = 0V)$ | f= 2.5GHz - 4.0GHz | -2.76 | -2.3 | | dB | |
| | | f= 5.0GHz | -3.24 | -2.7 | | | |
| | | f= 50MHz - 1.25GHz | | -22.8 | -18.2 | | |
| DDII | Differential Deturn Loss | f= 1.25GHz - 2.5GHz | | -35 | -28 | 4D | |
| DDIL _{RL} | Differential Return Loss | f= 2.5GHz - 4.0GHz | | -9.4 | -7.52 | dB | |
| | | f= 5.0GHz | | -7 | -5.6 | | |
| | | f= 50MHz - 1.25GHz | | -34.8 | -27.8 | dB | |
| DDII | Differential Off Isolation | f= 1.25GHz - 2.5GHz | | -25.2 | -20.2 | | |
| DDIL _{OFF} | Differential Off Isolation | f= 2.5GHz - 4.0GHz | | -19.5 | -15.6 | | |
| | | f= 5.0GHz | | -18.5 | -14.8 | | |
| DDNEXT Near End Crosstalk | | f= 50MHz - 1.25GHz | | -48.5 | -38.8 | | |
| | Near End Crosstalk | f= 1.25GHz - 2.5GHz |) | -43.4 | -34.7 | 4D | |
| | | f= 2.5GHz - 4.0GHz | | -42.7 | -34.2 | dB | |
| | | f= 5.0GHz | | -42.7 | -34.2 | | |
| BW | -3dB Bandwidth | | | 8.3 | | GHz | |

Notes:

^{1.} Guaranteed by design. Typical values are at $V_{DD} = 3.3V$, $T_A = 25^{\circ}C$ ambient and maximum loading.



Diff. Insertion Loss and Return Test Circuit

Diff. Off Isolation Test Circuit

4

Diff. Near End Xtalk Test Circuit

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Differential Insertion Loss



Differential Return Loss



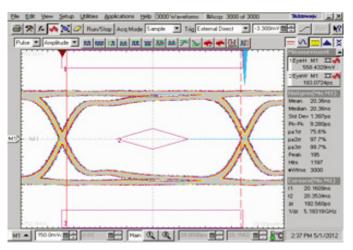


Differential Off Isolation

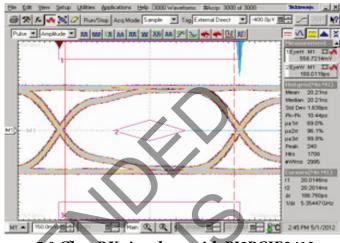


Differential Crosstalk

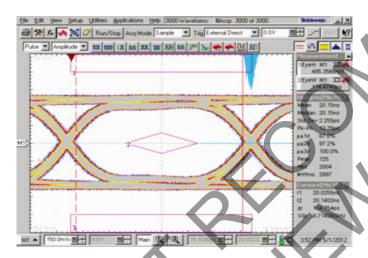




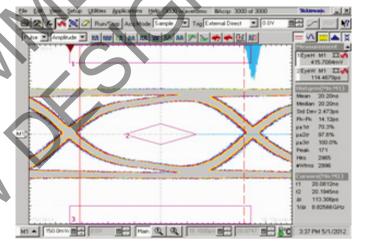
5.0 Gbps RX signal eye without PI3PCIE3413



5.0 Gbps RX signal eye with PI3PCIE3413



8.0 Gbps RX signal eye without PI3PCIE3413



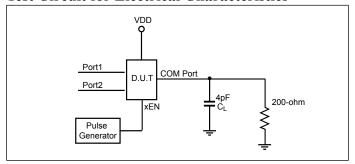
8.0 Gbps RX signal eye with PI3PCIE3413

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Test Circuit for Electrical Characteristics⁽¹⁻⁵⁾



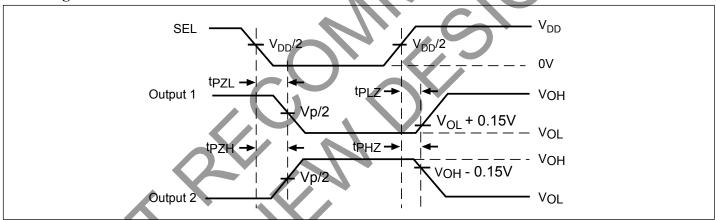
Switch Positions

| Test | Switch |
|-------------------------------------|--------|
| t_{PLZ}, t_{PZL} | 3.0V |
| t _{PHZ} , t _{PZH} | GND |
| Prop Delay | Open |

Notes:

- 1. $C_L = Load$ capacitance: includes jig and probe capacitance.
- 2. R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator
- 3. Output 1 is for an output with internal conditions such that the output is low except when disabled by the output control. output 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- 4. All input impulses are supplied by generators having the following characteristics: PRR \leq MHz, $Z_0 = 50\Omega$, $t_R \leq 2.5$ ns, $t_F \leq 2.5$ ns
- 5. The outputs are measured one at a time with one transition per measurement.

Switching Waveforms



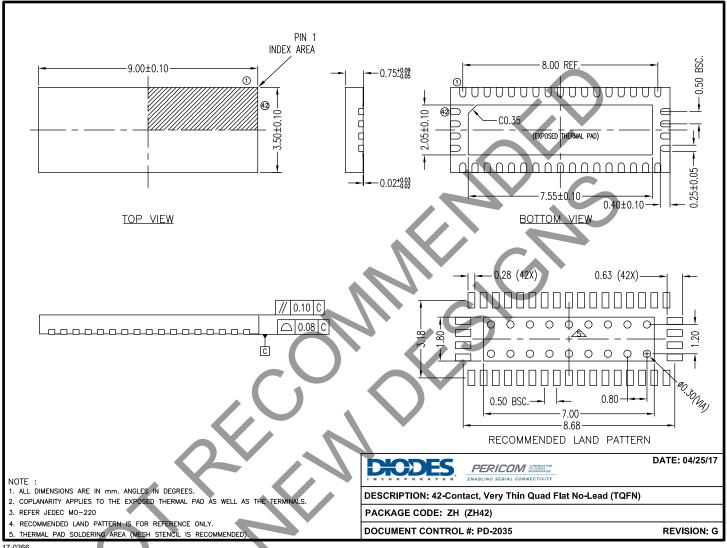
Voltage Waveforms Enable and Disable Times





PI3PCIE3413

Packaging Information: 42-contact (TQFN)



For latest package info.

 $please\ check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-pericom-pack$

Ordering Information

| Ordering Code | Package Code | Package Description |
|----------------|--------------|--|
| PI3PCIE3413ZHE | ZH | 42-contact, Thin Fine Pitch Quad Flat No-Lead (TQFN) |

- Thermal characteristics can be found on the company web site at www.diodes.com/design/support/packaging/
- E = Pb-free and Green
- X suffix = Tape/Reel





PI3PCIE3413

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74HCT138D.652 74HCT139D.652 TC74HC4052AFT(EL) SN74LVC257AMPWREP 74LVC1G157GV.125 74HC253DB,112
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