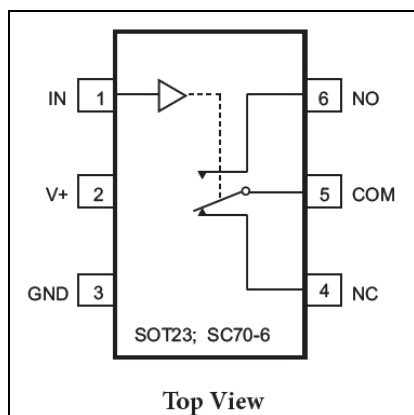


SOTiny™ Low Resistance, Low-Voltage Single-Supply SPDT Switch

Features

- CMOS Technology for Bus and Analog Applications
- Low On-Resistance: 8Ω at 3.0V
- Wide V+ Range: 1.65V to 6.0V
- Rail-to-Rail Signal Range
- Fast Transition Speed: 2ns at 5.0V
- High Off Isolation: -63dB @ 10MHz
- Break-Before-Make Switching
- High Bandwidth: 350MHz
- Extended Industrial Temperature Range:
-40°C to 85°C
- Packaging (Pb-free and Green):
-6-pin Small Compact SC70(C6)

Pin Assignment



PI5A4599B

Description

The PI5A4599B is a high-bandwidth, fast single-pole double-throw (SPDT) CMOS switch. It can be used as an analog switch or as a low-delay bus switch. Specified over a wide operating power supply voltage range, 1.65V to 6.0V, the PI5A4599B has a maximum ON resistance of 12-ohms at 1.65V, 9-ohms at 2.3V & 6-ohms at 4.5V.

Break-before-make switching prevents both switches being enabled simultaneously. This eliminates signal disruption during switching.

The control input, IN, is independent of supply voltage.

Applications

- Cell Phones
- PDAs
- MP3 Players
- Portable Instrumentation
- Battery powered Communications
- Computer Peripherals

Pin Description

Pin No	Pin Name	Description
1	IN	Logic control
2	V+	Positive Power Supply
3	GND	Ground
4	NC	Data Port (Normally connected)
5	COM	Common Output/Data Port
6	NO	Data Port

Logic Function Table

Logic Inputs(IN)	NC	NO
0	ON	OFF
1	OFF	ON

Maximum Ratings

Storage Temperature.....	-65°C to +150°C
Ambient Temperature with Power Applied.....	-40°C to +85°C
Supply Voltage V+	-0.5V to +7.0V
DC Switch Voltage V _{IN}	-0.5V to V+ +0.5V
DC Input Voltage V _{INPUT}	-0.5V to +7.0V
DC Output Current V _{OUT}	128mA
DC V+ or Ground Current I+ /I _{GND}	±100mA
Junction Temperature under Bias (T _J)	150°C
Junction Lead Temperature (T _L) (Soldering, 10 seconds)	260°C
Power Dissipation (P _D) @ +85°C	180mW

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Recommended Operating Conditions

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V+	Operating Voltage	-	1.65	-	6.0	V
V _{IN}	Control Input Voltage	-	0	-	V+	V
V _S	Switch Input Voltage	-	0	-	V+	V
V _{OUT}	Output Voltage	-	0	-	V+	V
T _A	Operating Temperature	-	-40	25	85	°C
t _r , t _f	Input Rise and Fall Time	Control Input V+ = 2.3V to 3.6V	0	-	10	ns/V
		Control Input V+ = 4.5V to 6.0V	0	-	5	ns/V

Note: Control input must be held HIGH or LOW; it must not float.

DC Electrical Characteristics

 (T_A = -40°C to 85°C, unless otherwise noted.)

Parameter	Description	Test Conditions	Temperature (T _A :°C)	Min.	Typ.	Max.	Units
V _{IAR}	Analog Input Signal Range	V+	-40°C to 85°C	0	-	V+	V
R _{ON}	ON Resistance ⁽¹⁾	V+=4.5V, I _O = 30mA, V _{INPUT} = 0V	25°C	-	4	6	Ω
		V+=4.5V, I _O =-30mA, V _{INPUT} =2.4V		-	5	8	
		V+=4.5V, I _O = -30mA, V _{INPUT} =4.5V		-	7	11	
		V+=4.5V, I _O = 30mA, V _{INPUT} = 0V	-40°C to 85°C	-	-	6	
		V+=4.5V, I _O = -30mA, V _{INPUT} =2.4V		-	-	8	
		V+=4.5V, I _O = -30mA, V _{INPUT} =4.5V		-	-	11	
		V+=3.0V, I _O = 24mA, V _{INPUT} = 0V	25°C	-	5	8	
		V+=3.0V, I _O = -24mA, V _{INPUT} =3.0V		-	10	15	
		V+=3.0V, I _O = 24mA, V _{INPUT} =0V		-40°C to 85°C	-	-	
		V+=3.0V, I _O =-24mA, V _{INPUT} =3.0V	-		-	15	
		V+=2.3V, I _O = 8mA, V _{INPUT} = 0V	25°C		-	6	
		V+=2.3V, I _O = -8mA, V _{INPUT} =2.3V		-	13	20	
		V+=2.3V, I _O = 8mA, V _{INPUT} = 0V		-40°C to 85°C	-	-	
		V+=2.3V, I _O =-8mA, V _{INPUT} =2.3V	-		-	20	
		V+=1.65V, I _O = 4mA, V _{INPUT} = 0V	25°C		-	8	
		V+=1.65V, I _O =-4mA, V _{INPUT} =1.65V		-	20	30	
		V+=1.65V, I _O = 4mA, V _{INPUT} = 0V		-40°C to 85°C	-	-	
		V+=1.65V, I _O =-4mA, V _{INPUT} =1.65V	-		-	25	
ΔR _{ON}	ON Resistance Match Between Channels ^(1,2,3)	V+=4.5V, I _A =-30mA, V _{INPUT} =3.15V	25°C		-	0.15	-
		V+=3.0V, I _A =-24mA, V _{INPUT} = 2.1V		-	0.2	-	
		V+=2.3V, I _A = -8mA, V _{INPUT} = 1.6V		-	0.3	-	
		V+=1.65V, I _A =-4mA, V _{INPUT} =1.15V		-	0.5	-	
R _{ONF}	ON Resistance Flatness ^(1,2,4)	V+=5.0V, I _A = -30mA, 0 ≤ V _{INPUT} ≤ V+	25°C	-	4.6	-	Ω
		V+=3.3V, I _A = -24mA, 0 ≤ V _{INPUT} ≤ V+		-	7	-	
		V+=2.5V, I _A = -8mA, 0 ≤ V _{INPUT} ≤ V+		-	15	-	
		V+=1.8V, I _A = -4mA, 0 ≤ V _{INPUT} ≤ V+		-	77	-	
V _{IH}	Input High Voltage (Logic High Level)	V+=1.65V	-40°C to 85°C	1	-	-	V
		V+ = 2.3V		1.2	-	-	
		V+ = 3V		1.3	-	-	
		V+ = 4.2V		1.5	-	-	
		V+ = 5.5V		1.8	-	-	
V _{IL}	Input Low Voltage (Logic Low Level)	V+=1.65V	-40°C to 85°C	-	-	0.4	V
		V+ = 2.3V		-	-	0.6	
		V+ = 3V		-	-	0.8	
		V+ = 4.2V		-	-	1	
		V+ = 5.5V		-	-	1.2	
I _{LKC}	Input Leakage Current	0 ≤ V _{IN} ≤ 5.5V, V+ = 0V to 5.5V	25°C	-	-	±0.1	μA
			-40°C to 85°C	-	-	±1.0	
I _{OFF}	OFF State Leakage Current	0 ≤ V _{IN} ≤ 5.5V, V+ = 1.65V to 5.5V	25°C	-	-	±0.1	μA
			-40°C to 85°C	-	-	±10	
I+	Quiescent Supply Current	All channels ON or OFF, V _{IN} = V+ or GND, I _{OUT} = 0, V+ = 5.5V V	25°C	-	-	1	μA
			-40°C to 85°C	-	-	5	

Notes:

- Measured by voltage drop between COM and NC/NO pins at the indicated current through the device. ON resistance is determined by the lower of the voltages on two ports (COM or NC/NO).
- Parameter is characterized but not tested in production.
- ΔR_{ON} = R_{ON} max – R_{ON} min. measured at identical V+, temperature and voltage levels.
- Flatness is defined as difference between maximum and minimum value of ON resistance over the specified range of conditions. Guaranteed by design.

Capacitance ⁽¹⁾

($T_A = 25^\circ\text{C}$, unless otherwise noted.)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
C_{IN}	Control Input	$V_+ = 5.0\text{V}$, $f = 1\text{ MHz}$ ⁽¹⁾	-	2.5	-	pF
C_{OFF}	NC/NO Port, Switch OFF		-	5.0	-	
$C_{COM ON}$	COM Port, Switch ON		-	15.0	-	

Notes:

1. Capacitance is characterized but not tested in production

Switch and AC Characteristics ⁽¹⁾

Parameter	Description	Test Conditions	Supply Voltage	Temperature (T_A : $^\circ\text{C}$)	Min.	Typ.	Max.	Units
t_{ON}	Turn-On Time	$R_L = 100\Omega$, $C_L = 35\text{pF}$, <i>See Test Circuit Figure 1.</i>	$V_+ = 3.3\text{V}$, V_{NO} or $V_{NO} = 1.5\text{V}$, $V_{IL} = 1.5\text{V}$,	$+25^\circ\text{C}$	-	5	25	ns
			$V_+ = 5.0\text{V}$, V_{NO} or $V_{NO} = 3\text{V}$, $V_{IL} = 3\text{V}$,		-	3	15	
t_{OFF}	Turn-Off Time	$R_L = 100\Omega$, $C_L = 35\text{pF}$, <i>See Test Circuit Figure 1.</i>	$V_+ = 3.3\text{V}$, V_{NO} or $V_{NO} = 1.5\text{V}$, $V_{IL} = 1.5\text{V}$,	$+25^\circ\text{C}$	-	2	12	
			$V_+ = 5.0\text{V}$, V_{NO} or $V_{NO} = 3\text{V}$, $V_{IL} = 3\text{V}$,		-	1.5	7	
t_{BM}	Break Before Make Time	<i>See Test Circuit Figure 2.</i>	$V_+ = 1.65\text{V}$ to 1.95V	-40 to 85°C	0.5	-	-	
			$V_+ = 2.3\text{V}$ to 2.7V		0.5	-	-	
			$V_+ = 3.0\text{V}$ to 3.6V		0.5	-	-	
			$V_+ = 4.5\text{V}$ to 5.5V		0.5	-	-	
Q	Charge Injection	$C_L = 0.1\text{nF}$, $V_{GEN} = 0\text{V}$, $R_{GEN} = 0\Omega$. <i>See Test Circuit Figure 3.</i>	$V_+ = 5.0\text{V}$	25°C	-	5	-	pC
			$V_+ = 3.3\text{V}$		-	4	-	
OIRR	Off Isolation	$R_L = 50\Omega$, $V_{GEN} = 0\text{V}$, $R_{GEN} = 0\Omega$, $f = 1\text{MHz}$. ⁽³⁾ <i>See test circuit 5</i>	$V_+ = 1.65\text{V}$ to 5.5V	25°C	-	-82	-	dB
X_{TALK}	Crosstalk Isolation	$R_L = 50\Omega$, $V_{GEN} = 0\text{V}$, $R_{GEN} = 0\Omega$, $f = 1\text{MHz}$.	$V_+ = 1.65\text{V}$ to 5.5V	25°C	-	-82	-	
f_{3dB}	-3dB Bandwidth	<i>See Test Circuit Figure 8</i>	$V_+ = 1.65\text{V}$ to 5.5V	25°C	-	350	-	MHz

Notes:

- Guaranteed by design.
- The device contributes no other propagation delay other than the RC delay of the switch ON resistance and the 50pF load capacitance, when driven by an ideal voltage source with zero output impedance.
- Off Isolation = $20 \text{ Log}_{10} [V_{NC} \text{ (or } V_{NO})/V_{COM}]$ and is measured in dB.
- Crosstalk Isolation = $20 \text{ Log}_{10} [V_{NO}/V_{NC}]$ and is measured in dB.

Test Circuits and Timing Diagrams

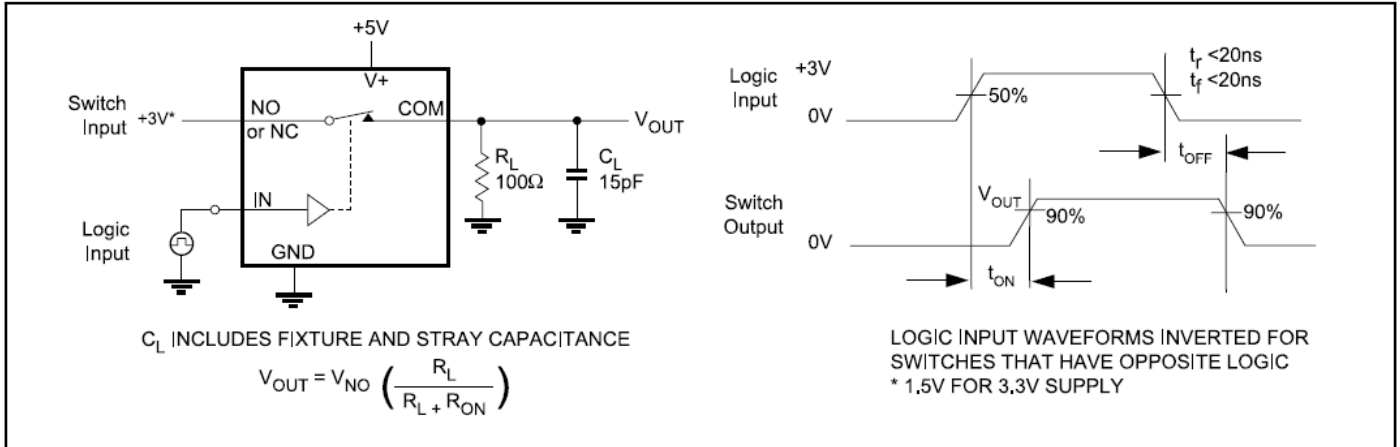


Figure 1. Switching Time

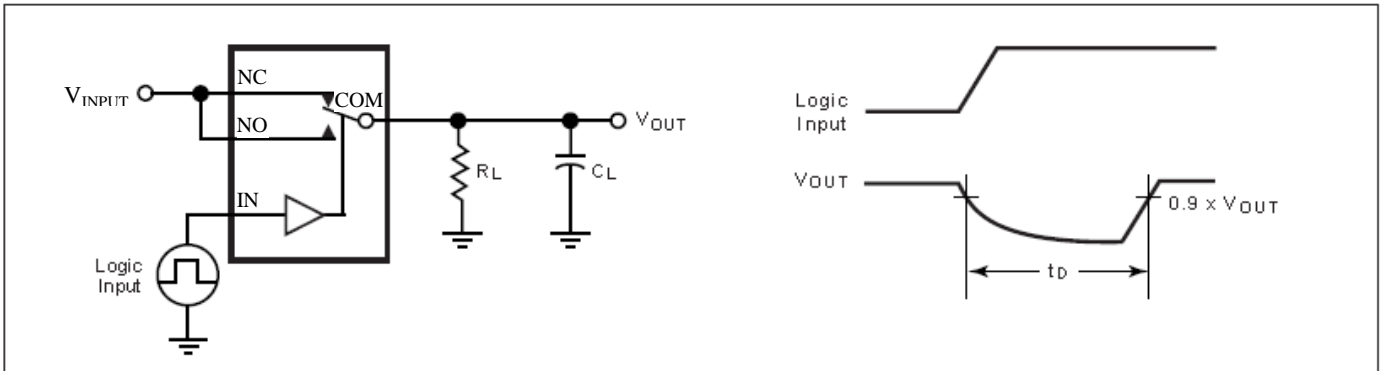


Figure 2. Break Before Make Interval Timing

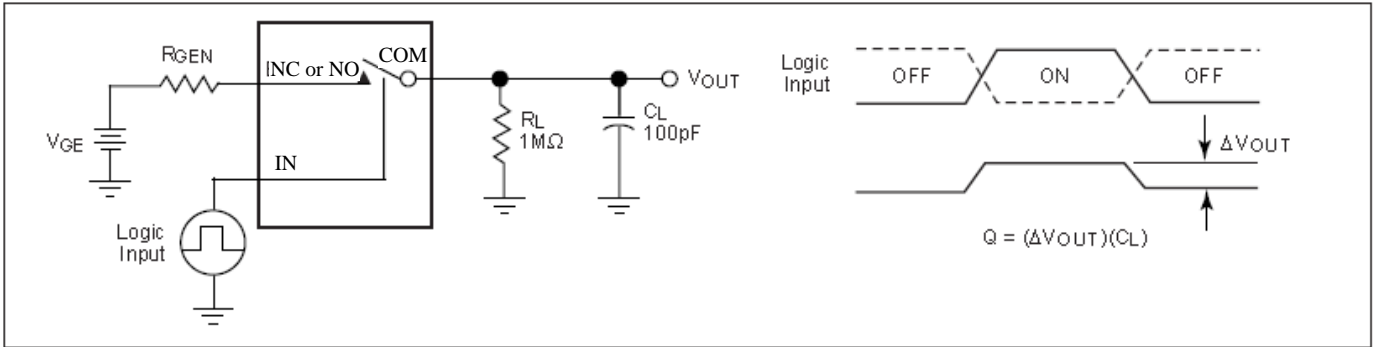


Figure 3. Charge Injection Test

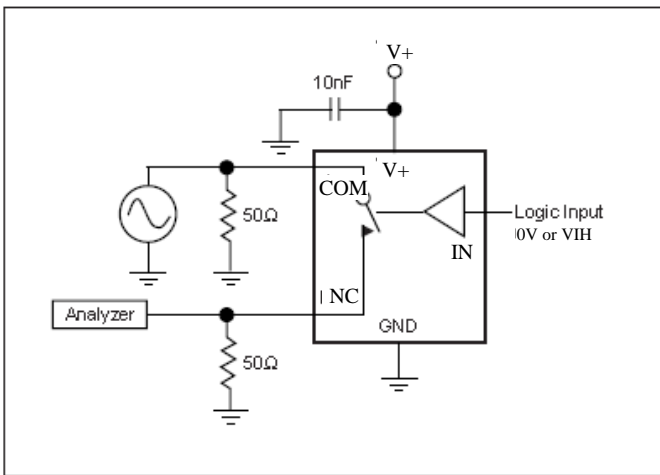


Figure 4. Off Isolation

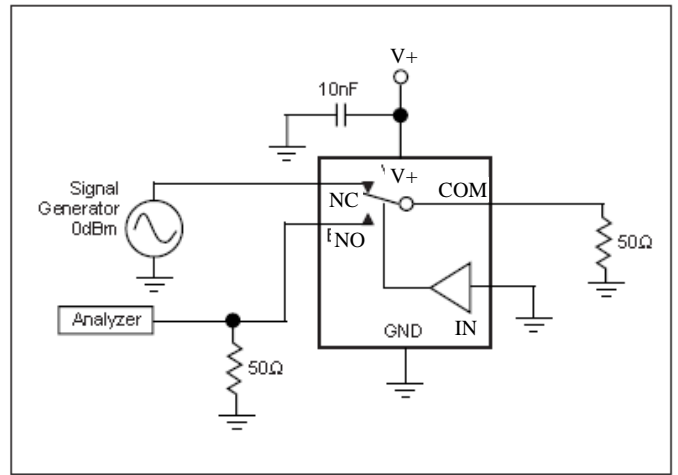


Figure 5. Crosstalk

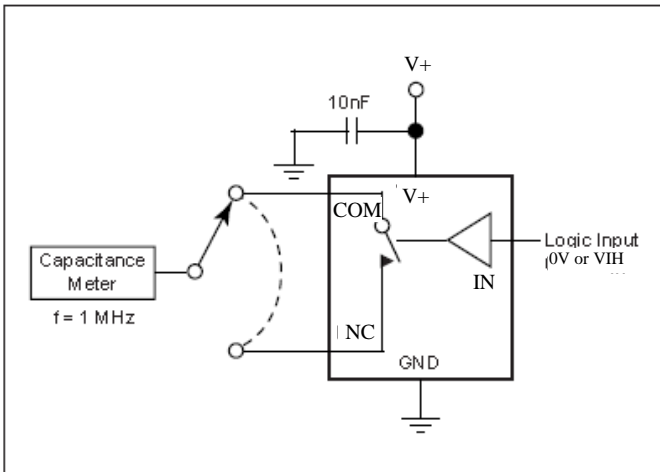


Figure 6. Channel Off Capacitance

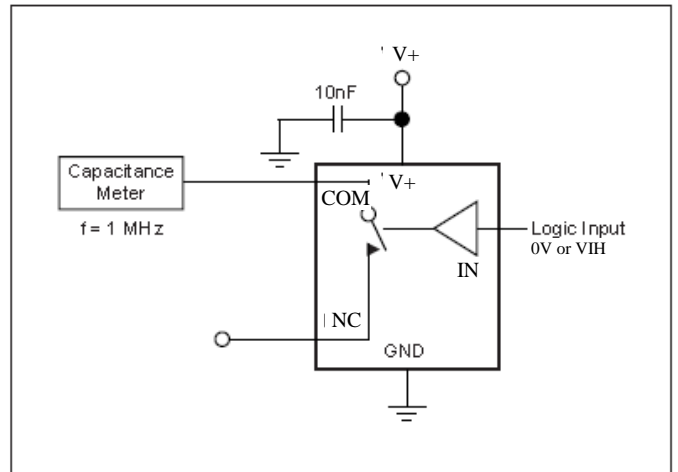


Figure 7. Channel On Capacitance

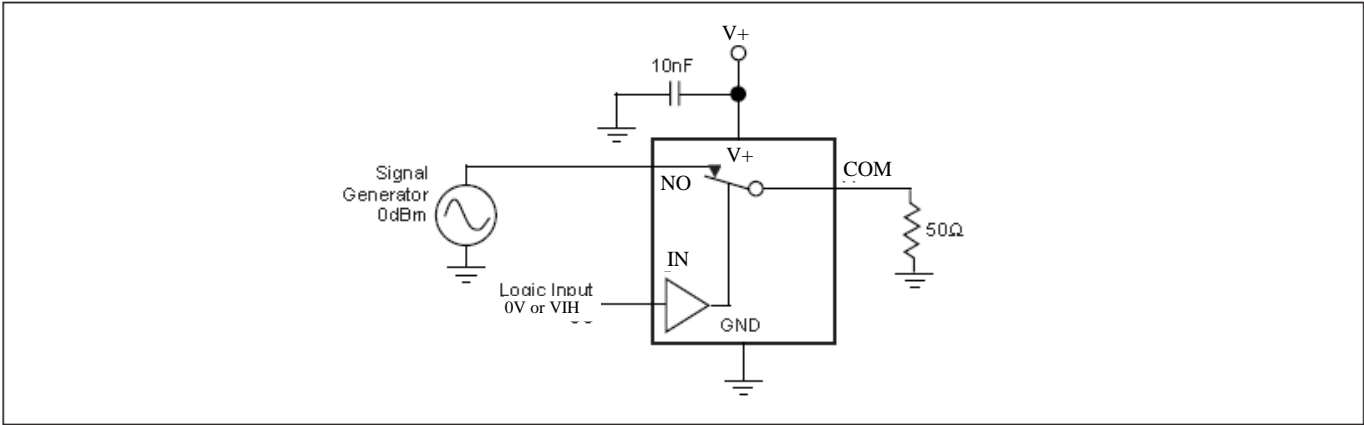


Figure 8. Bandwidth

Mechanical Information
C-6 (SC70)

PKG. DIMENSIONS(MM)		
SYMBOL	Min	Max
A	-	1.10
A1	0.00	0.10
A2	0.70	1.00
b	0.15	0.40
c	0.08	0.22
D	1.80	2.20
E	1.10	1.40
E1	1.80	2.40
e	0.65 BSC	
L	0.26	0.46
θ	0°	8°

Notes:

1. Comply with MO-203C/AB, except b Max, D Min and D Max.
2. PACKAGE OUTLINE DIMENSIONS DO NOT INCLUDE MOLD FLASH AND METAL BURR

PERICOM Enabling Serial Connectivity		DATE: 03/29/16
DESCRIPTION: 6-Pin, SOT363 (SC70)		
PACKAGE CODE: C (C6)		
DOCUMENT CONTROL#: PD-1902	REVISION: B	

16-0078

For latest package info.

please check: <http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/>

Ordering Information

Part Number	Package Code	Package	Top Marking
PI5A4599BCEX	C	6-Pin, SOT363 (SC70)	nK

Notes:

- Thermal characteristics can be found on the company web site at www.diodes.com/design/support/packaging/
- E = Pb-free and Green
- X suffix = Tape/Reel

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