



3:1 Mux/DeMux Bus Switch

#### **Features**

- → Near-Zero propagation delay
- $\rightarrow$  5-ohm switches connect inputs to outputs
- $\rightarrow$  Direct bus connection when switches are ON
- $\rightarrow$  Ultra Low Quiescent Power (0.2µA typical)– Ideally suited for notebook applications
- → Packaging (Pb-free & Green available):
  - 8 pin 118-mil wide plastic MSOP (U)

#### **Block Diagram**



### Truth Table <sup>(1)</sup>

Select			
<b>S1</b>	<b>S0</b>	YA	Function
L	L	IA0	S1-0 = 0
L	Н	IA1	S1-0 = 1
Н	L	IA2	S1-0 = 2
Н	Н	Hi-Z	S1-0 = 3

Note:

1. H = High Voltage Level

L = Low Voltage Level

# Description

Diodes' PI5C3309 is a 3:1 Multiplexer/Demultiplexerwith 3-State outputs. Inputs can be connected to outputs with lowOn-Resistance (5-ohm) with no additional ground bounce noise orpropagation delay.



#### **Pin Configuration**

## **Pin Description**

Pin Name	Description	
IAn	Data Inputs	
S0-1	Select Inputs	
YA	Data Output	
GND	Ground	
V <sub>CC</sub>	Power	



Note:



PI5C3309

#### **Maximum Ratings**

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature
Ambient Temperature with Power Applied40°C to +85°C
Supply Voltage to Ground Potential (Inputs & $V_{CC}$ only) –0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O only) -0.5V to +7.0V
DC Input Voltage0.5V to +7.0V
DC Output Current 120mA
Power Dissipation0.5W

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

#### **DC Electrical Characteristics** (Over Operating Range, $T_A = -40^{\circ}$ C to $+85^{\circ}$ C, $V_{CC} = 5V \pm 5\%$ )

Parameters	Description	Test Conditions <sup>(1)</sup>	Min.	Typ. <sup>(2)</sup>	Max.	Units	
V <sub>IH</sub>	Input HIGH Voltage	Guaranteed logic HIGH level	2.0			V	
V <sub>IL</sub>	Input LOW Voltage	Guaranteed logic LOW level	-0.5		0.8	v	
I <sub>IH</sub>	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$			±1		
I <sub>IL</sub>	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$			±1	μΑ	
I <sub>OZH</sub>	High Impedance Output Current	$0 \leq I_N,  Y_N \leq V_{CC}$			±1		
V <sub>IK</sub>	Clamp Diode Voltage	$V_{CC} = Min, I_{IN} = -18mA$	-0.7		1.2	V	
I <sub>OS</sub>	Short Circuit current	$I(Y)=0V, Y(I)=V_{CC}$			100	mA	
V <sub>H</sub>	Input Hysteresis at Control Pins			150		mV	
D	Societation Provintences (4)	$V_{CC} = Min., V_{IN} = 0.0V, I_{ON} = 48mA$		5	7		
NON	Switch on Resistance (	$V_{CC} = Min., V_{IN} = 2.4V, I_{ON} = 15mA$		10	15		

#### Notes:

For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type. 1.

Typical values are at  $V_{CC} = 5.0V$ ,  $T_A = 25^{\circ}C$  ambient and maximum loading. 2.

Not more than one output should be shorted at one time. Duration of the test should not exceed one second. 3.

4. Measured by the voltage drop between I and Y pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (I, Y) pins.

#### **Capacitance** ( $T_A = 25^{\circ}C$ , f = 1 MHz)

Parameters <sup>(1)</sup>	Description	Test Conditions	Тур.	Units
C <sub>IN</sub>	Input Capacitance		3	
C <sub>OFF</sub>	IA Capacitance, Switch Off	$V_{IN} = 0V$	4	pF
C <sub>ON</sub>	IA Capacitance, Switch On		16	

Notes:

This parameter is determined by device characterization but is not production tested. 1.





#### **Power Supply Characteristics**

Parameters	Description	Test Conditions <sup>(1)</sup>		Min.	Typ. <sup>(2)</sup>	Max.	Units
I <sub>CC</sub>	Quiescent Power Supply Current	V – Mou	$V_{IN}$ = GND or $V_{CC}$		0.1	3.0	μA
ΔI <sub>CC</sub>	Supply Current per Input @ TTL HIGH	$v_{\rm CC} - max.$	$V_{IN} = 3.4 V^{(3)}$			2.5	mA
I <sub>CCD</sub>	Supply Current per Input per MHz <sup>(4)</sup>	V <sub>CC</sub> = Max., I and Y pins open Control Input Tog- gling 50% Duty Cycle				0.25	mA/ MHz

Notes:

For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device. 1.

Typical values are at  $V_{CC} = 5.0V$ ,  $+25^{\circ}C$  ambient. 2.

Per TTL driven input ( $V_{IN}$  = 3.4V, control inputs only); I and Y pins do not contribute to I<sub>CC</sub>. 3.

This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The I and Y inputs gener-4. ate no significant AC or DC currents as they transition. The parameter is not tested, but is guaranteed by design.

#### Switching Characteristics over Operating Range

	Description	Conditions	PI5C3309		T
Parameters			Com		
			Min.	Max.	
t <sub>IY</sub>	Propogation $Delay^{(1,2)} I_N$ to $Y_N$			0.25	
t <sub>SY</sub>	Bus Select Time $S_N$ to $Y_N$		0.5	6.6	
t <sub>PZH</sub> t <sub>PZL</sub>	Bus Enable Time $S_{\rm N}$ to $Y_{\rm N}$	$C_{L} = 50 \text{pF}$ $B_{L} = 500 \text{O}$	0.5	6.0	ns
t <sub>PHZ</sub> t <sub>PLZ</sub>	Bus Disable Time $S_N$ to $Y_N$	NL 50032	0.5	6.0	

Notes:

This parameter is guaranteed but not tested on Propagation Delays. 1.

2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

## **Part Marking**

U Package



Y: Date Code (Year) W: Date Code (Workweek) 1st X: Assembly Site Code 2nd X: Wafer Fab Site Code





### Packaging Mechanical: 8-MSOP(U)



#### For latest package info.

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#### **Ordering Information**

Ordering Code	Packaging Code	Package Description
PI5C3309UEX	U	8-pin, Mini Small Outline Package (MSOP)

Notes:

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.

2. See http://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. Thermal characteristics can be found on the company web site at www.diodes.com/design/support/packaging/

3. E = Pb-free and Green

4. X suffix = Tape/Reel





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