



### Product Summary (@T<sub>A</sub> = +25°C)

V <sub>RRM</sub> (V)	I <sub>0</sub> (A)	V <sub>F</sub> Max (V)	I <sub>R</sub> Max (μA)
800	2	1.1	5

### Description

The S2KDF is a rectifier packaged in the low-profile D-FLAT package. Providing high current capability for standard rectification, this device is ideal for use in general rectification applications.

## Applications

- Switching Mode Power Supplies
- Chargers
- LED lightings
- Inverters
- **AC-DC** Adapters

### 2.0A SURFACE MOUNT GLASS PASSIVATED RECTIFIER

### **Features and Benefits**

- **Glass Passivated Die Construction**
- Surge Overload Rating to 55A Peak
- **High Current Capability**
- Low-Profile Design, Package Height Less than 1.1mm
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (S2KDFQ)

### **Mechanical Data**

- Case: D-FLAT •
- Case Material: Molded Plastic, "Green" Molding Compound. • UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (93)
- Polarity: Cathode Band
- Weight: 0.036 grams (Approximate)

### D-FLAT



Top View

## Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
S2KDF-13	AEC-Q101	D-FLAT	10,000/Tape & Reel

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied. 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

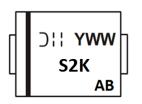
Notes:

and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## Marking Information





S2K= Product Type Marking Code ⊃¦¦ = Manufacturers' Code Marking YWW = Date Code Marking Y = Last Digit of Year (ex: 5 for 2015) WW = Week Code (01 to 53) AB = Foundry and Assembly Code



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage (Note 5)	Vrrm V <sub>rwm</sub> V <sub>r</sub>	800	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	560	V
Average Rectified Output Current @ T <sub>A</sub> =	+25°C lo	2.0	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	55	А

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance, Junction to Terminal (Note 7)	R <sub>θJT</sub>	23	°C/W
Typical Thermal Resistance, Junction to Air (Note 7)	$R_{\theta JA}$	82	°C/W
Operating and Storage Temperature Range	$T_{J,} T_{STG}$	-55 to +150	°C

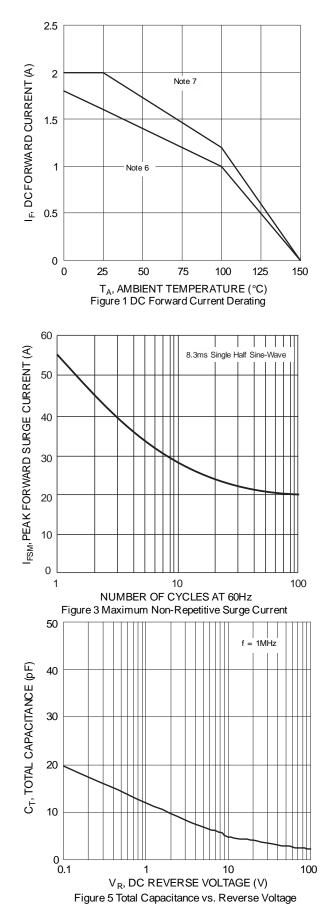
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

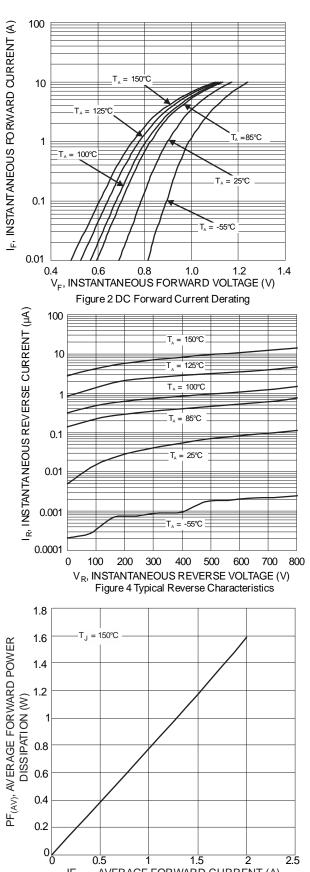
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 5)	V <sub>(BR)R</sub>	800	—	_	V	$I_R = 10 \mu A$
Forward Voltage	VF	_	0.90 0.78 0.95 0.84	1.0 — 1.1 —	v	$\begin{split} I_F &= 1A, \ T_J = +25^\circ C \\ I_F &= 1A, \ T_J = +125^\circ C \\ I_F &= 2A, \ T_J = +25^\circ C \\ I_F &= 2A, \ T_J = +125^\circ C \end{split}$
Reverse Leakage Current (Note 5)	I <sub>R</sub>		0.12 0.005	5	μA mA	V <sub>R</sub> = 800V, T <sub>J</sub> = +25°C V <sub>R</sub> = 800V, T <sub>J</sub> = +125°C
Total Capacitance	CT		8		pf	$V_R = 4V_{DC}, f = 1MHz$

Notes:

Short duration pulse test used to minimize self-heating effect.
 Device mounted on FR-4 substrate, 1" x 1", 2oz, single-sided, PC boards with 0.1" x 0.15" copper pads.
 Device mounted on FR-4 substrate, 0.4" x 0.5", 2oz, single-sided, PC boards with 0.2" x 0.25" copper pads.





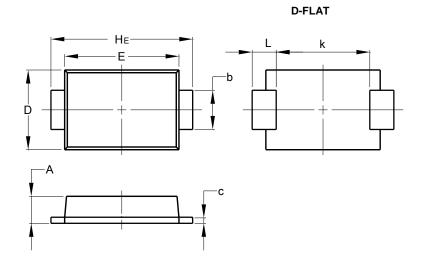






# Package Outline Dimensions

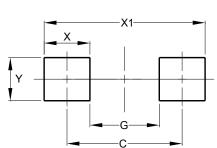
Please see http://www.diodes.com/package-outlines.html for the latest version.



D-FLAT					
Dim	Dim Min				
Α	0.90	1.10			
b	1.25	1.65			
С	0.10	0.40			
D	2.25	2.95			
Е	3.95	4.60			
k	2.80	-			
H <sub>E</sub> 5.00 5		5.60			
Ĺ	0.50	1.30			
All Dimensions in mm					

# Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



D-FLAT

Dimensions	Value (in mm)	
С	4.65	
G	2.80	
Х	1.85	
X1	6.50	
Y	1.70	



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