



#### Product Summary (@T<sub>A</sub> = +25°C)

	-		
V <sub>RRM</sub> (V)	I <sub>O</sub> (A)	VF MAX (V)	I <sub>R MAX</sub> (μΑ)
60	4	0.52	150

## **Description and Applications**

The SBRT4U60LP is a 4A, 60V single rectifier packaged in the low profile U-DFN3030-8 package. Providing low V<sub>F</sub> and excellent high temperature stability, this device is ideal for use in general rectification applications such as:

- **Bypass Diode**
- Boost Diode
- **Blocking Diode**
- **Recirculating Diode**

#### 4A TRENCH SUPER BARRIER RECTIFIER

## **Features and Benefits**

- Reduced Ultra-Low Forward Voltage Drop (V<sub>F</sub>); Better Efficiency • and Cooler Operation
- Reduced High Temperature Reverse Leakage; Increased Reliability Against Thermal Runaway Failure in High **Temperature Operation**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

## **Mechanical Data**

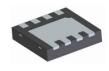
- Case: U-DFN3030-8 •
- Case Material: Molded Plastic, "Green" Molding Compound. UL • Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu Annealed over Copper Lead Frame. Solderable per MIL-STD-202, Method 208@4

Pin 1 indicato

C = CATHODE A = ANODE

Weight: 0.0172 grams (Approximate)

U-DFN3030-8



Bottom View

Top View Schematic and Pin Configuration

## Ordering Information (Note 4)

	Part Number	Case	Packaging		
SBRT4U60LP-7		U-DFN3030-8	3000/Tape & Reel		
Notes:	Notes: 1 No purposely added lead Fully FLI Directive 2002/95/FC (RoHS) 2011/65/FLI (RoHS 2) & 2015/863/FLI (RoHS 3) compliant				

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



T4U60 = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 18 for 2018) WW = Week Code (01 to 53)



#### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>RM</sub>	60	V
Average Rectified Output Current	Io	4	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	25	А

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 5)	R <sub>θJA</sub>	110	°C/W
Typical Thermal Resistance Junction to Case (Note 5)	Rejc	10	°C/W
Typical Thermal Resistance Junction to Ambient (Note 6)	R <sub>0JA</sub>	70	°C/W
Typical Thermal Resistance Junction to Case (Note 6)	R <sub>θJC</sub>	4	°C/W
Total Power Dissipation (Note 5)	Ртот	1.4	W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +175	°C

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

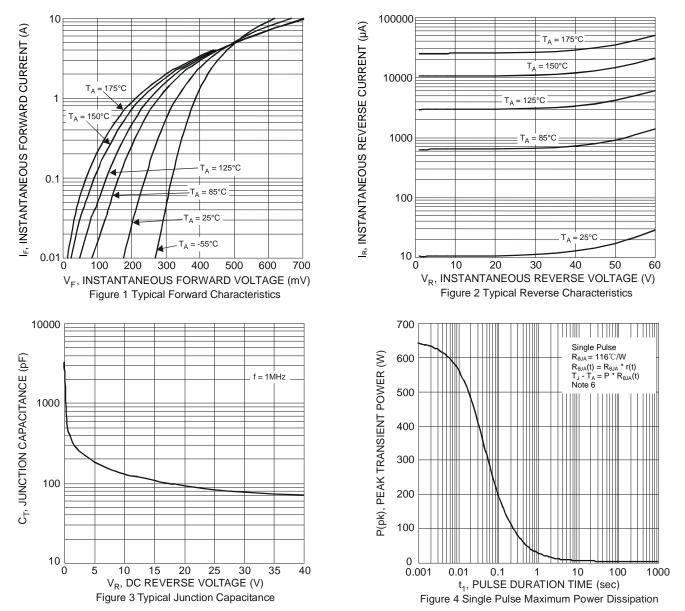
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop	VF		0.38 0.46 0.33 0.45	 0.52 	V	$\begin{split} I_F &= 2A, \ T_J = +25^{\circ}C \\ I_F &= 4A, \ T_J = +25^{\circ}C \\ I_F &= 2A, \ T_J = +125^{\circ}C \\ I_F &= 4A, \ T_J = +125^{\circ}C \end{split}$
Leakage Current (Note 7)	I <sub>R</sub>		30 6	150 —	μA mA	$V_R = 60V, T_J = +25^{\circ}C$ $V_R = 60V, T_J = +125^{\circ}C$
Total Capacitance	Ст	_	180	_	pF	V <sub>R</sub> = 5V, f = 1MHz

Notes:

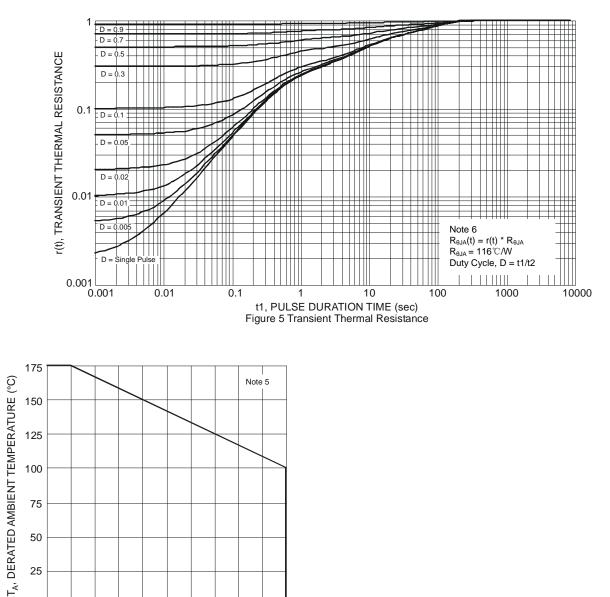
Device mounted on FR-4 substrate, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.
Device mounted on FR-4 substrate, 2 oz. Copper, 1 sq. inch Cu pad.
Short duration pulse test used to minimize self-heating effect.











50

25

0 ∟ 0

6

12 18 24 30 36 42 48

V<sub>R</sub>, DC REVERSE VOLTAGE (V) Figure 6 Operating Temperature Derating

54

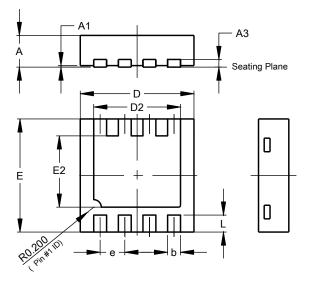
60



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

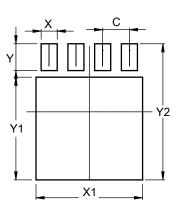
#### U-DFN3030-8



U-DFN3030-8					
Dim	Min	Max	Тур		
Α	0.57	0.63	0.60		
A1	0	0.05	0.02		
A3	-	-	0.15		
b	0.29	0.39	0.34		
D	2.90	3.10	3.00		
D2	2.19	2.39	2.29		
е	-	-	0.65		
E	2.90	3.10	3.00		
E2	1.64	1.84	1.74		
L	0.30	0.60	0.45		
All	All Dimensions in mm				

# Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



U-DFN3030-8

Dimensions	Value (in mm)	
С	0.650	
Х	0.390	
X1	2.590	
Y	0.650	
Y1	2.490	
Y2	3.300	



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