



#### SDM40E20LSQ/AQ

#### DUAL SURFACE MOUNT SCHOTTKY BARRIER DIODE

#### **Product Summary**

V <sub>R</sub> (V)	I <sub>F</sub> (A)	V <sub>F</sub> Max (V) @ +25°C	I <sub>R</sub> Max (μA) @ +25°C
20	0.4	0.43	250

## Applications

- DC-DC Converters
- Mobile Telecommunications
- Blocking Diodes
- Reverse Polarity Protection

#### Features and Benefits

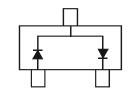
- Very Low Forward Voltage Drop
- Guard Ring Construction for Transient Protection
- High Conductance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

#### **Mechanical Data**

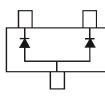
- Case: SOT23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Lead Free Plating (Matte Tin Finish Annealed over Alloy 42 Leadframe). Solderable per MIL-STD-202, Method 208 <sup>(3)</sup>
- Polarity: See Diagram
- Weight: 0.008 grams (Approximate)



Top View



SDM40E20LSQ



SDM40E20LAQ

#### Ordering Information (Note 5)

Part Number	Compliance	Case	Packaging
SDM40E20LSQ-7-F	Automotive	SOT23	3000/Tape & Reel
SDM40E20LAQ-7	Automotive	SOT23	3000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

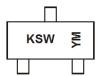
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

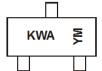
4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.

5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



 $\begin{array}{l} \mathsf{KSW} = \mathsf{SDM40E20LSQ} \ \mathsf{Product} \ \mathsf{Type} \ \mathsf{Marking} \ \mathsf{Code} \ \mathsf{YM} = \mathsf{Date} \ \mathsf{Code} \ \mathsf{Marking} \ \mathsf{Y} = \mathsf{Year} \ (\mathsf{ex:} \ \mathsf{G} = \mathsf{2019}) \ \mathsf{M} = \mathsf{Month} \ (\mathsf{ex:} \ \mathsf{9} = \mathsf{September}) \end{array}$ 



KWA = SDM40E20LAQ Product Type Marking Code YM = Date Code Marking

M = Month (ex: 9 = September)

Date Code Key

Notes:

Year	2015	2016	2017	2018	3 201	9 20	020	2021	2022	2023	2024	2025
Code	С	D	E	F	G		Н	I	J	К	L	М
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	g Sep	Oct	Nov	Dec
Code	1	2	3	1	5	6	7	8	٩	0	N	П

Y = Year (ex: G = 2019)M = Month (ex: G = Sontember)



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	20	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	14	V
Forward Continuous Current (Note 6)	l <sub>F</sub>	0.4	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	2	А

## **Thermal Characteristics**

Characteristic		Symbol	Value	Unit	
Power Dissipation (Note 6)		<b>D</b>	225	mW	
	(Note 7)	PD	300		
Typical Thermal Resistance Junction to Ambient (Note 6			444	°C 41/	
	(Note 7)	$R_{ heta}$ JA	333	°C/W	
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-65 to +125	°C	

### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

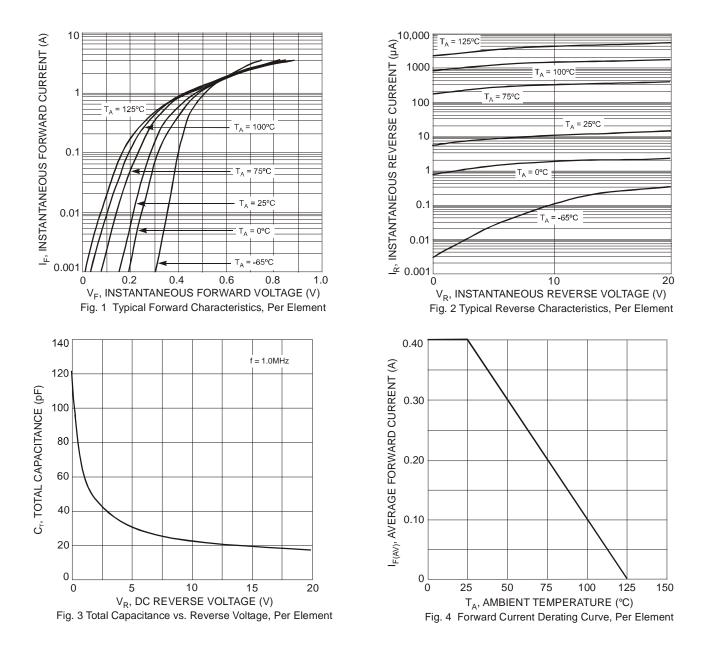
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Breakdown Voltage (Note 8)	V <sub>(BR)R</sub>	20	_		V	$I_{R} = 0.5 mA$
Forward Voltage Drop	\/-	_	_	0.310	V	$I_{\rm F} = 0.1 {\rm A}$
Forward voltage Drop	VF			0.430		I <sub>F</sub> = 0.5A
Leakage Current (Note 8)	I <sub>R</sub>	_	_	100		V <sub>R</sub> = 10V
Leakage Current (Note 8)				250	μA	$V_R = 20V$
Total Capacitance	CT	_	120		pF	$f = 1MHz, V_R = 0V_{DC}$

Notes: 6. Device mounted on FR-5 1.0 x 0.75 x 0.062 inch PCB pad layout.

Device mounted on Alumina PCB, 0.4 inch x 0.3 inch x 0.024 inch pad layout.
Short duration pulse test used to minimize self-heating effect.



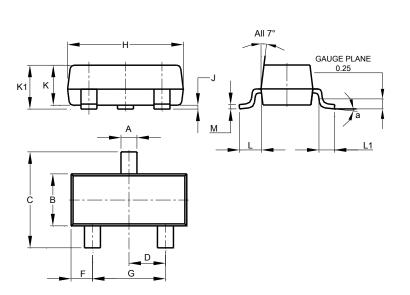
# SDM40E20LSQ/AQ





### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

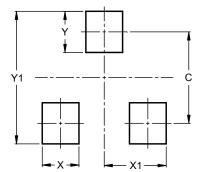


SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	0°	8°					
All	Dimens	ions in	mm				

### Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23



#### SOT23



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