ZXMD63P03X

### **DUAL 30V P-CHANNEL ENHANCEMENT MODE MOSFET**

## **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>
-30V	0.185Ω	-2.0A

## **Description**

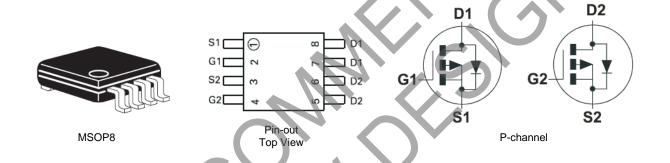
This new generation of high density MOSFETs from Diodes Incorporated utilises a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.

## **Features**

- Low On-resistance
- Fast Switching Speed
- Low Threshold
- Low Gate Drive
- Low Profile SOIC Package

## **Applications**

- DC DC Converters
- Power Management Functions
- Disconnect Switches
- Motor Control



## **Ordering Information**

Part Number	Device Marking	Reel Size (inches)	Tape Width (mm)	Quantity Per Reel
ZXMD63P03XTA	ZXM63P03	7	12mm Embossed	1000 Units
ZXMD63P03XTC	ZXM63P03	13	12mm Embossed	4000 Units



## **Absolute Maximum Ratings**

PARAMETER	SYMBOL	P-CHANNEL	UNIT
Drain-Source Voltage	V <sub>DSS</sub>	-30	V
Gate- Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current (V <sub>GS</sub> =-4.5V, T <sub>A</sub> =25°C) (b) (d) (V <sub>GS</sub> =-4.5V, T <sub>A</sub> =70°C) (b) (d)	I <sub>D</sub>	-2.0 -1.6	A
Pulsed Drain Current (c)(d)	I <sub>DM</sub>	-9.6	A
Continuous Source Current (Body Diode)(b)(d)	Is	-1.4	A
Pulsed Source Current (Body Diode)(c)(d)	I <sub>SM</sub>	-9.6	A
Power Dissipation at T <sub>A</sub> =25°C (a)(d) Linear Derating Factor	P <sub>D</sub>	0.87 6.9	W mW/°C
Power Dissipation at T <sub>A</sub> =25°C (a)(e) Linear Derating Factor	P <sub>D</sub>	1.04 8.3	W mW/°C
Power Dissipation at T <sub>A</sub> =25°C (b)(d) Linear Derating Factor	P <sub>D</sub>	1.25 10	w W/°C
Operating and Storage Temperature Range	T <sub>j</sub> :T <sub>stg</sub>	-55 to +150	°C

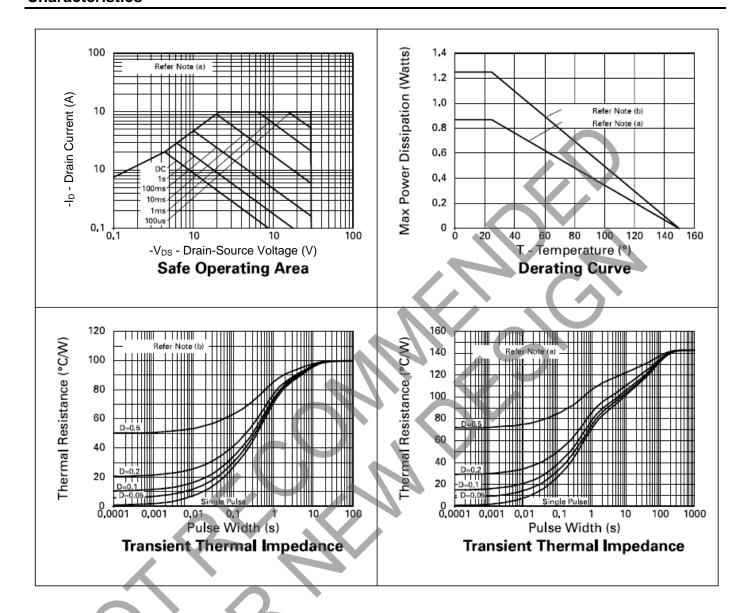
## **Thermal Resistance**

PARAMETER	SYMBOL	VALUE	UNIT
Junction to Ambient (a)(d)	R <sub>eJA</sub>	143	°C/W
Junction to Ambient (b)(d)	$R_{\theta JA}$	100	°C/W
Junction to Ambient (a)(e)	Reja	120	°C/W

- (a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions
- (b) For a device surface mounted on FR4 PCB measured at t≤10 secs.
- (c) Repetitive rating pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph.
- (d) For device with one active die.
- (e) For device with two active die running at equal power.



### **Characteristics**



## **NOT RECOMMENDED FOR NEW DESIGN -NO ALTERNATE PART**

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# **Electrical Characteristics** (@ $T_A = +25$ °C, unless otherwise specified.)

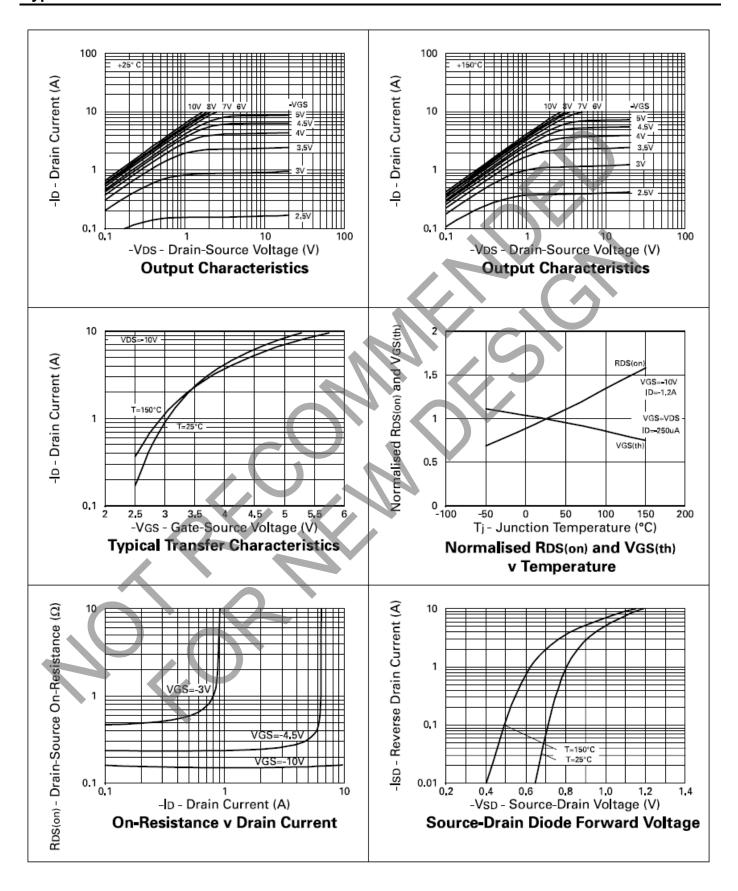
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS	
STATIC							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	-30			V	I <sub>D</sub> =-250μA, V <sub>GS</sub> =0V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			-1	μА	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V	
Gate-Body Leakage	I <sub>GSS</sub>			±100	nA	$V_{GS}$ = $\pm 20V$ , $V_{DS}$ = $0V$	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	-1.0			V	$I_{D} = -250 \mu A, V_{DS} = V_{GS}$	
Static Drain-Source On-State Resistance (1)	R <sub>DS(on)</sub>			0.185 0.27	$\Omega$	V <sub>GS</sub> =-10V, I <sub>D</sub> =-1.2A V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-0.6A	
Forward Transconductance (3)	g <sub>fs</sub>	0.92			S	V <sub>DS</sub> =-10V,I <sub>D</sub> =-0.6A	
DYNAMIC (3)							
Input Capacitance	C <sub>iss</sub>		270		pF	V 25 V V 0V	
Output Capacitance	C <sub>oss</sub>		80		pF	V <sub>DS</sub> =-25 V, V <sub>GS</sub> =0V, f=1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>		30		pF		
SWITCHING (2) (3)							
Turn-On Delay Time	t <sub>d(on)</sub>		2.6		ns		
Rise Time	t <sub>r</sub>		4.8		ns	V <sub>DD</sub> =-15V, I <sub>D</sub> =-1.2A	
Turn-Off Delay Time	t <sub>d(off)</sub>		13.1		ns	$R_G=6.2\Omega$ , $R_D=6.2\Omega$ (Refer to test	
Fall Time	tf		9.3		ns	circuit)	
Total Gate Charge	$Q_g$			7	nC	V 24V V 10V	
Gate-Source Charge	Qgs			1.2	nC	V <sub>DS</sub> =-24V,V <sub>GS</sub> =-10V, I <sub>D</sub> =-1.2A	
Gate Drain Charge	$Q_{gd}$		1	2	nC	(Řefer to test circuit)	
SOURCE-DRAIN DIODE							
Diode Forward Voltage (1)	V <sub>SD</sub>			-0.95	V	T <sub>j</sub> =25°C, I <sub>S</sub> =-1.2A, V <sub>GS</sub> =0V	
Reverse Recovery Time (3)	t <sub>rr</sub>		21.4		ns	T <sub>j</sub> =25°C, I <sub>F</sub> =-1.2A,	
Reverse Recovery Charge(3)	Q <sub>rr</sub>		15.7		nC	di/dt= 100A/μs	

#### NOTES:

<sup>(1)</sup> Measured under pulsed conditions. Width=300us. Duty cycle ≤2%.
(2) Switching characteristics are independent of operating junction temperature.
(3) For design aid only, not subject to production testing.

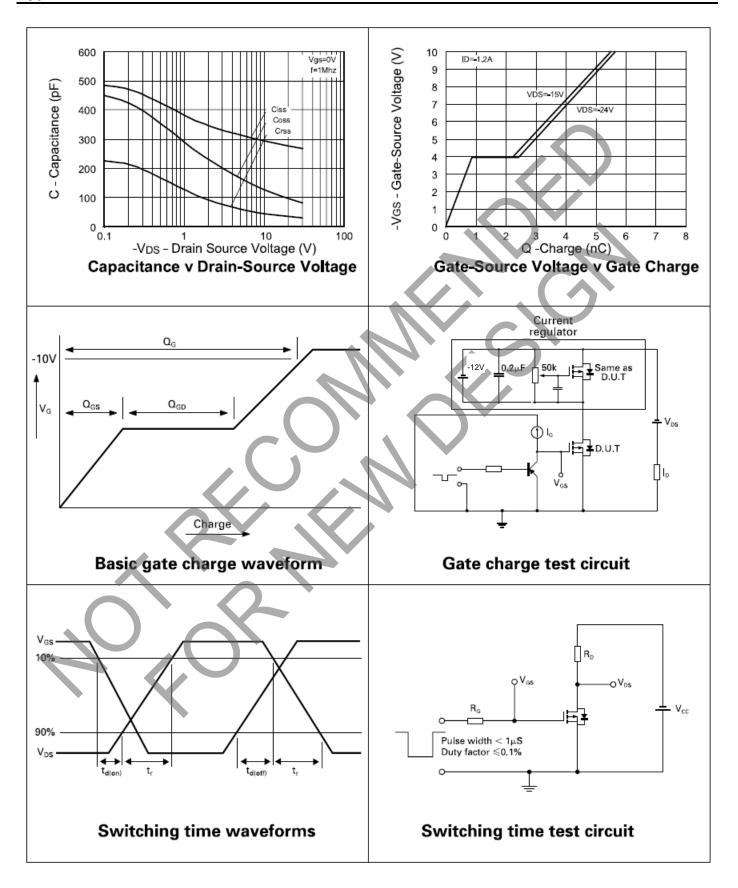


## **Typical Characteristics**





## **Typical Characteristics** (Cont.)

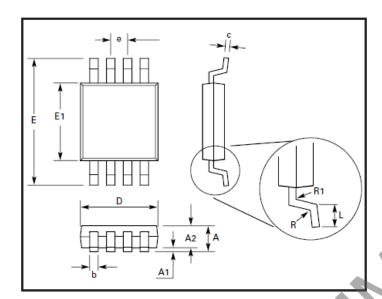




## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

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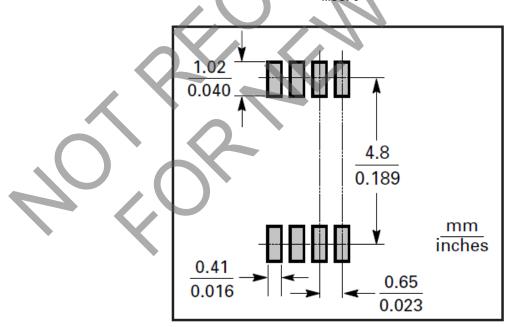


DIM	Millimeters		Inc	hes	
	MIN	MAX	MIN	MAX	
Α	0.91	1.11	0.036	0.044	
A1	0.10	0.20	0.004	0.008	
В	0.25	0.36	0.010	0.014	
С	0.13	0.18	0.005	0.007	
D	2.95	3.05	0.116	0.120	
е	0.65NOM		0.0256		
e1	0.33NOM		0.0128		
E	2.95	3.05	0.116	0.120	
Н	4.78	5.03	0.188	0.198	
1	0.41	0.66	0.016	0.026	
θ°	0°	6°	0°	6°	

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

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