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TFT LCD Module

Product Specification

DT070BTFT-PTS1

7.0" (1024(RGB) x 600 DOTS) TFT Module
with PCAP

May 23, 2016

Revision Record

REV	CHANGES	DATE
0.0 (Ref. 1.0 20150614)	First release	Aug 13, 2015
1.0 (Ref. 1.0 20160218)	- In 3. General Information, changed the outline dimension from "166.80 x 102.05 x 4.75" to "164.90 x 100.00 x 4.50", and added "Driver IC of PCAP: FT5426"; - Updated 4. Outline Drawing with the new PCAP; - Updated PCAP signal interface in 5. Interface Signals; - Changed the Luminance from "180 Min." to "170 Min." in 9. Optical Specification;	Mar 16, 2016
2.0 (Ref. 1.1 20160512)	- Increased depth from "4.50" to "4.55" in section 3. General Information; - Changed controller/driver from "EK79001DCGB/EK73217ACGA" to "EK79001ECGA/EK73215BCGA" in section 3. General Information; - Updated drawing in section 4. Outline Drawing; - Added "Recommend connector Part No.: FH12S-40S-0.5SH" in section 5. Interface Signals.	May 23, 2016

Table of Content

1. Scope

This data sheet is to introduce the specification of DT070BTFT-PTS1, active matrix TFT module. It is composed of a color TFT-LCD panel, driver ICs, FPC, capacitive touch panel and a backlight unit. The 7.0" display area contains 1024x3 (RGB) x 600 pixels.

As to basic specification of the driver IC, refer to the IC specification and datasheet.

2. Application

Digital equipment which need color display, mobile navigator/video systems.

3. General Information

Item	Contents	Unit
Size	7.0	inch
Resolution	1024x3(RGB) x 600	/
Interface	LVDS	/
Technology Type	IPS TFT	/
Pixel Configuration	R.G.B. Stripe	
Outline Dimension (W x H x D)	164.90 x 100.00 x 4.55	mm
Active Area	154.21 x 85.92	mm
Backlight Type	LED	/
Controller/ driver	EK79001ECGA/EK73215BCGA	
Driver IC of PCAP	FT5426	
Weight	TBD	g

4. Outline Drawing

PIN	SYMBOL
1	VCOM
2	VDD
3	VDD
4	NC
5	RESET
6	STBYB
7	GND
8	RXIN0-
9	RXIN0+
10	GND
11	RXIN1-
12	RXIN1+
13	GND
14	RXIN2-
15	RXIN2+
16	GND
17	RXCLKIN+
18	RXCLKIN-
19	GND
20	RXIN3-
21	RXIN3+
22	GND
23	NC
24	NC
25	GND
26	GND
27	NC
28	SELB
29	AVDD
30	GND
31	LED-
32	LED-
33	SHLR
34	UPDN
35	VGL
36	NC
37	NC
38	VGH
39	LED+
40	LED+

NOTES:
1.DISPLAY TYPE: TFT
2.OPERATING TEMP:-20°C ~ 70°C
3.STORAGE TEMP: -30°C ~ 80°C
4.Backlight: LED
5.Driver IC OF CTP: FT5426
6.RoHS Compliance

Backlight LED 3*7=21

DETAIL A(2:1)

7.0"TFT
1024*RGB*600 DOTS
VIEWING ANGLE: ALL

TP P/N	NAME
1	SCL
2	SDA
3	VDD
4	RESET
5	INT
6	GND

DRAWN BY:	
CHECKED BY:	
APPROVED BY:	
CONFIRMED BY:	

TITLE: DT070BTFT-PTS1
DWG NO:
DWG NAME:
SCALE:　UNIT: mm
SHEET NO: OF

5. Interface Signals

Recommend connector Part No.: FH12S-40S-0.5SH

No	Symbol	Description	Remark
1	VCOM	Common voltage	
2	VDD	Power supply	
3	VDD	Power supply	
4	NC	No connection	
5	RESET	Global reset pin	
6	STBYB	Standby mode	
7	GND	Power Ground	
8	RXIN0-	Differential Data Input, CH0 (Negative)	
9	RXIN0+	Differential Data Input, CH0 (Positive)	
10	GND	Power Ground	
11	RXIN1-	Differential Data Input, CH1 (Negative)	
12	RXIN1+	Differential Data Input, CH1 (Positive)	
13	GND	Power Ground	
14	RXIN2-	Differential Data Input, CH2 (Negative)	
15	RXIN2+	Differential Data Input, CH2 (Positive)	
16	GND	Power Ground	
17	RXCLKIN-	Differential Clock Input (Negative)	
18	RXCLKIN+	Differential Clock Input (Positive)	
19	GND	Power Ground	
20	RXIN3-	Differential Data Input, CH3 (Negative)	
21	RXIN3+	Differential Data Input, CH3 (Positive)	
22	GND	Power Ground	
23	NC	No connection	
24	NC	No connection	
25	GND	Power Ground	
26	NC	No connection	
27	NC	No connection	
28	SELB	In LVDS interface connected HSD to FPC for Pin Setting HSD="L":8 bit ; HSD="H":6 bit	
29	AVDD	Power supply for analog circuits	
30	GND	Power Ground	
31	LED-	Power for LED backlight (Cathode)	
32	LED-	Power for LED backlight (Cathode)	
33	SHLR	Source Right or Left sequence control. Normally pull high.	Note
34	UPDN	Gate Up or Down scan control. Normally pull low	Note
35	VGL	Gate OFF Voltage	
36	NC	No connection	
37	NC	No connection	
38	VGH	Gate ON Voltage	
39	LED+	Power for LED backlight (Anode)	
40	LED+	Power for LED backlight (Anode)	

Note:

UPDN	SHLR	Function
0	1	Normal Display
0	0	Inverse Left and Right
1	1	Inverse Up and Down
1	0	Inverse Left and Right Inverse Up and Down

PCAP signal interface:

Pin	Symbol	Description
1	SCL	I2C clock signal
2	SDA	I2C data signal
3	VDD	Power supply
4	RESET	Reset Pin
5	INT	Interrupt output Pin
6	GND	Power ground

6. Absolute Maximum Ratings

6.1 Electrical absolute maximum ratings

Parameter	Symbol	MIN	MAX	Unit	Remark
Power Supply Voltage	VDD	-0.3	5.0	V	
	AVDD	-0.5	15.0	V	
	VGH	-0.3	40.0	V	
	VGL	-20	0.3	V	
Logic Signal Input / Output Voltage	V_{IOVCC}	-0.3	VDD+0.5	V	
Current of LED	ILED	0	175	mA	

Notes:
1. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.
2. Please be sure users are grounded when handing LCD Module.

6.2 Environment conditions

Parameter	Symbol	MIN	MAX	Unit	Remark
Operating Temperature	TOPR	-20	+70	°C	
Storage Temperature	TSTG	-30	+80	°C	

Notes:
1. The response time will become lower when operated at low temperature.
2. Background color changes slightly depending on ambient temperature.
   The phenomenon is reversible.
3. Ta<=40℃:85%RH MAX.
   Ta>=40℃:Absolute humidity must be lower than the humidity of 85%RH at 40℃.

7. Electrical Specifications

7.1 Electrical characteristics

GND=0V, Ta=25°C

Item		Symbol	MIN	TYP	MAX	Unit	Remark
Power Supply		VDD	2.6	3.3	3.6	V	
		VCOM	2.85		3.45	V	
		AVDD	9.4	9.6	9.8	V	
		VGH	17	18	19	V	
		VGL	-6.6	-6.0	-5.4	V	
Input Voltage	H	VIH	0.8VDD	---	VDD	V	VDD=2.8V
	L	VIL	0	---	0.2VDD	V	
Current Consumption		I_{VDD}	---	30	45	mA	
		I_{AVDD}	---	35	45	mA	

7.2 LED backlight

Ta=25°C

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Forward current	IL	---	140	---	mA	
Forward voltage	VL	---	9.6	---	V	Note 1
LED life time	---	---	25,000	---	Hr	Note 2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25°C and IL=140mA.

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and IL=140mA. The LED lifetime could be decreased if operation IL is larger than 140mA.

Note 3: LED power consumption is around 0.297W.

8. Command / AC Timing

## 8.1 Timing Characteristics of Input Signals

ITEM			SYMBOL	MIN	TYP	MAX	UNIT
LVDS Input Signal Sequence	CLK Frequency		tclk	45	51.2	57	MHz
LCD Input Signal Sequence (Input LVDS Transmitter)	DENA	Horizontal	Horizontal Total Time	t_H	1324	1344	1364	tCLK
			Horizontal Effective Time	t_{HA}		1024		tCLK
			Horizontal Blank Time	t_{HB}	300	320	340	tCLK
		Vertical	Vertical Total Time	t_V	625	635	645	t_H
			Vertical Effective Time	t_{VA}		600		t_H
			Vertical Blank Time	t_{VB}	25	35	45	t_H

8.2 Timing Sequence (Timing Chart)

8.2.1. Horizontal Timing Sequence
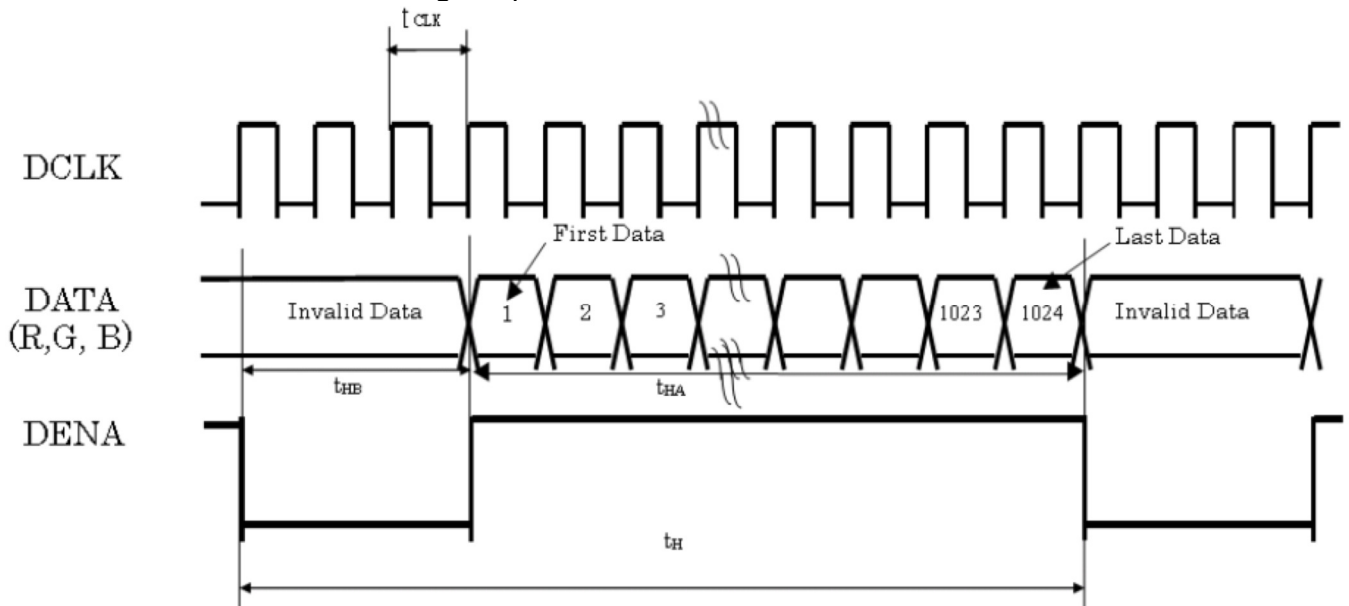
8.2.2 Vertical Timing Sequence
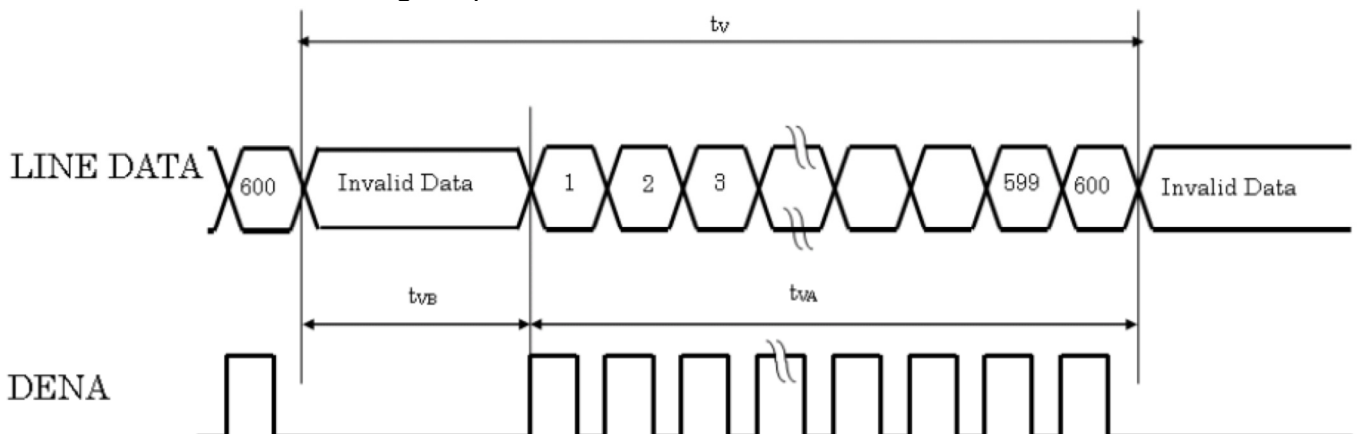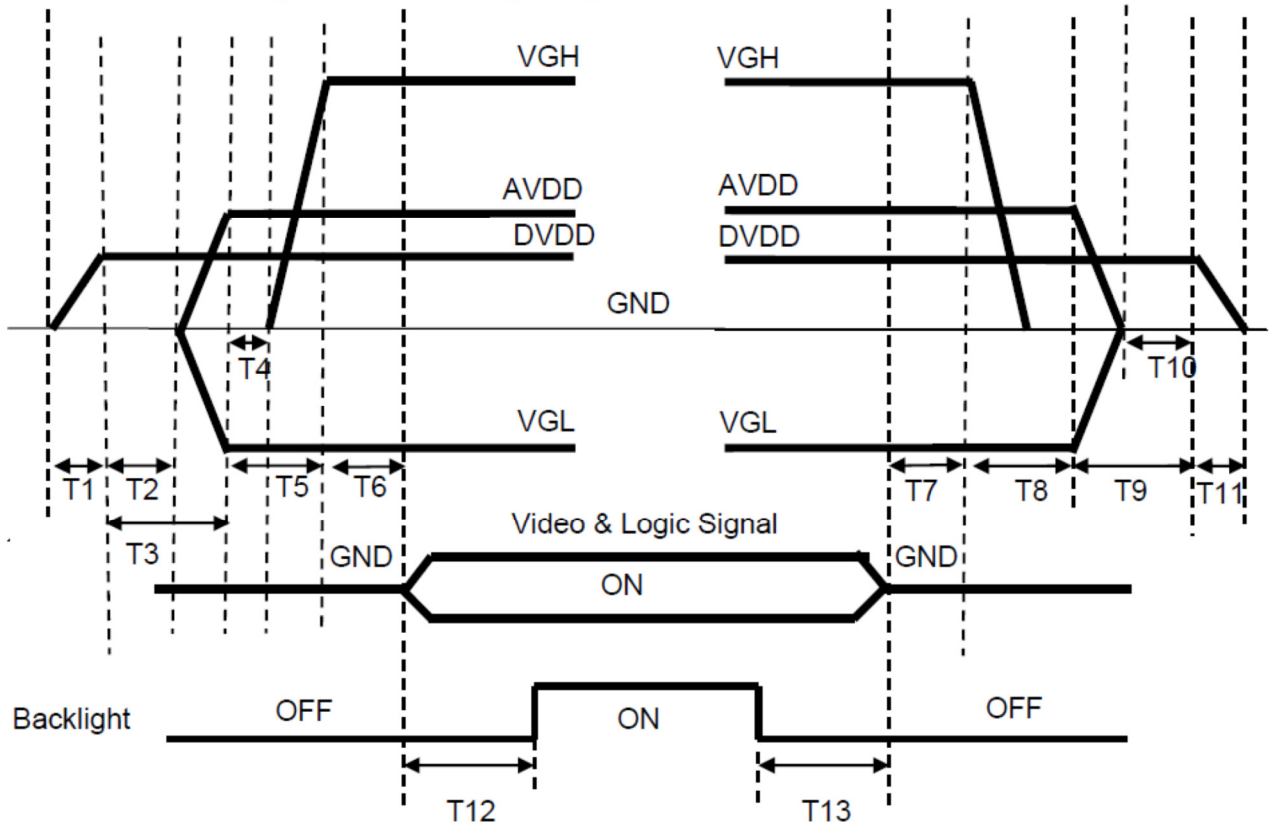
8.2.3 LVDS Input Data Mapping

6bits LVDS Input
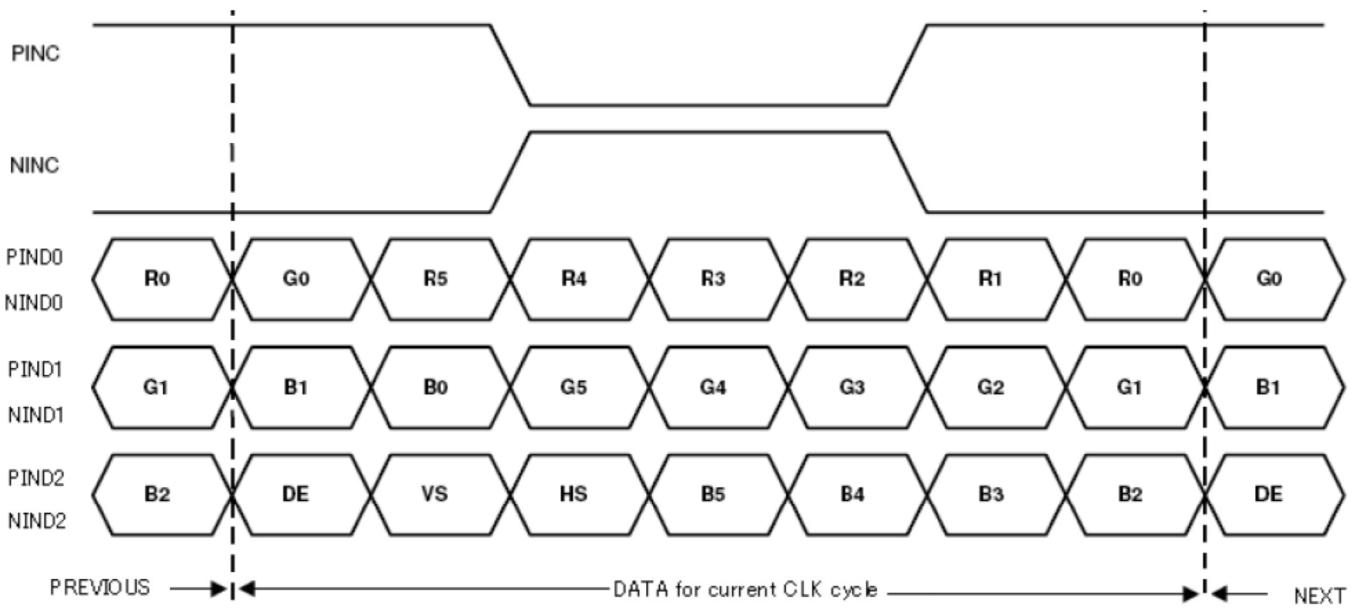
8bits LVDS Input
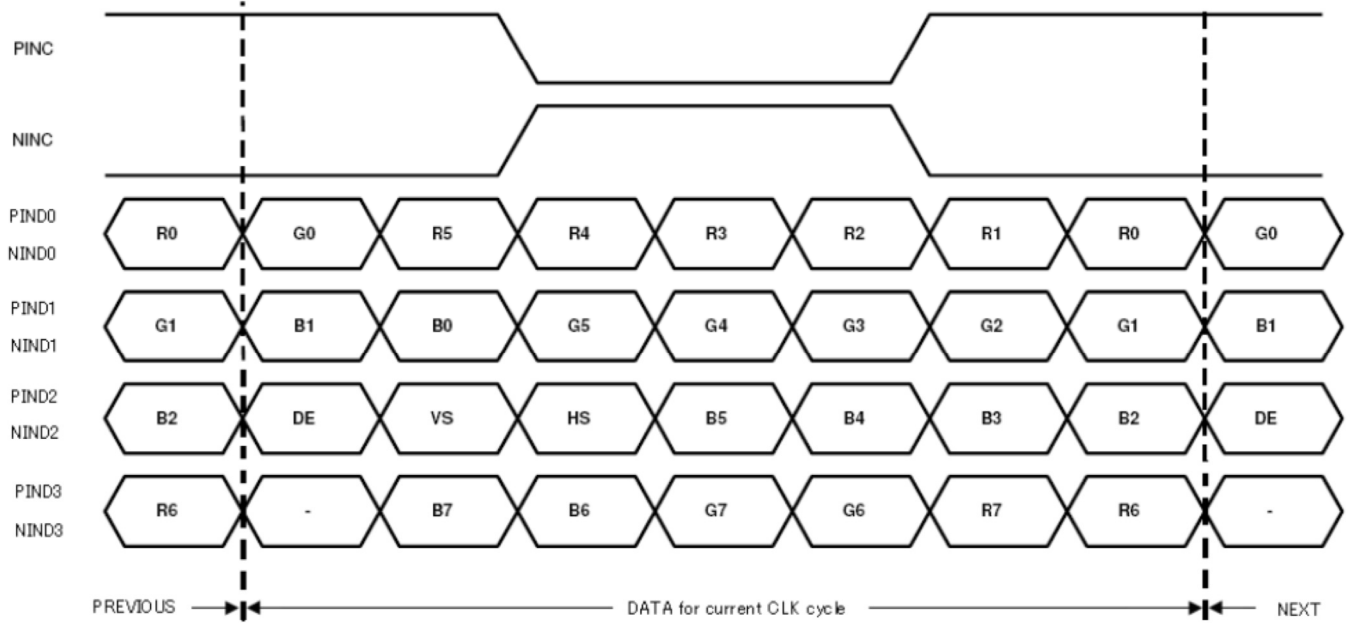
8.3 Power ON/Off Sequence

Power On：DVDD→AVDD/VGL →VGH →Video &Logic Signal→Backlight

Power Off：Backlight→Video &Logic Signal→ VGH→AVDD/VGL→DVDD

$0 < T1 \leqq 10ms$ $T7 > 0ms$

$T2 > 0ms$ $T8 > 0ms$

$T3 > 20ms$ $T9 > 0ms$

$T4 > 0ms$ $T10 > 0ms$

$T5 > 10ms$ $0 < T11 \leqq 10ms$

$0 < T6 \leqq 10ms$ $T13 \geqq 200ms$

$T12 \geqq 200ms$

9. Optical Specification

Ta=25°C

Item		Symbol	Condition	MIN	TYP	MAX	Unit	Remark
Contrast ratio		CR	θ=0°	---	800	---		Note 1, 2
Response time		Tr+Tf	25°C	---	25	---	ms	Note 1, 3
View angles		ΘT	CR≧10	80	85	---	Degree	Note 4
		ΘB		80	85	---		
		ΘL		80	85	---		
		ΘR		80	85	---		
Chromaticity	White	x	Brightness is on	Typ - 0.03	0.290	Typ + 0.03		Note 1, 5
		y			0.331			
	Red	x			0.632			
		y			0.311			
	Green	x			0.297			
		y			0.536			
	Blue	x			0.140			
		y			0.154			
Luminance		L		170	210	---	cd/m^2	Note 1, 6
Uniformity		U		80	90	---	%	Note 1, 7

Note 1: Definition of optical measurement system.

Temperature = 25℃(±3℃)

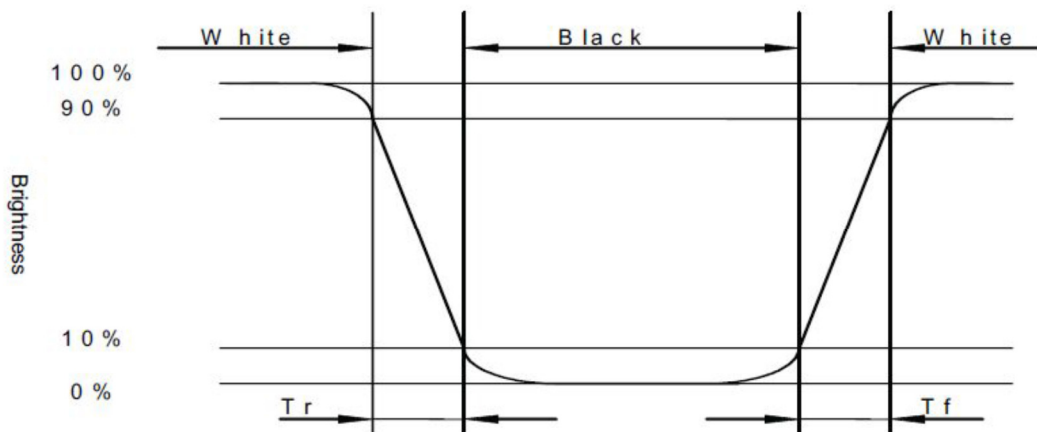LED back-light: ON, Environment brightness < 150 lx
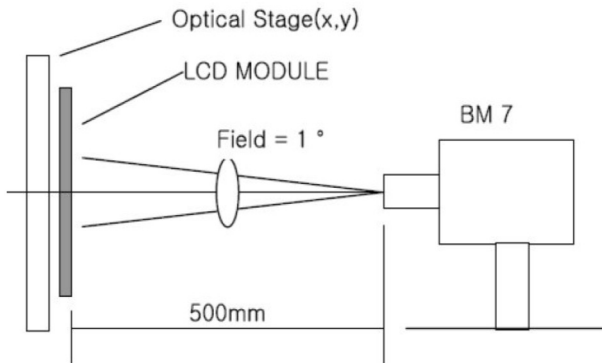
Note 2: Contrast ratio is defined as follow:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$
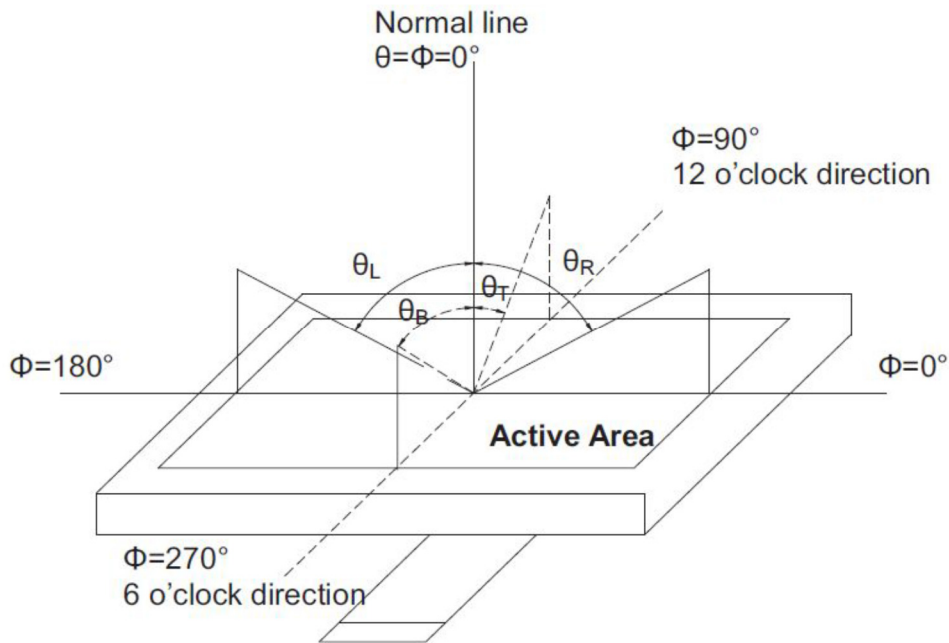
Note 3: Response time is defined as follow:

Response time is the time required for the display to transition from black to white (Rise time, Tr) and from white to black (Decay Time, Tf).
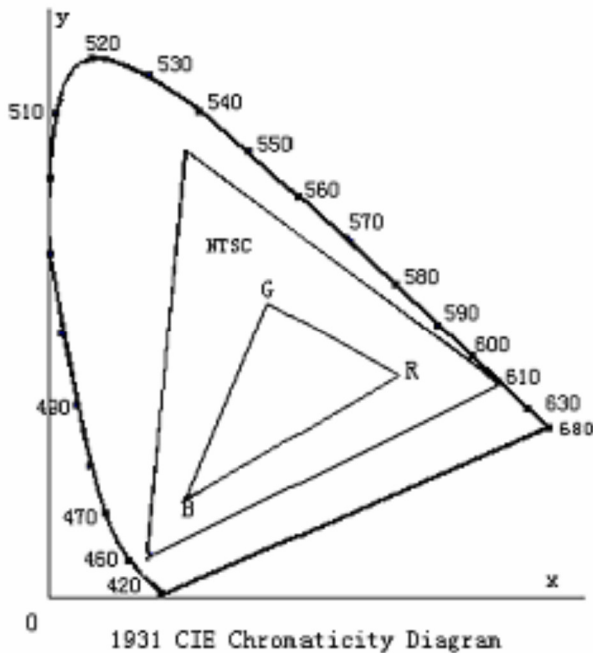
Note 4: Viewing angle range is defined as follow:

Viewing angle is measured at the center point of the LCD.

Note 5: Color chromaticity is defined as follow (CIE1931)

Color coordinates measured at center point of LCD.

1931 CIE Chromaticity Diagram

$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

Note 6: Luminance is defined as follow:

Luminance is defined as the brightness of all pixels "White" at the center of display area on optimum contrast.

Note 7: Luminance Uniformity is defined as follow:

Active area is divided into 9 measuring areas (Refer Fig.2). Every measuring point is placed at the center of each measuring area.

$$\text{Uniformity (U)} = \frac{\text{Minimum Luminance(brightness) in 9 points}}{\text{Maximum Luminance(brightness) in 9 points}}$$
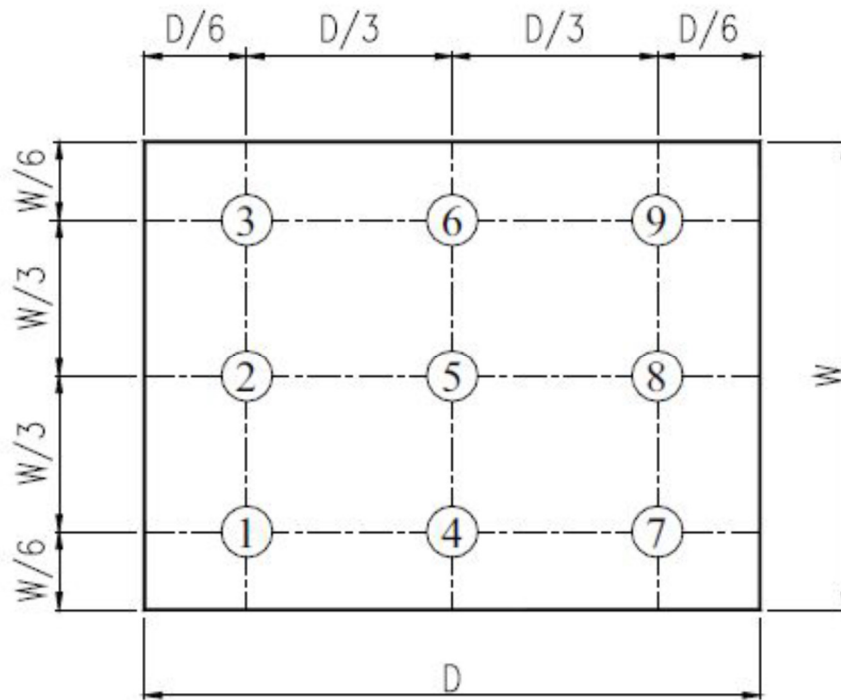
Fig. 2 Definition of uniformity

10. Environmental / Reliability Tests

No	Test Item	Condition	Judgment Criteria
1	High Temp Operation	Ta=+70°C, 96hrs	Per table below
2	Low Temp Operation	Ta=-20°C, 96hrs	Per table below
3	High Temp Storage	Ta=+80°C, 96hrs	Per table below
4	Low Temp Storage	Ta=-30°C, 96hrs	Per table below
5	High Temp & High Humidity Storage	Ta=+60°C, 90% RH, 96hrs	Per table below (polarizer discoloration is excluded)
6	Thermal Shock (Non-operation)	-30°C 30 min ~ +80°C 30 min, Change time: 5 min, 5 cycles	Per table below
7	ESD (Operation)	Air discharge: ±8KV, Contact discharge: ±4KV	Per table below
8	Vibration (Non-operation)	10Hz~150Hz, 100m/s^2, 120min	Per table below
9	Shock (Non-operation)	Half- sine wave,300m/s^2,11ms	Per table below
10	Package Drop Test	Height: 80cm, 1 corner, 3 edges, 6 surfaces	Per table below

Inspection	Criterion (after test)
Appearance	No crack on the FPC, on the LCD panel
Alignment of LCD panel	No bubbles in the LCD panel No other defects of alignment in active area
Electrical current	Within device specifications
Function / Display	No broken circuit, no short circuit or no black line No other defects of display

11. Precautions for Use of LCD Modules

11.1 Safety
The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

11.2 Handling
a. The LCD and touch panel is made of plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
b. Do not handle the product by holding the flexible pattern portion in order to assure the reliability.
c. Transparency is an important factor for the touch panel. Please wear clear finger sacks, gloves and mask to protect the touch panel from finger print or stain and also hold the portion outside the view area when handling the touch panel.
d. Provide a space so that the panel does not come into contact with other components.
e. To protect the product from external force, put a covering lens (acrylic board or similar board) and keep an appropriate gap between them.
f. Transparent electrodes may be disconnected if the panel is used under environmental conditions where dew condensation occurs.
g. Property of semiconductor devices may be affected when they are exposed to light, possibly resulting in IC malfunctions.
h. To prevent such IC malfunctions, your design and mounting layout shall be done in the way that the IC is not exposed to light in actual use.

11.3 Static electricity
a. Ground soldering iron tips, tools and testers when they are in operation.
b. Ground your body when handling the products.
c. Power on the LCD module before applying the voltage to the input terminals.
d. Do not apply voltage which exceeds the absolute maximum rating.
e. Store the products in an anti-electrostatic bag or container.

11.4 Storage
a. Store the products in a dark place at +25°C±10°C with low humidity (40% RH to 60% RH). Don't expose to sunlight or fluorescent light.
b. Storage in a clean environment, free from dust, active gas, and solvent.

11.5 Cleaning
a. Do not wipe the touch panel with dry cloth, as it may cause scratch.
b. Wipe off the stain on the product by using soft cloth moistened with ethanol. Do not allow ethanol to get in between the upper film and the bottom glass. It may cause peeling issue or defective operation. Do not use any organic solvent or detergent other than ethanol.

11.6 Cautions for installing and assembling
Bezel edge must be positioned in the area between the Active area and View area. The bezel may press the touch screen and cause activation if the edge touches the active area. A gap of approximately 0.5mm is needed between the bezel and the top electrode. It may cause unexpected activation if the gap is too narrow. There is a tolerance of 0.2 to 0.3mm for the outside dimensions of the touch panel and tail. A gap must be made to absorb the tolerance in the case and connector.
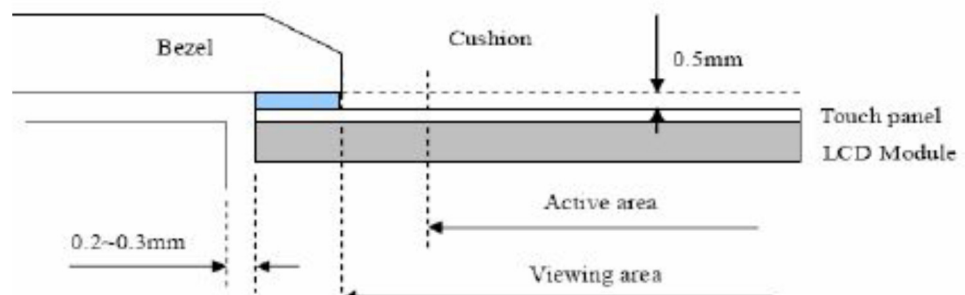
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