

RS1A(H) THRU RS1M(H)

1.0AMP SURFACE MOUNT GLASS FAST RECOVERY RECTIFIER

Features

- Fast switching for high efficiency
- Low Power Loss, High Efficiency
- High current capability
- For Use in Low Voltage Application
- Plastic Case Material has UL Flammability

Classication Rating 94V-0

Mechanical Data

- Case: Molded plastic SMA
- Terminals: Plated leads solderable per MIL-STD-750,Method 2026 guaranteed
- Polarity:Cathode Band or Cathode Notch
- Mounting Position: Any
- Making: Type Number

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified

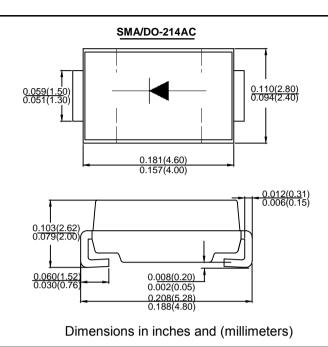
Single phase, half wave, 60Hz, resistive or inductive load

For capacitive load derate current by 20%

Type Number	SYMBOL	RS1A(H)	RS1B(H)	RS1D(H)	RS1G(H)	RS1J(H)	RS1K(H)	RS1M(H)	Unit
Maximum Recurrent Peak Reverse Voltage	VRRM	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	VRMS	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	VDC	50	100	200	400	600	800	1000	V
Average Rectified Output Current @T∟ =100°C	IF(AV)	1.0							А
Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	Ifsm	30							A
Rating for fusing (t<8.3ms)	l ² t	3.74							A²s
Forward Voltage @IF=1.0A	V _{FM}	1.3							V
Peak Reverse Current @T _A =25 °C	1	5.0 200							uA
At Rated DC Blocking Voltage@T _A =125°C	I _R								
Maximum Reverse Recovery Time (Note1)	Trr	150 250 500)	ns		
Typical Junction Capacitance (Note 2)	CJ	7							pF
Typical Thermal Resistance Junction to Ambient(Note 3)	R⊕∧ R⊕∟	100 32							℃/W
Operating Temperature Range	TJ	-55 to+150							°C
Storage Temperature Range	Tstg	-55 to +150							°C

Note: 1.Reverse Recovery Test Conditions:IF=0.5A,IR=1.0A,IRR=0.25A.

- 2. Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C
- 3. Device mounted on FR-4 substrate, 1"*1", 2oz, single-sided, PC boards with 0.1"*0.15" copper pad.



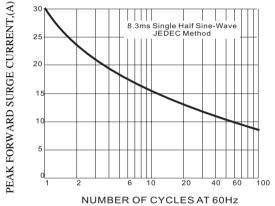






2.0 1.0 0.5 0.5 0.5 0.5 0.5 0.5 0.5 100 150 Lead Temperature (°C)

FIG.3MAXIMUM NON-REPEITIVE SURGE CURRENT





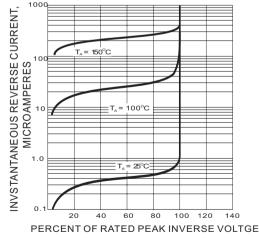


FIG.2TYPICAL FORWARD CHARACTERISTICS

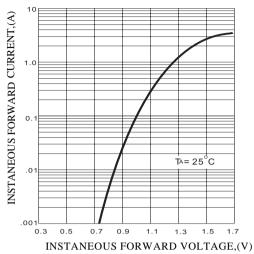
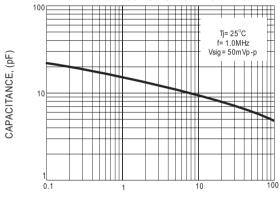
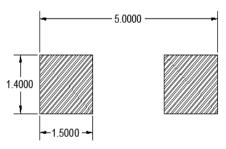


FIG.4TYPICAL JUNCTION CAPACITANCE



REVERSE VOLTAGE, (V)

SMA PAD LAYOUT





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