## SS22 THRU SS225

### 2.0 AMP SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

## Features

- Schottky Brrier Chip
- Low Power Loss,High Efficiency
- Ideally Suited for Automatic Assembly
- Surge Overload Rating to 50A Peak
- Plastic Case Material has UL Flammability

Classification Rating 94V-0

## Mechanical Data

- Case: Molded plastic SMA
- Terminals: Plated leads solderable per MIL-STD-750,Method 2026 guaranteed
- Polarity: Color band denotes cathode end
- Mounting Position: Any
- Making: Type Number


Dimensions in inches and (millimeters)

## Maximum Ratings and Electrical Characteristics

Rating at $25^{\circ} \mathrm{C}$ ambient temperature unless otherwise specified
Single phase, half wave, 60 Hz , resistive or inductive load
For capacitive load derate current by $20 \%$

| Type Number | SYMBOL | $\begin{aligned} & \text { SS } \\ & 22 \end{aligned}$ | $\begin{array}{\|l\|} \hline \text { SS } \\ 23 \end{array}$ | $\begin{aligned} & \text { SS } \\ & 24 \end{aligned}$ | $\begin{aligned} & \text { SS } \\ & 245 \end{aligned}$ | $\begin{aligned} & \text { SS } \\ & 25 \end{aligned}$ | $\begin{aligned} & \text { SS } \\ & 26 \end{aligned}$ | $\begin{aligned} & \text { SS } \\ & 28 \end{aligned}$ | $\begin{aligned} & \text { SS } \\ & 210 \end{aligned}$ | $\begin{array}{\|l} \text { SS } \\ 215 \end{array}$ | $\begin{array}{\|l\|} \hline \text { SS } \\ 220 \end{array}$ | $\begin{aligned} & \hline \text { SS } \\ & 225 \end{aligned}$ | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Maximum Recurrent Peak Reverse Voltage | VRRM | 20 | 30 | 40 | 45 | 50 | 60 | 80 | 100 | 150 | 200 | 250 | V |
| Maximum RMS Voltage | VRMS | 14 | 21 | 28 | 31 | 35 | 42 | 56 | 70 | 105 | 140 | 175 | V |
| Maximum DC Blocking Voltage | Vdc | 20 | 30 | 40 | 45 | 50 | 60 | 80 | 100 | 150 | 200 | 250 | V |
| Average Rectified Output Current $@ T L=100^{\circ} \mathrm{C}$ | lo | 2.0 |  |  |  |  |  |  |  |  |  |  | A |
| Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method) | Ifsm | 50 |  |  |  |  |  |  |  |  |  |  | A |
| Rating for fusing (t<8.3ms) | $1^{2} \mathrm{t}$ | 10.37 |  |  |  |  |  |  |  |  |  |  | $\mathrm{A}^{2} \mathrm{~s}$ |
| Forward Voltage @IF=2.0A | $V_{\text {FM }}$ |  | 0.5 |  |  | 0.7 |  |  | . 85 |  | . 92 | 0.95 | V |
| Peak Reverse Current @ $T_{A}=25^{\circ} \mathrm{C}$ | IR | 0.1 |  |  |  |  |  | 0.05 |  |  |  |  | mA |
| At Rated DC Blocking Voltage @ $\mathrm{T}_{\mathrm{A}}=100^{\circ} \mathrm{C}$ |  |  |  |  | 10 |  |  |  |  |  |  |  |  |
| Typical Junction Capacitance (Note 2) | CJ | 150 |  |  |  |  |  |  |  |  |  |  | pF |
| Typical Thermal Resistance per leg | RөJL | 88 |  |  |  |  |  |  |  |  |  |  | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Operating Temperature Range | TJ | -55 to +150 |  |  |  |  |  |  |  |  |  |  | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range | Tstg | -55 to +150 |  |  |  |  |  |  |  |  |  |  | ${ }^{\circ} \mathrm{C}$ |

Note: 1.Pulse Test with PW=300usec,1\%Duty Cycle.
2.Mounted on P.C.Board with $5.0 \mathrm{~mm}^{2}$ ( 0.13 mm thick) copper pad areas.

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Fig． 1 Forward Current Derating Curve


Fig． 3 Max Non－Repetitive Peak Fwd Surge Current


SMA PAD LAYOUT



Fig． 4 T ypical Reverse Characteristics（per element）


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