ESD1006HC4V5B



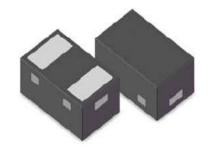
ESD1006HC4V5B Transient Voltage Suppressors ESD Protection Diode

General description

Silicon Protection Diode in a DFN1006 Package.

FEATURES

- 500 Watt peak pulse power (t/p=8/20)
- Capacitance Typ. 70 pF
- · Bidirectional configurations
- Low Leakage Current
- Low clamping voltage
- Protection one data line / Power Line to :
- IEC 61000-4-2 \pm 30Kv Contact \pm 30Kv air.
- IEC 61000-4-4 EFT 40A(5/50nS)
- IEC 61000-4-5 Lightning 45A(8/20uS)
- RoHS Compliant
- Green EMC
- Matte Tin(Sn) Lead Finish



DFN1006 Package



ELECTRICAL SYMBOL

Absolute Maximum Ratings $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

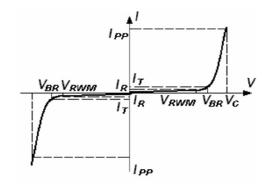
Symbol	Parameter	Value	Units	
Ppp	Peak pulse power	500	W	
TL	Max Lead Solder Temperature range (10 Second Duration)	260	°C	
T_{stg}	Storage Temperature Range	-55 to +150	°C	
Tر	Junction Temperature	-55 to +125	25 °C	
ESD	IEC61000-4-2 Air Discharge Contact Discharge	±30 ±30	KV	
EFT	IEC61000-4-4	40	Α	
Lightning	IEC61000-4-5	45	Α	

Device Marking:

Device Type	Marking	Shipping		
ESD1006HC4V5B	E3	10,000/Reel		

Electrical Parameter

Symbol	Parameter	
I _{PP}	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @ IPP	
V _{RWM}	Working Peak Reverse Voltage	
I _R	Maximum Reverse Leakage Current @ V _{RWM}	
I _T	Test Current	
V _{BR}	Breakdown Voltage @ I _T	

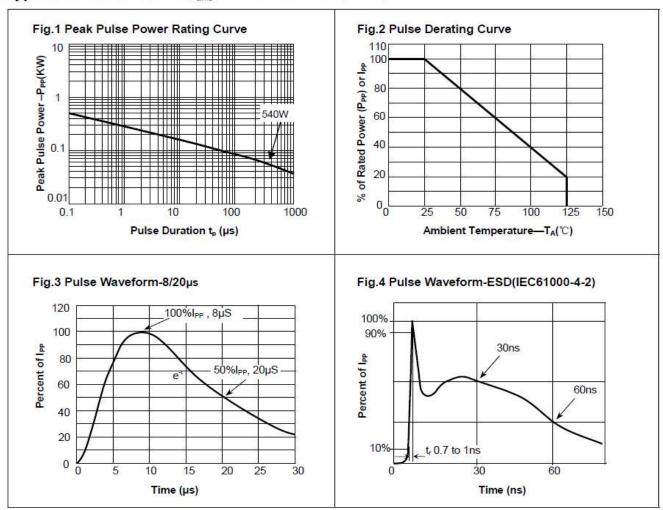


Electrical Characteristics (T_A = 25°C unless otherwise noted)

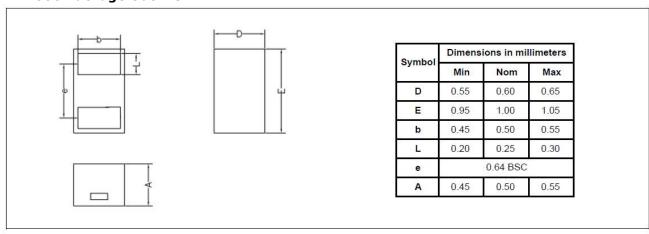
Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
V _{BR}	Reverse Breakdown Voltage	I _T =1mA	5.1		7.5	Volts
V _{RWM}	Reverse Stand-off Voltage		-4.5		4.5	Volts
I _R	Reverse Leakage Current	V _R =4.5V			0.2	uA
Vc	Clamping Voltage(IEC 61000-4-5)	Ipp=1A			5.5	Volts
Vc	Clamping Voltage(IEC 61000-4-5)	Ipp=20A			7.5	Volts
Vc	Clamping Voltage(IEC 61000-4-5)	Ipp=45A			11	Volts
Cj	Junction Capacitance	V _R =0V, f= 1MHz		70		pF

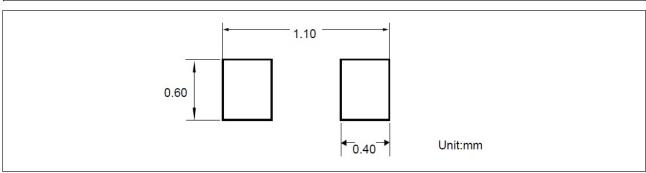


Typical Characteristics (T_{amb} = 25 °C unless otherwise specified)



DFN1006 Package Outline







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