ESD8D3V3CA



ESD8D3V3CA

Transient Voltage Suppressors ESD Protection Diode

General description

Silicon Diode in a SOD882 Plastic Package.

FEATURES

- Capacitance Max. 15 pF
- Small Body Outline Dimensions
- Low Leakage Current
- RoHS Compliant
- Green EMC
- Matte Tin(Sn) Lead Finish
- MSL1

Absolute Maximum Ratings ($T_A = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Value	Units
TL	Max Lead Solder Temperature range (10 Second Duration)	260	°C
T _{stg}	Storage Temperature Range	-55 to +150	°C
T _{OP}	Max operation Temperature Range	125	°C
ESD	IEC61000-4-4 (EFT 5/50nS)	40	A
ESD	IEC61000-4-2 Air Discharge Contact Discharge	±30 ±30	κv

Package Outline



SOD882 Package



Device Marking:

Device Type	Marking
ESD8D3V3CA	F3 or BK

Symbol	Parameter	Value	Units
Vesd	ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	±30 ±30	kV
Ррр	Peak Pulse Power (8/20µs)	84	W
Торт	Operating Temperature	-55/+125	С
Tstg	Storage Temperature	-55/+150	С

ABSOLUTE MAXIMUM RATING

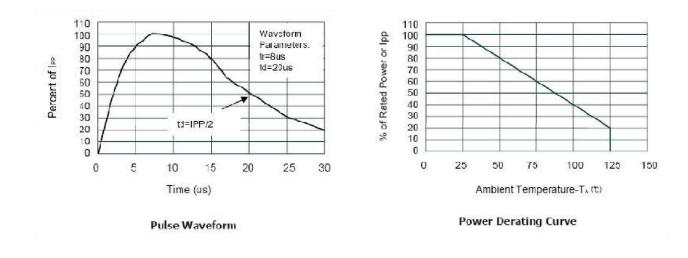
Electrical Characteristics (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Тур	Max	Units
VRWM	Reverse Working Voltage				3.3	V
VBR	Reverse Breakdown Voltage	$I_T = 1mA$	3.6			V
Ir	Reverse Leakage Current	$V_{RWM} = 3.3V$			0.5	μΑ
Vc	Clamping Voltage	$I_{PP} = 1A, t_p = 8/20 \mu s$		4.4	5.6	V
Vc	Clamping Voltage	$I_{PPmax}=7A$, $t_p=8/20\mu s$		7.0	12.0	V
CJ	Junction Capacitance	$V_R = 0V$, $f = 1MHz$		13	15	pF

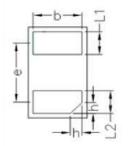
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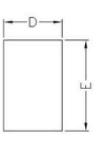


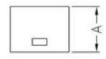
WAVEFORM CURVE:



SOD882 Package Outline

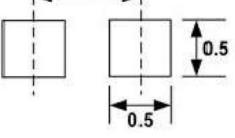






		L	Jnit: m
	MIN	NOM	MAX
D	0.55	0.60	0.65
Е	0.95	1.00	1.05
L1	0.20	0.25	0.30
L2	0.20	0.25	0.30
b	0.45	0.50	0.55
е	0.65BSC		
А	0.45	0.50	0.55
h	0.07	0.12	0.17

Dimension: Millimeter (Stencil thickness: 0.1)



Soldering Footprint

ESD8D3V3CA



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