

SS52 THUR SS510

SS52 THUR SS510 Schottky Barrier Rectifiers

General description

5.0Amp Surface Mounted Schottky Barrier Rectifiers

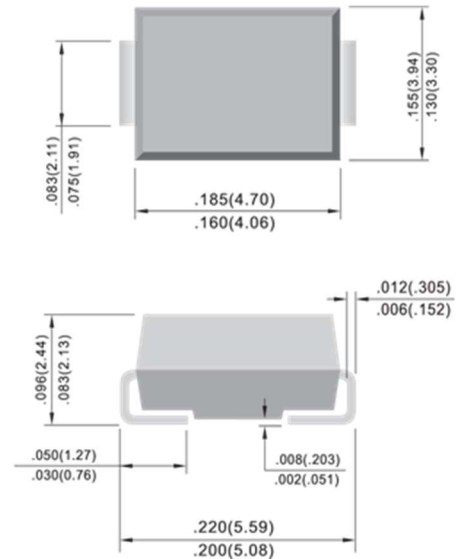
FEATURES

- Flammability Classification 94V-O
- Plastic package has Underwriters Laboratory
- For surface mounted applications
- Built-in strain relief
- High surge capacity

MECHANICAL DATA

- Case: JEDEC DO-214AA molded plastic
- Terminals: Solder plated, solderable per MIL-STD-202G, Method 208
- Polarity: Color band denotes positive end (cathode)
- Standard packaging: 12mm tape (EIA-481)
- Weight: 0.003 ounce, 0.093 gram

SMB/DO214AA



Device Name : SS 52 - SS 510

■ Color Band Denotes Cathode

Maximum Ratings And Electrical Characteristics

Characteristic	Symbol	SS52	SS53	SS54	SS55	SS56	SS58	SS59	SS510	Unit	
Peak Repetitive Reverse Voltage	V_{RRM}										
Working Peak Reverse Voltage	V_{RWM}	20	30	40	50	60	80	90	100	V	
DC Blocking Voltage	V_R										
RMS Reverse Voltage	$V_{R(RMS)}$	14	21	28	35	42	56	64	71	V	
Average Rectified Output Current	I_O	5.0								A	
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	100								A	
Forward Voltage @ $I_F = 5.0A$	V_{FM}	0.55			0.75		0.85			V	
Peak Reverse Current @ $T_A = 25^\circ C$ At Rated DC Blocking Voltage @ $T_A = 100^\circ C$	I_{RM}					0.5 20					mA
Typical Thermal Resistance Junction to Ambient (Note 1)	R_{JA}					50					K/W
Typical junction capacitance	C_J					200					pF
Operating Temperature Range	T_J					-55 to +150					$^\circ C$
Storage Temperature Range	T_{STG}					-55 to +150					$^\circ C$

Note: 1. Measured at 1MHz and applied reverse voltage of 4.0V D.C.

2. P.C.B. mounted with 0.2x0.2" (5.0x5.0mm) copper pad areas

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Rating And Characteristic Curves

FIG. 1- FORWARD CURRENT DERATING CURVE

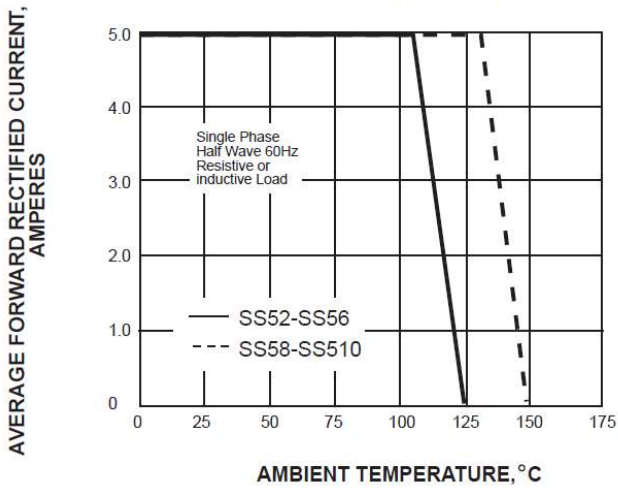


FIG. 2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

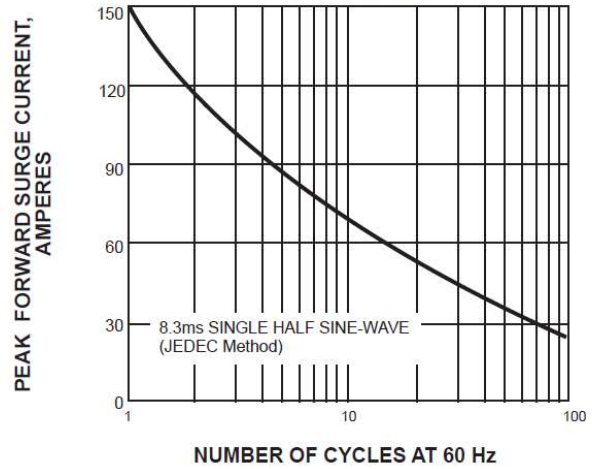


FIG. 3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

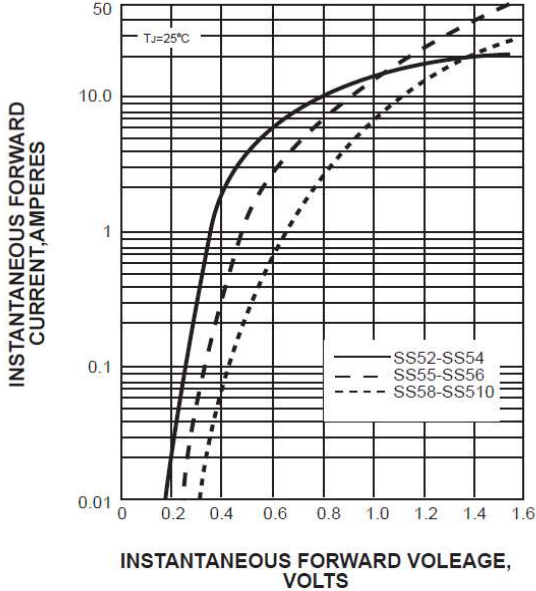


FIG. 4-TYPICAL REVERSE CHARACTERISTICS

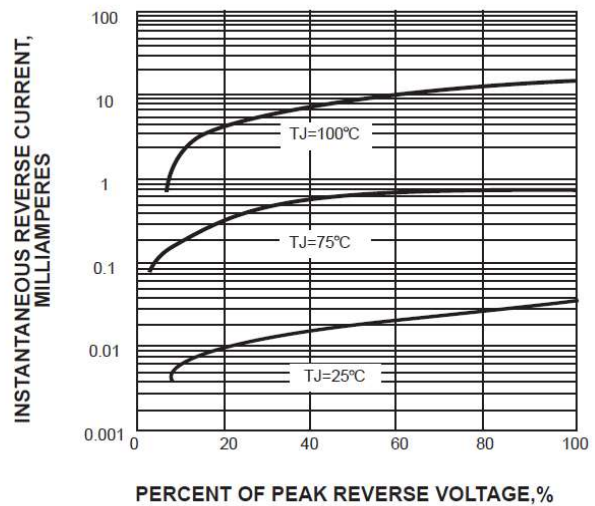


FIG. 5-TYPICAL JUNCTION CAPACITANCE

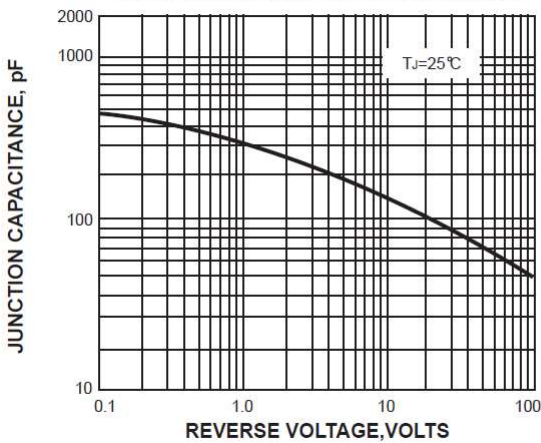
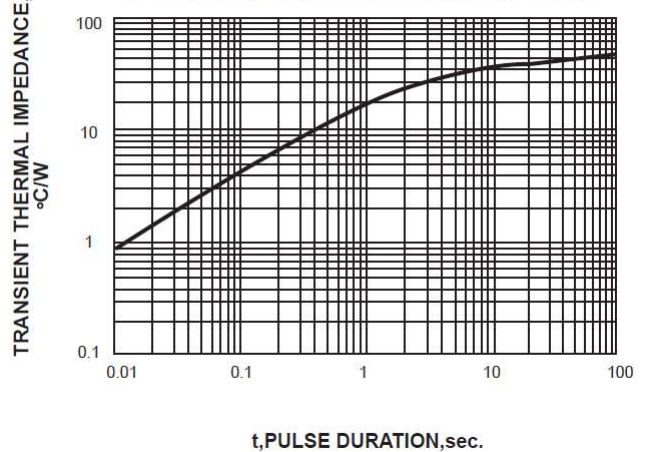


FIG. 6-TYPICAL TRANSIENT THERMAL IMPEDANCE



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