

## Features

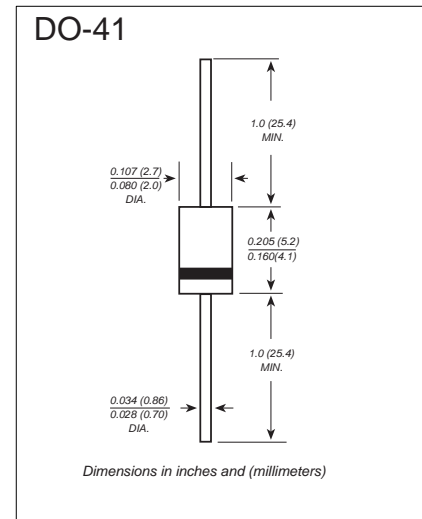
- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- Metal silicon junction, majority carrier conduction
- Guardring for overvoltage protection
- Low power loss, high efficiency
- High current capability, low forward voltage drop
- High surge capability
- Comply with RoHS standard

## Applications

- Low Voltage, High Frequency Inverters
- Free Wheeling, and Polarity Protection Applications

## Mechanical Data

- package: DO-41
- Terminals: Plated axial leads, solderable per MIL-STD-750, Method 2026
- Mounting Position : Any



## Maximum Ratings And Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

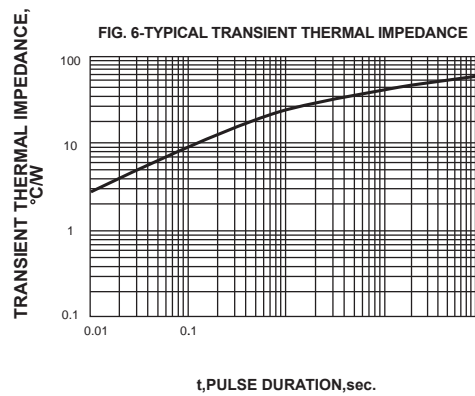
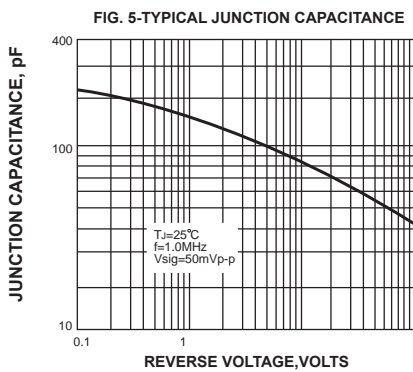
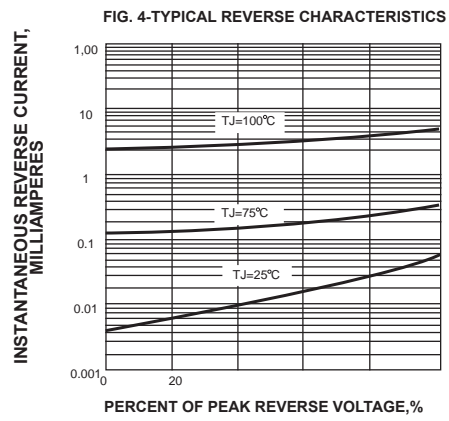
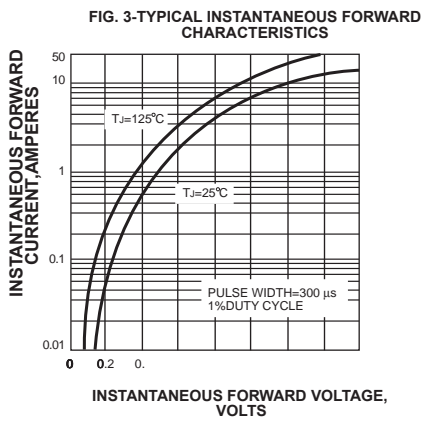
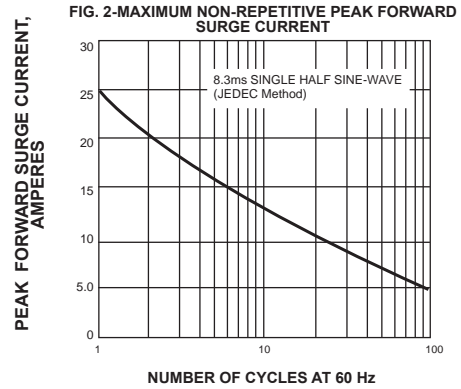
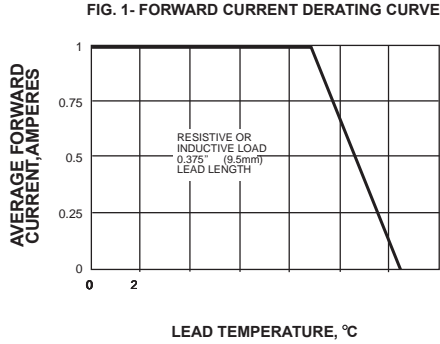
|  | SYMBOLS         | 1N5817      | 1N5818 | 1N5819 | UNITS                     |
|--|-----------------|-------------|--------|--------|---------------------------|
| Maximum repetitive peak reverse voltage  | $V_{RRM}$       | 20          | 30     | 40     | VOLTS                     |
| Maximum RMS voltage  | $V_{RMS}$       | 14          | 21     | 28     | VOLTS                     |
| Maximum DC blocking voltage  | $V_{DC}$        | 20          | 30     | 40     | VOLTS                     |
| Maximum average forward rectified current<br>0.375" (9.5mm) lead length at $T_L=90^\circ\text{C}$      | $I_{AV}$        | 1.0         |        |        | Amp                       |
| Peak forward surge current<br>8.3ms single half sine-wave superimposed on<br>rated load (JEDEC Method) | $I_{FSM}$       | 25.0        |        |        | Amps                      |
| Maximum instantaneous forward voltage at 1.0A  | $V_F$           | 0.450       | 0.550  | 0.600  | Volts                     |
| Maximum DC reverse current<br>at rated DC blocking voltage   | $I_R$           | 0.5<br>10.0 |        |        | mA                        |
| Typical junction capacitance (NOTE 1)  | $C_J$           | 110.0       |        |        | pF                        |
| Typical thermal resistance (NOTE 2)  | $R_{\theta JA}$ | 50.0        |        |        | $^\circ\text{C}/\text{W}$ |
| Operating junction and storage temperature range   | $T_J, T_{STG}$  | -65 to +125 |        |        | $^\circ\text{C}$          |

Note: 1. Measured at 1MHz and applied reverse voltage of 4.0V D.C.

2. Thermal resistance from junction to ambient at 0.375" (9.5mm) lead length, P.C.B. mounted



■ Characteristics Curves( $T_A=25^\circ\text{C}$  unless otherwise noted)



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