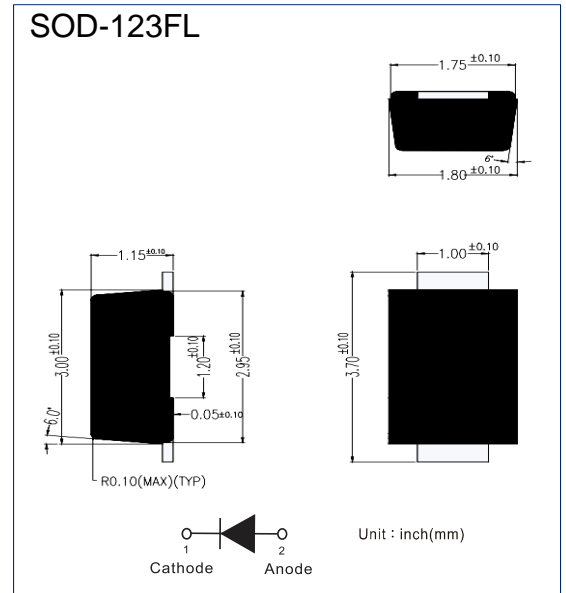


Features

- Low profile package
- Ideal for automated placement
- Guardring for overvoltage protection
- Low power losses, high efficiency
- High forward surge capability
- Compliant with RoHS standards, halogen-free

Mechanical Data

- Package: SOD-123FL
- Terminals: Tin plated leads, solderable per
- Polarity: Cathode line denotes the cathode end



Maximum Ratings ($T_a=25^\circ\text{C}$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	DSK12	DSK13	DSK14	DSK15	DSK16	DSK18	DSK110	DSK115	DSK120	
Repetitive peak reverse voltage	VRRM	V	20	30	40	50	60	80	100	150	200	
Average rectified output current @60Hz sine wave, Resistance load, T_a (FIG.1)	IO	A	1.0									
Surge(non-repetitive)forward current @60Hz half-sine wave,1 cycle, $T_j=25^\circ\text{C}$	IFSM	A	30									
Storage temperature	T_{stg}	$^\circ\text{C}$	-55 ~+150									
Junction temperature	T_j	$^\circ\text{C}$	-55 ~+150					-55 ~+175				



Electrical Characteristics

PARAMETER	SYMBOL	UNIT	TEST CONDITIONS	DSK12	DSK13	DSK14	DSK15	DSK16	DSK18	DSK110	DSK115	DSK120
Maximum instantaneous forward voltage drop per diode	V _F	V	I _{FM} =3.0A	0.55		0.70		0.85		0.95		
Maximum DC reverse current at rated DC blocking voltage per diode @ V _{RM} =V _{RRM}	I _{RRM}	mA	T _a =25°C	0.5				0.1				
			T _a =100°C	10				5				

Characteristics Curves

FIG1: I_o-T_L Curve

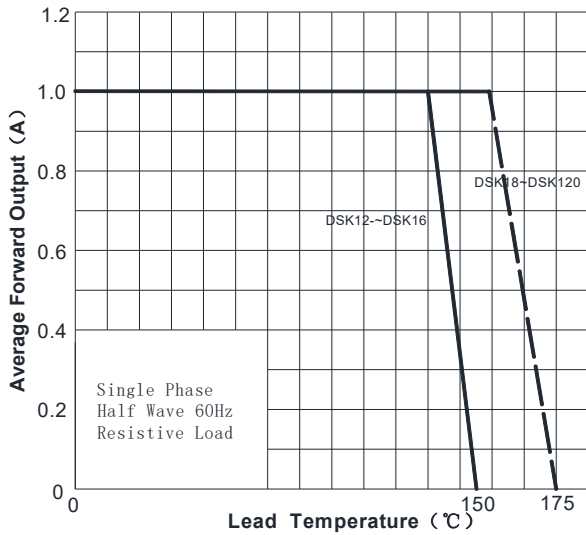


FIG2: Surge Forward Current Capability

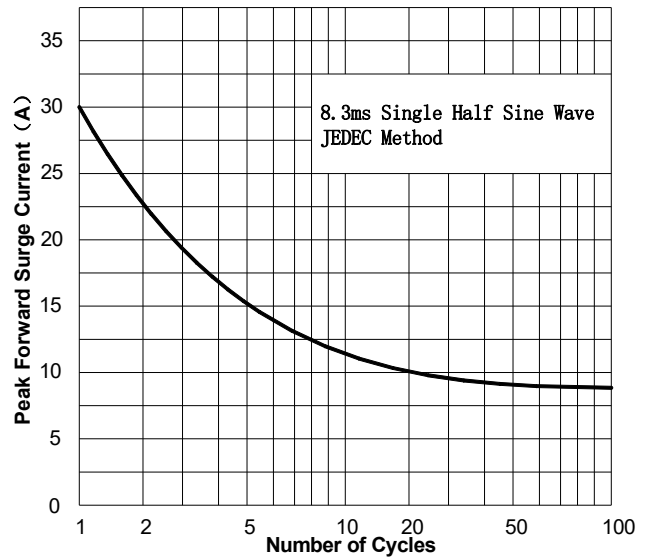


FIG3: Forward Voltage

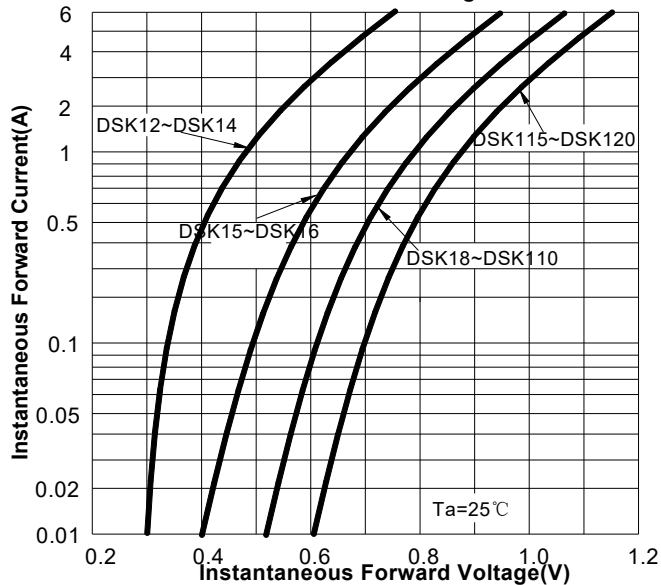
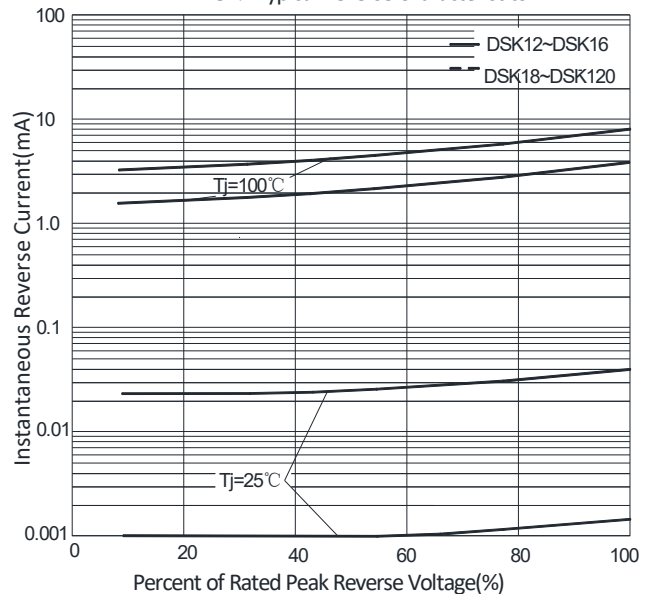


FIG4: Typical Reverse Characteristics



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