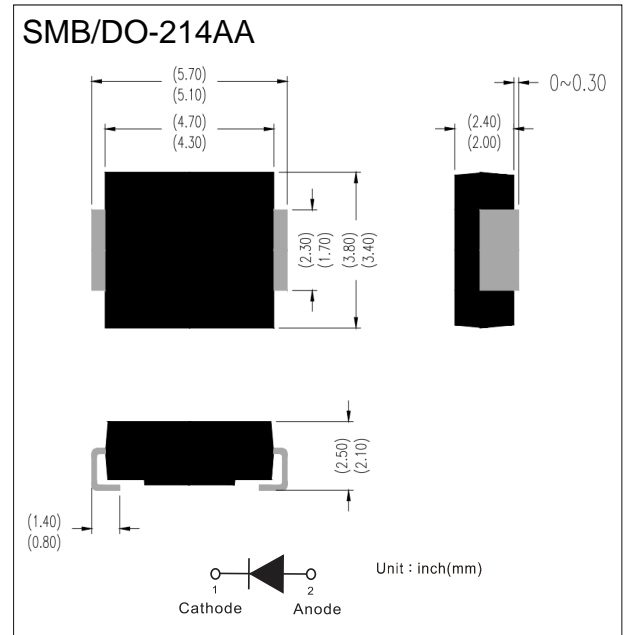


■ **Features**

- Glass passivated junction chip
- Ideal for automated placement
- Super Fast reverse recovery time
- Comply with RoHS standard, halogen-free

■ **Mechanical Data**

- package:SMB/DO-214AA
- Polarity: Indicated by cathode band
- Epoxy: UL 94V-0 rate flame retardant
- Mounting Position : Any



■ **Absolute Maximum Ratings($T_A=25^\circ\text{C}$ unless otherwise noted)**

PARAMETER	SYMBOL	UNIT	MURS220	MURS240	MURS260
Maximum Repetitive Peak Reverse Voltage	VRRM	V	200	400	600
Maximum RMS Voltage	VRMS	V	140	280	420
Maximum DC blocking Voltage	VDC	V	200	400	600
Average rectified output current @60Hz sine wave, resistance load, TL (Fig.1)	I_O	A	2.0		
Forward Surge Current (Non-repetitive) @60Hz Half-sine wave, 1 cycle, $T_j=25^\circ\text{C}$	I_{FSM}	A	50		
Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, $T_j=25^\circ\text{C}$			100		
Current squared time @ $1\text{ms} \leq t \leq 8.3\text{ms}$ $T_j=25^\circ\text{C}$	I^2t	A^2s	10.375		
Storage temperature	T_{stg}	$^\circ\text{C}$	-55 ~ +150		
Junction temperature	T_j	$^\circ\text{C}$	-55 ~ +150		

■ Thermal Performance($T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	UNIT	MURS220	MURS240	MURS260
Typical Thermal resistance	$R_{\theta J-A}^{(1)}$	$^{\circ}\text{C}/\text{W}$	60		
	$R_{\theta J-L}^{(1)}$		20		
	$R_{\theta J-C}^{(1)}$		15		

Note:

(1) Thermal resistance from junction to ambient and from junction to lead mounted on P.C.B. with 0.3" x 0.3" (8.0 mm x 8.0 mm) copper pad areas

■ Electrical Specifications($T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	UNIT	TEST CONDITIONS	MURS220	MURS240	MURS260
Maximum instantaneous forward voltage	V _F	V	I _F =2.0A	0.92	1.25	
Maximum reverse recovery time	t _{rr}	ns	I _F =0.5A, I _R =1.0A, I _r =0.25A	25	50	
Maximum DC reverse current at rated DC blocking voltage	I _R	μA	T _J =25 $^{\circ}\text{C}$	5.0		
			T _J =125 $^{\circ}\text{C}$	50		
Typical junction capacitance	C _j	pF	Measured at 1MHz and Applied Reverse Voltage of 4.0 V.D.C	25	25	24

■ Characteristics
■ MURS220

PARAMETER	SYMBOL	UNIT	TEST CONDITIONS		Min	Typ	Max
Reverse Recovery Time	T _{RR}	ns	T _J =25 $^{\circ}\text{C}$	I _F =1A, di/dt=-50A/us V _{RM} =30V	-	26	-
			T _J =25 $^{\circ}\text{C}$	I _F =2A di/dt=-200A/us V _{RM} =100V	-	23	-
			T _J =125 $^{\circ}\text{C}$		-	30	-
Peak recovery current	I _{RRM}	A	T _J =25 $^{\circ}\text{C}$	I _F =2A di/dt=-200A/us V _{RM} =100V	-	3.1	-
			T _J =125 $^{\circ}\text{C}$		-	5.0	-
Reverse recovery charge	Q _{rr}	nC	T _J =25 $^{\circ}\text{C}$	I _F =2A di/dt=-200A/us V _{RM} =100V	-	35.4	-
			T _J =125 $^{\circ}\text{C}$		-	73.8	-
Non-repetitive avalanche energy	E _{AS}	mJ	T _J =25 $^{\circ}\text{C}$	I _R =1.8 A, L=15 mH	24.3	-	-

▪ MURS240

PARAMETER	SYMBOL	UNIT	TEST CONDITIONS		Min	Typ	Max
Reverse Recovery Time	T_{RR}	ns	Tj=25°C	$I_F=1A, di/dt=-50A/us$ $V_{RM}=30V$	-	35	-
			Tj=25°C	$I_F=2A$ $di/dt=-200A/us$ $V_{RM}=200V$	-	30	-
			Tj=125°C		-	45	-
Peak recovery current	I_{RRM}	A	Tj=25°C	$I_F=2A$ $di/dt=-200A/us$ $V_{RM}=200V$	-	3.7	-
			Tj=125°C		-	5.8	-
Reverse recovery charge	Qrr	nC	Tj=25°C	$I_F=2A$ $di/dt=-200A/us$ $V_{RM}=200V$	-	55.4	-
			Tj=125°C		-	130.6	-
Non-repetitive avalanche energy	EAS	mJ	Tj=25°C	$I_R=0.5A, L=15\text{ mH}$	1.9	-	-

▪ MURS260

PARAMETER	SYMBOL	UNIT	TEST CONDITIONS		Min	Typ	Max
Reverse Recovery Time	T_{RR}	ns	Tj=25°C	$I_F=1A, di/dt=-50A/us$ $V_{RM}=30V$	-	50	-
			Tj=25°C	$I_F=2A$ $di/dt=-200A/us$ $V_{RM}=400V$	-	43	-
			Tj=125°C		-	66	-
Peak recovery current	I_{RRM}	A	Tj=25°C	$I_F=2A$ $di/dt=-200A/us$ $V_{RM}=400V$	-	5.0	-
			Tj=125°C		-	7.4	-
Reverse recovery charge	Qrr	nC	Tj=25°C	$I_F=2A$ $di/dt=-200A/us$ $V_{RM}=400V$	-	105.9	-
			Tj=125°C		-	243.8	-
Non-repetitive avalanche energy	EAS	mJ	Tj=25°C	$I_R=0.5A, L=15\text{ mH}$	1.9	-	-



■ Characteristics Curves($T_A=25^\circ\text{C}$ unless otherwise noted)

FIG.1: I_o - T_L Curve

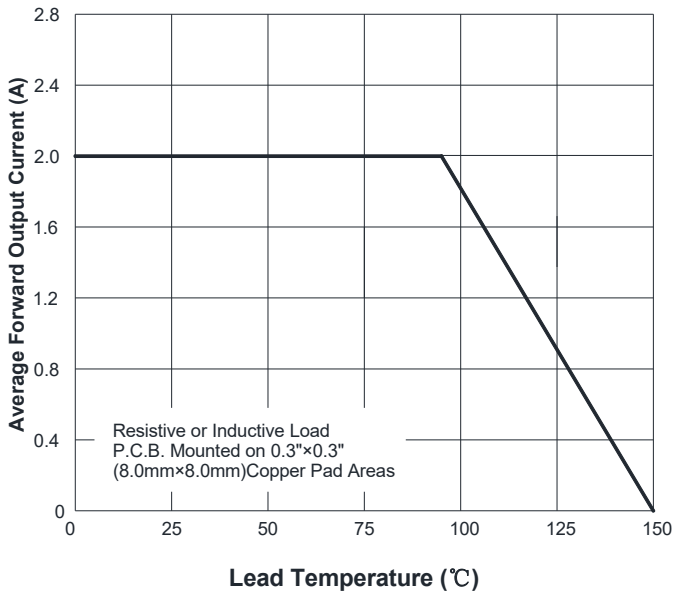


FIG.2: Forward Surge Current Capability

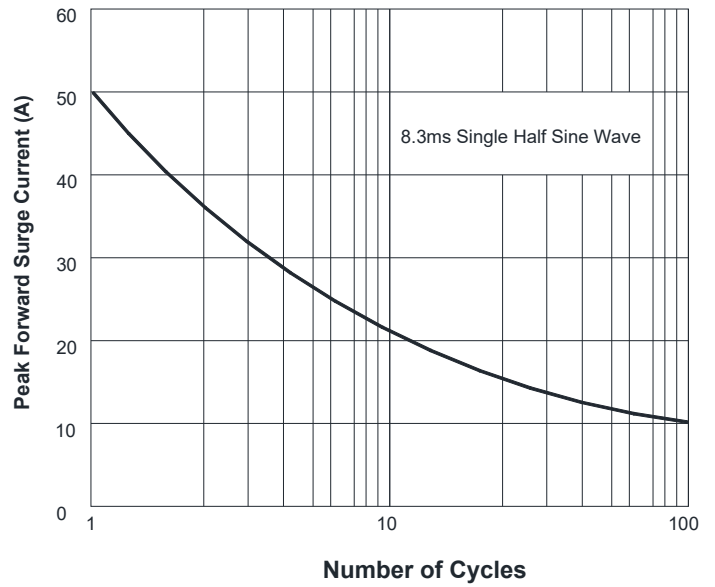


FIG.3: Typical Forward Voltage

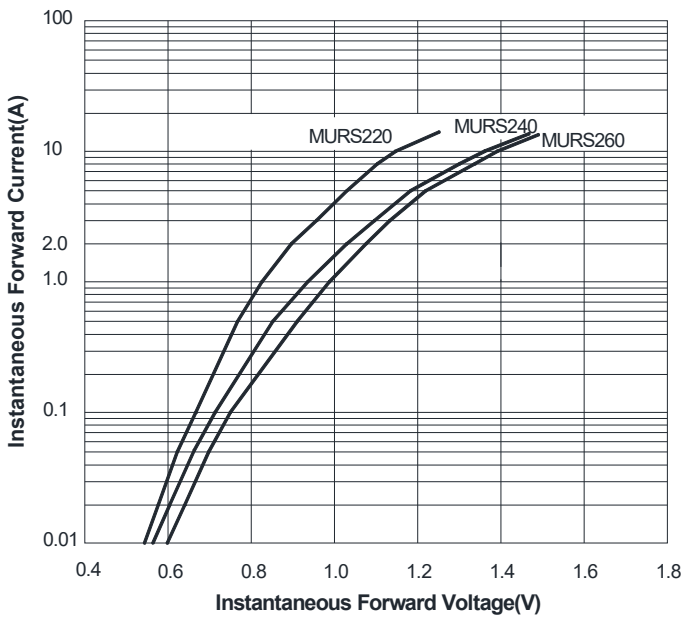


FIG.4: Typical Reverse Characteristics

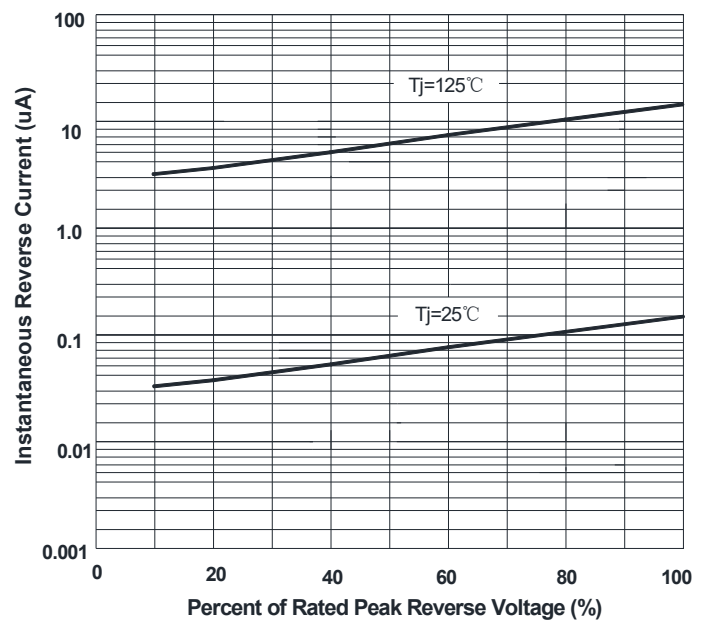
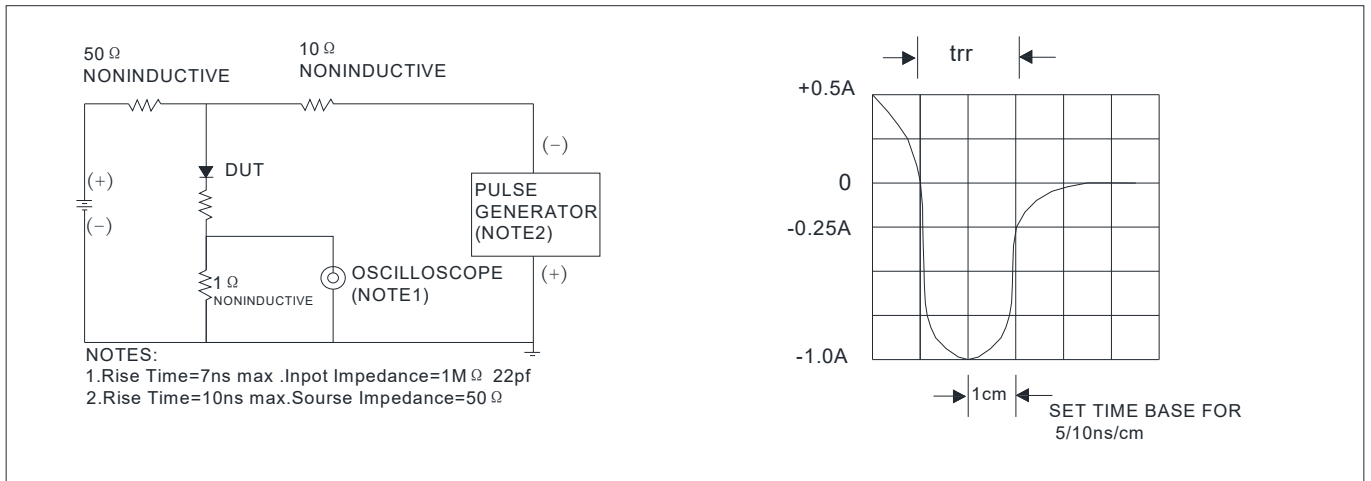




FIG.5: Diagram of circuit and Testing wave form of reverse recovery time



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