

# DIM400GDM33-F000

## **Dual Switch IGBT Module**

DS5616-4 January 2018 (LN35023) Replaces DS5616-3

## **FEATURES**

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- Soft Punch Through Silicon
- Isolated AISiC Base with AIN Substrates
- Lead Free construction

## **APPLICATIONS**

- High Reliability Inverters
- Motor Controllers
- **Traction Drives**
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM400GDM33-F000 is a single switch 3300V, nchannel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

## **ORDERING INFORMATION**

Order As:

## DIM400GDM33-F000

Note: When ordering, please use the complete part number

#### **KEY PARAMETERS**

$V_{CES}$		3300V
V <sub>CE(sat)</sub>	* (typ)	2.8V
l <sub>c</sub> ` ´	(max)	400A
I <sub>C(PK)</sub>	(max)	800A

<sup>\*</sup> Measured at the auxiliary terminals

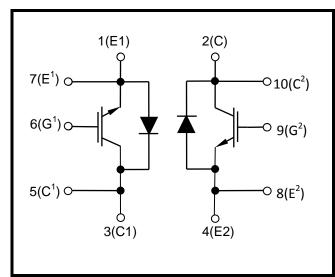


Fig. 1 Circuit configuration



Fig. 2 Package



## **ABSOLUTE MAXIMUM RATINGS**

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V <sub>CES</sub>	Collector-emitter voltage	V <sub>GE</sub> = 0V	3300	V
$V_{GES}$	Gate-emitter voltage		±20	V
I <sub>C</sub>	Continuous collector current	T <sub>case</sub> = 90°C	400	Α
I <sub>C(PK)</sub>	Peak collector current	1ms, T <sub>case</sub> = 115°C	800	Α
P <sub>max</sub>	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	5.2	kW
l <sup>2</sup> t	Diode I <sup>2</sup> t value	$V_R = 0$ , $t_p = 10$ ms, $T_j = 125$ °C	80	kA <sup>2</sup> s
V <sub>isol</sub> *	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	6000	V
$Q_{PD}$	Partial discharge – per module	IEC1287, V <sub>1</sub> = 3500V, V <sub>2</sub> = 2600V, 50Hz RMS	10	рC

## THERMAL AND MECHANICAL RATINGS

Internal insulation material:

Baseplate material:

Creepage distance:

Clearance:

CTI (Comparative Tracking Index):

AIN

AISiC

33mm

20mm

>600

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R <sub>th(j-c)</sub>	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	24	°C/kW
R <sub>th(j-c)</sub>	Thermal resistance – diode	Continuous dissipation - junction to case	-	-	48	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	8	°C/kW
T <sub>j</sub>	Junction temperature	Transistor	-	-	125	°C
		Diode	-	-	125	°C
T <sub>stg</sub>	Storage temperature range	-	-40	-	125	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm



## **ELECTRICAL CHARACTERISTICS**

 $T_{case}$  = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
,	Collector cut-off current	$V_{GE} = 0V$ , $V_{CE} = V_{CES}$			2	mA
I <sub>CES</sub>		$V_{GE} = 0V$ , $V_{CE} = V_{CES}$ , $T_{case} = 125$ °C			30	mA
I <sub>GES</sub>	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			1	μA
$V_{GE(TH)}$	Gate threshold voltage	$I_C = 40$ mA, $V_{GE} = V_{CE}$	5.5	6.5	7.0	V
\ \ \ †	Collector-emitter	V <sub>GE</sub> = 15V, I <sub>C</sub> = 400A		2.8		V
V <sub>CE(sat)</sub> †	saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 400A, T <sub>j</sub> = 125°C		3.6		V
I <sub>F</sub>	Diode forward current	DC		400		Α
I <sub>FM</sub>	Diode maximum forward current	t <sub>p</sub> = 1ms		800		Α
\	Diode forward voltage	I <sub>F</sub> = 400A		2.9		V
V <sub>F</sub> <sup>†</sup>		I <sub>F</sub> = 400A, T <sub>j</sub> = 125°C		3.0		V
C <sub>ies</sub>	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		72		nF
Qg	Gate charge	±15V		10		μC
C <sub>res</sub>	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		1.1		nF
L <sub>M</sub>	Module inductance			25		nΗ
R <sub>INT</sub>	Internal transistor resistance			260		μΩ
SC <sub>Data</sub>	Short circuit current, I <sub>SC</sub>	$T_{j} = 125^{\circ}\text{C}, \ V_{CC} = 2500\text{V}$ $t_{p} \le 10 \mu\text{s}, \ V_{GE} \le 15\text{V}$ $V_{CE \ (max)} = V_{CES} - L^{*} \ x \ dI/dt$ IEC 60747-9		1850		А

 $<sup>\ \ ^{+}</sup>$  Measured at the power busbars, not the auxiliary terminals  $^{*}$  L is the circuit inductance +  $L_{\text{M}}$ 



## **ELECTRICAL CHARACTERISTICS**

T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 400A		2100		ns
t <sub>f</sub>	Fall time	$V_{GE} = \pm 15V$		210		ns
E <sub>OFF</sub>	Turn-off energy loss	$V_{CE} = 1800V$		520		mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{G(ON)} = 5.6\Omega$ $R_{G(OFF)} = 8.2\Omega$ $C_{ge} = 110 nF$ $L_S \sim 100 nH$		1130		ns
t <sub>r</sub>	Rise time			245		ns
E <sub>ON</sub>	Turn-on energy loss			620		mJ
$Q_{rr}$	Diode reverse recovery charge	$I_F = 400A$ $V_{CE} = 1800V$ $dI_F/dt = 2000A/\mu s$		160		μC
I <sub>rr</sub>	Diode reverse recovery current			330		Α
E <sub>rec</sub>	Diode reverse recovery energy			150		mJ

## $T_{case}$ = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 400A		2150		ns
t <sub>f</sub>	Fall time	$V_{GE} = \pm 15V$		220		ns
E <sub>OFF</sub>	Turn-off energy loss	V <sub>CE</sub> = 1800V		600		mJ
$t_{d(on)}$	Turn-on delay time	$R_{G(ON)} = 5.6\Omega$ $R_{G(OFF)} = 8.2\Omega$ $C_{ge} = 110 \text{nF}$ $L_{S} \sim 100 \text{nH}$		1160		ns
t <sub>r</sub>	Rise time			285		ns
E <sub>ON</sub>	Turn-on energy loss			870		mJ
$Q_{rr}$	Diode reverse recovery charge	I <sub>F</sub> = 400A		300		μC
I <sub>rr</sub>	Diode reverse recovery current	V <sub>CE</sub> = 1800V		400		Α
E <sub>rec</sub>	Diode reverse recovery energy	$dI_F/dt = 2000A/\mu s$		300		mJ



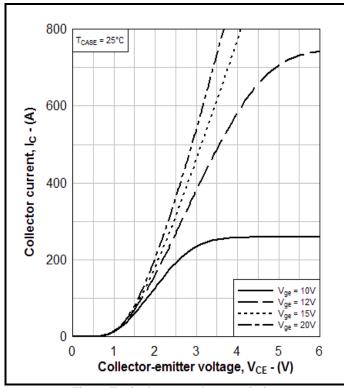


Fig. 3 Typical output characteristics

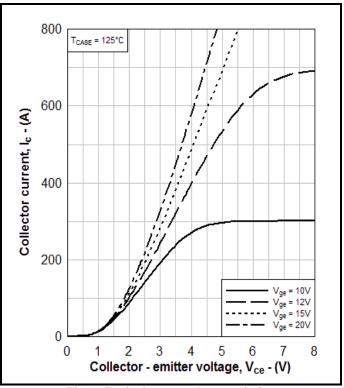


Fig. 4 Typical output characteristics

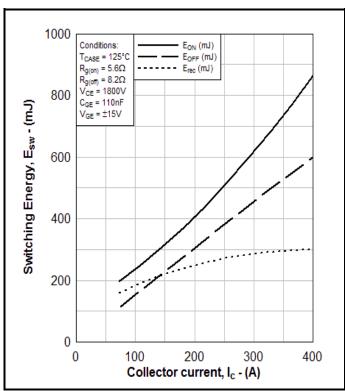


Fig. 5 Typical switching energy vs collector current

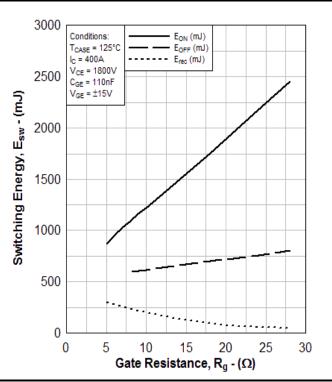


Fig. 6 Typical switching energy vs gate resistance



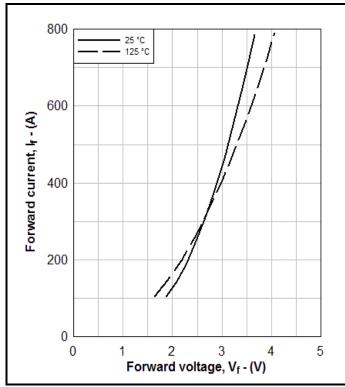


Fig. 7 Diode typical forward characteristics

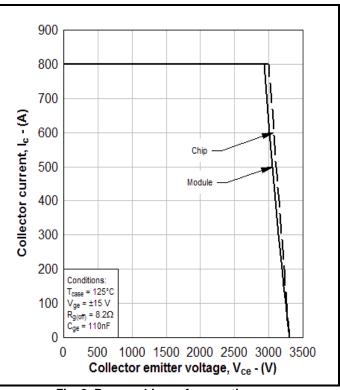


Fig. 8 Reverse bias safe operating area

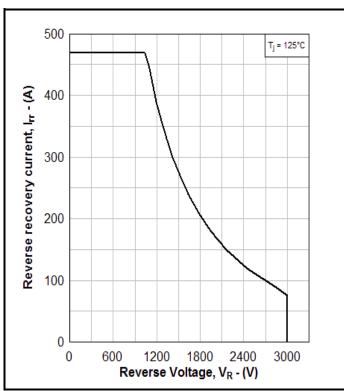


Fig. 9 Diode reverse bias safe operating area

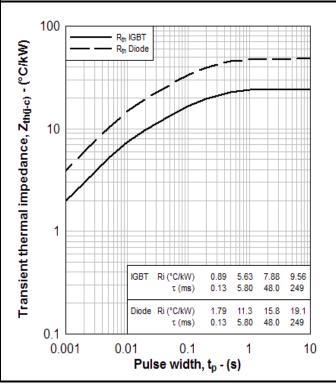


Fig. 10 Transient thermal impedance



## **PACKAGE DETAILS**

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

## DO NOT SCALE.

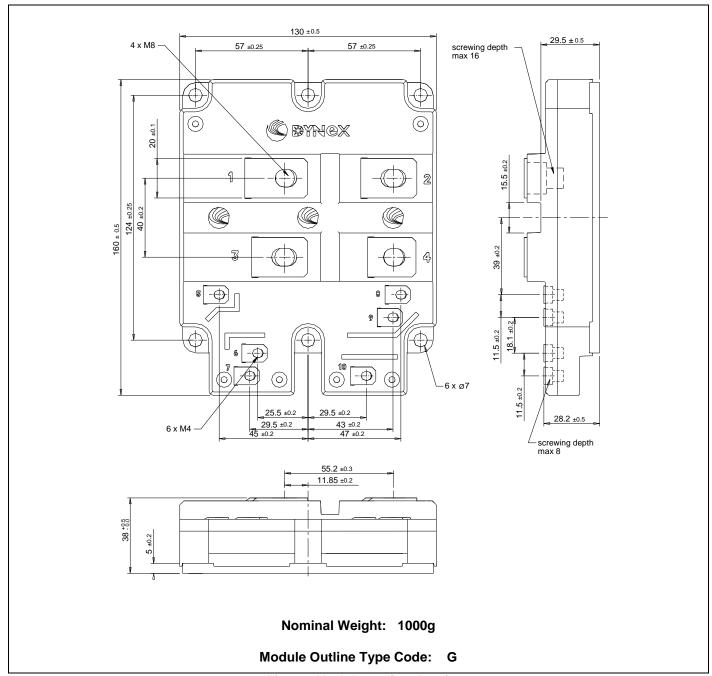


Fig. 11 Module outline drawing



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FF600R12IP4V FF800R17KP4\_B2 FF900R12IE4V MIXA30W1200TED MIXA450PF1200TSF FP06R12W1T4\_B3 FP100R07N3E4
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