# FP1108R

## High frequency, high current power inductors



#### **Product features**

- 11.0 mm x 8.0 mm x 7.5 mm surface mount package
- Ferrite core material
- Tight tolerance DCR for sensing circuits
- Inductance range from 100 nH to 210 nH
- Current range from 55 A to 100+ A

### **Applications**

- Multi-phase regulators
- Voltage Regulator Modules (VRMs)
- Desktop and server VRMs and EVRDs
- Notebook and laptop regulators
- Data networking and storage systems
- Graphics cards and battery power systems
- Point-of-Load modules
- DCR Sensing circuits

#### **Environmental data**

- Storage temperature range (component): -40 °C to +125 °C
- Operating temperature range: -40 °C to +125 °C (ambient plus self-temperature rise)
- Solder reflow temperature:
   J-STD-020 (latest revision) compliant









#### **Product specifications**

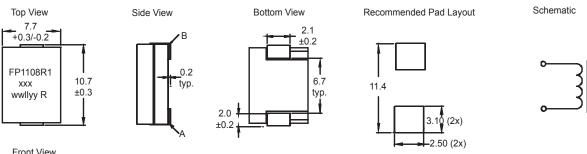
Part Number <sup>9</sup>	OCL <sup>1</sup> (nH) ±10%	FLL min.² (nH)	I <sub>rms</sub> <sup>3</sup> (A)	I <sub>sat</sub> 1 <sup>4</sup> (A)	I <sub>sat</sub> 2 <sup>5</sup> (A)	I <sub>sat</sub> 3 <sup>6</sup> (A)	I <sub>sat</sub> 4 <sup>7</sup> (A)	DCR (mΩ) @ +20 °C	K-factor <sup>8</sup>
FP1108R1-R10-R	100	81	05	100+	96	94	90	0.20.5%	330
FP1108R1-R15-R	150	110		77	72	66	63		330
FP1108R1-R18-R	180	132	65	65	61	58	50	0.29±5%	330
FP1108R1-R21-R	210	151		55	51	48	45		330

- 1. Open Circuit Inductance (OCL) Test Parameters: 100 kHz, 0.1  $V_{\rm ms'}$ 0.0 Adc, +25 °C
- 2. Full Load Inductance (FLL) Test Parameters: 100 kHz, 0.1  $V_{\rm rms'}$  I  $_{\rm sat}$  1, +25  $^{\circ}{\rm C}$
- 3. I<sub>ms</sub>: DC current for an approximate temperature rise of 40 °C without core loss. Derating is necessary for AC currents. PCB layout, trace thickness and width, air-flow, and proximity of other heat generating components will affect the temperature rise. It is recommended that the temperature of the part not exceed +125 °C under worst case operating conditions verified in the end application.
- Isat 1: Peak current for approximately 20% (R10 10%) rolloff @ +25 °C (R10 10%)
- 5. I<sub>sat</sub>2: Peak current for approximately 20% (R10 10%) rolloff @ +85 °C

- 6. Isat3: Peak current for approximately 20% (R10 10%) rolloff @ +100 °C
- 7. Isat4: Peak current for approximately 20% (R10 10%) rolloff @ +125 °C
- K-factor: Used to determine  $B_{pp}$  for core loss (see graph).  $B_{pp} = K * L * \Delta I$ .  $B_{pp}$  (Gauss), K: (K-factor from table), L: (Inductance in  $\mu H$ ),  $\Delta \tilde{\Gamma}$  (peak to peak ripple current in amps).
- 9. Part Number Definition: FP1108Rx-yyy-R
  - FP1108Rx = Product code and size
  - Rx = DCR indicator

  - yyy= Inductance value in μH "-R" suffix = RoHS compliant

#### **Dimensions - mm**





DCR measured from point "A" to point "B"

Part marking: FP1108R1 (Product code and size), xxx = Inductance value in µH, wwllyy= date code, R= revision level

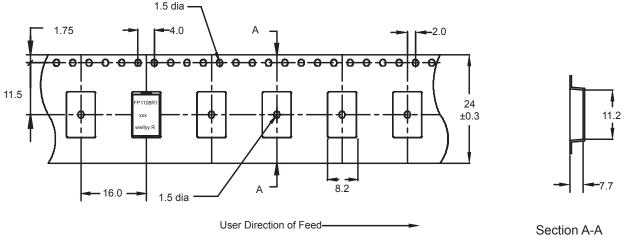
Tolerances are ±0.15 millimeters unless stated otherwise

PCB tolerances are ±0.1 millimeters unless otherwise specified.

All soldering surfaces to be be coplanar within 0.1 millimeters.

Termination finish: matte Sn with Ni underplate Do not route traces or vias underneath inductor

### Packaging information - mm

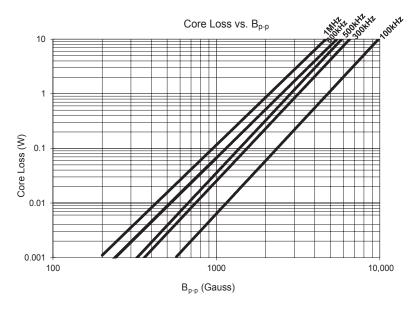


Supplied in tape and reel packaging, 500 parts per 13" diameter reel,

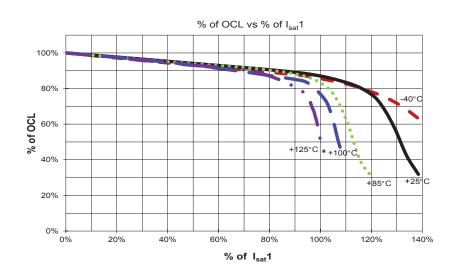
### Temperature rise vs total loss



### Core loss vs Bp-p



### **Inductance characteristics**



### **Solder Reflow Profile**

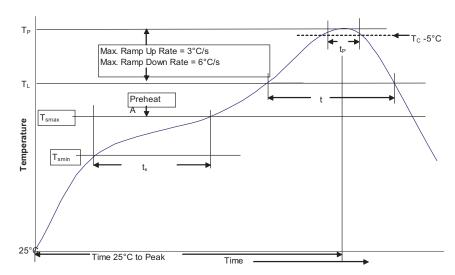


Table 1 - Standard SnPb Solder (T<sub>c</sub>)

	Volume	Volume
Package	mm³	mm³
Thickness	<350	≥350
<2.5mm	235°C	220°C
≥2.5mm	220°C	220°C

Table 2 - Lead (Pb) Free Solder (Tc)

	Volume	Volume	Volume
Package	mm³	mm³	mm <sup>3</sup>
Thickness	<350	350 - 2000	>2000
<1.6mm	260°C	260°C	260°C
1.6 - 2.5mm	260°C	250°C	245°C
>2.5mm	250°C	245°C	245°C

### **Reference JDEC J-STD-020**

Profile Feature		Standard SnPb Solder	Lead (Pb) Free Solder
Preheat and Soak	• Temperature min. (T <sub>smin</sub> )	100°C	150°C
	Temperature max. (T <sub>smax</sub> )	150°C	200°C
	• Time (T <sub>smin</sub> to T <sub>smax</sub> ) (t <sub>s</sub> )	60-120 Seconds	60-120 Seconds
Average ramp up ra	te T <sub>smax</sub> to T <sub>p</sub>	3°C/ Second Max.	3°C/ Second Max.
Liquidous temperature (TL)		183°C	217°C
Time at liquidous (t <sub>L</sub> )		60-150 Seconds	60-150 Seconds
Peak package body	temperature (T <sub>P</sub> )*	Table 1	Table 2
Time $(t_p)^{**}$ within 5 °C of the specified classification temperature $(T_c)$		20 Seconds**	30 Seconds**
Average ramp-down rate (T <sub>p</sub> to T <sub>smax</sub> )		6°C/ Second Max.	6°C/ Second Max.
Time 25°C to Peak Temperature		6 Minutes Max.	8 Minutes Max.

 $<sup>^{\</sup>star}$  Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

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<sup>\*\*</sup> Tolerance for time at peak profile temperature  $(t_p)$  is defined as a supplier minimum and a user maximum.

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