

HCM1A0503V2

Automotive grade high current power inductors



Product features

- AEC-Q200 qualified
- High current carrying capacity
- Magnetically shielded, low EMI
- DC-DC converter applications up to 1 MHz
- Filtering applications up to Self Resonant Frequency (SRF) [See product specification table]
- Inductance range from 0.20 μ H to 10 μ H
- Current range from 2.3 A to 20.5 A
- 5.7 mm x 5.4 mm footprint surface mount package in a 3.0 mm height
- Moisture Sensitivity Level (MSL): 1
- Alloy powder core material

Applications

- Body electronics
 - Central body control module
 - Vehicle access control system
 - Headlamps, tail lamps and interior lighting and LED lighting
 - Heating ventilation and air conditioning controllers (HVAC)
 - Doors, window lift and seat control
- Advanced driver assistance systems
 - 77 GHz radar system
 - Basic and smart surround, and rear and front-view camera
 - Adaptive cruise control (ACC)
 - Automatic parking control
 - Collision avoidance system/ Car black box system
- Infotainment and cluster electronics
 - Active noise cancellation (ANC)
 - Audio subsystem: head unit and trunk amp
 - Digital instrument cluster
 - In-vehicle infotainment (IVI) and navigation
 - Port power/USB HUB for front and rear passengers
- Chassis and safety electronics
 - Airbag control unit
- Engine and Powertrain Systems
 - Electric pumps, motor control and auxiliaries
 - Powertrain control module (PCU)/ Engine Control unit (ECU)
 - Transmission Control Unit (TCU)

Environmental data

- Storage temperature range (Component): -55 $^{\circ}$ C to +155 $^{\circ}$ C
- Operating temperature range: -55 $^{\circ}$ C to +155 $^{\circ}$ C (ambient plus self-temperature rise)
- Solder reflow temperature: J-STD-020 (latest revision) compliant



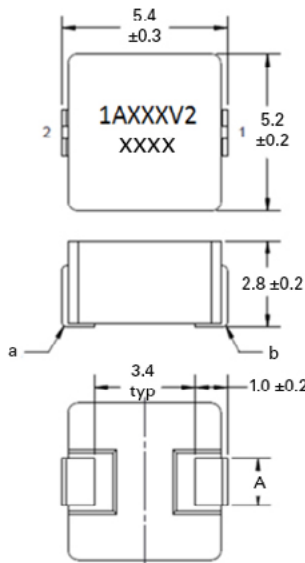
Product specifications

Part number ⁶	OCL ¹ (μH) $\pm 20\%$	FLL ² (μH) minimum	I_{rms}^3 (A)	I_{sat}^4 (A)	DCR (m Ω) typical @ +20 °C	DCR (m Ω) maximum @ +20 °C	SRF (MHz) typical	K-factor ⁵
HCM1A0503V2-R20-R	0.20	0.128	20.5	20	1.6	2.0	160	1514
HCM1A0503V2-R22-R	0.22	0.141	15.8	23	2.1	2.3	150	1267
HCM1A0503V2-R33-R	0.33	0.211	14	16	2.9	3.4	120	1167
HCM1A0503V2-R35-R	0.35	0.224	14	15	2.9	3.4	110	1062
HCM1A0503V2-R47-R	0.47	0.30	11	12	5.3	6.1	80	867
HCM1A0503V2-R68-R	0.68	0.435	10	12	6.9	8.0	70	672
HCM1A0503V2-R75-R	0.75	0.48	10	11.5	6.6	7.7	57	720
HCM1A0503V2-1R0-R	1.0	0.64	8.4	8.5	10	11.4	53	631
HCM1A0503V2-1R5-R	1.5	0.96	6.2	8.5	15.4	17.7	40	435
HCM1A0503V2-2R2-R	2.2	1.4	5.5	6.4	20	23	33	407
HCM1A0503V2-3R3-R	3.3	2.1	4.7	6.5	32	38	25	376
HCM1A0503V2-4R7-R	4.7	3.0	4.1	6.0	41	47	22	367
HCM1A0503V2-5R6-R	5.6	3.6	3.6	4.5	51	59	19	236
HCM1A0503V2-6R8-R	6.8	4.35	3.3	4.0	61	70	16	220
HCM1A0503V2-100-R	10	6.4	2.8	2.3	90	108	13	235

- Open Circuit Inductance (OCL) Test Parameters: 100 kHz, 0.25 V_{rms}, 0.0 Adc, +25 °C
- Full Load Inductance (FLL) Test Parameters: 100 kHz, 0.25 V_{rms}, I_{rms}, +25 °C
- I_{rms}: DC current for an approximate temperature rise of 30 °C without core loss. Derating is necessary for AC currents. PCB layout, trace thickness and width, air-flow, and proximity of other heat generating components will affect the temperature rise. It is recommended that the temperature of the part not exceed +155 °C under worst case operating conditions verified in the end application.

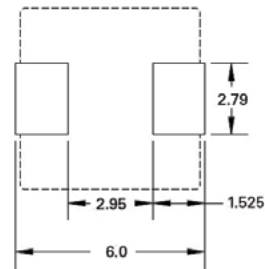
- I_{sat}: Peak current for approximately 20% rolloff @ +25 °C
- K-factor: Used to determine B_{pp} for core loss (see graph). B_{p-p} = K * L * ΔI . B_{pp}: (Gauss), K: (K-factor from table), L: (Inductance in μH), ΔI (Peak to peak ripple current in Amps).
- Part Number Definition: HCM1A0503V2-xxx-R
HCM1A0503V2 = Product code and size
xxx= inductance value in μH , R= decimal point,
If no R is present then last character equals number of zeros
-R suffix = RoHS compliant

Dimensions (mm)

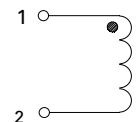


Dimension A	
HCM1A0503V2-R22, R33, R35-R	HCM1A0503V2-R20, R47 through 100-R
1.5 ±0.2	2.3 ±0.2

Recommended pad layout



Schematic

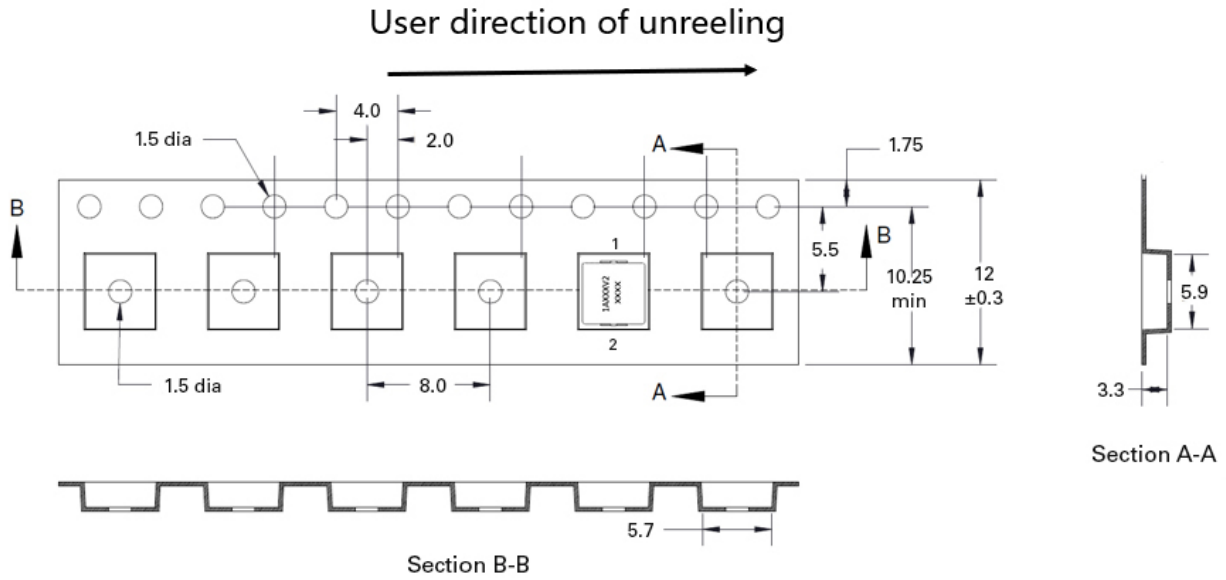


Part marking: 1AxxxV2, xxx=inductance value in μH , R=decimal point. If no R is present then last character equals number of zeros. xxxx=Lot code
All soldering surfaces to be coplanar within 0.1 millimeters
Tolerances are ± 0.3 millimeters unless stated otherwise
Pad layout tolerances are ± 0.1 millimeters unless stated otherwise
DCR measured from point "a" to point "b"
Do not route traces or vias underneath the inductor

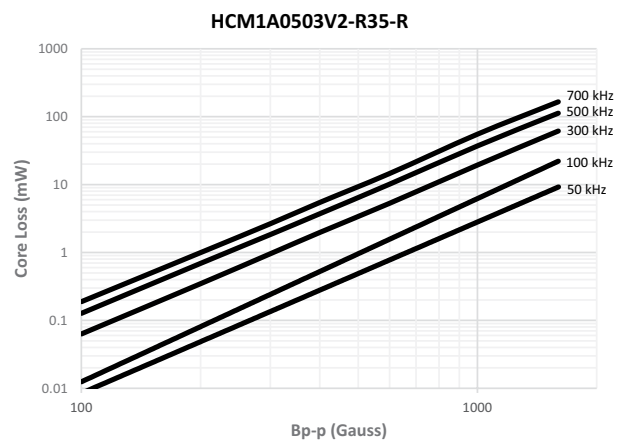
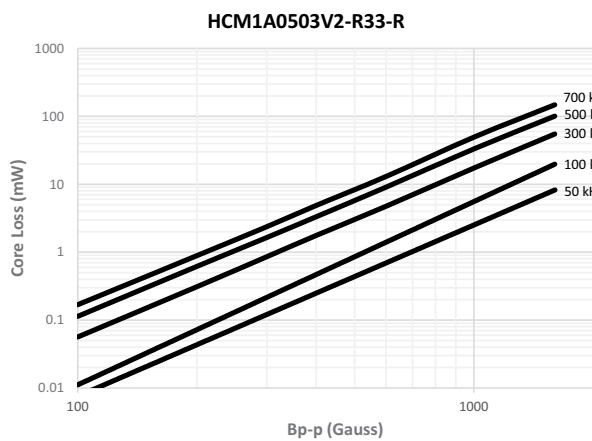
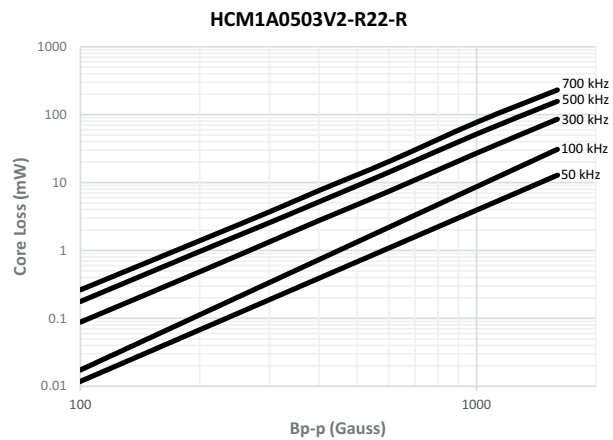
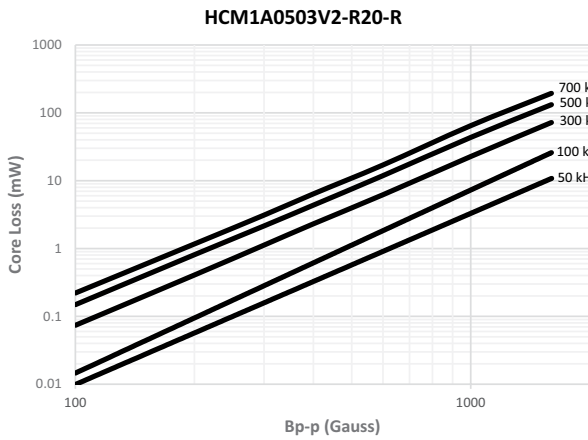
Packaging information (mm)

Drawing not to scale

Supplied in tape and reel packaging, 2000 parts per 13" diameter reel

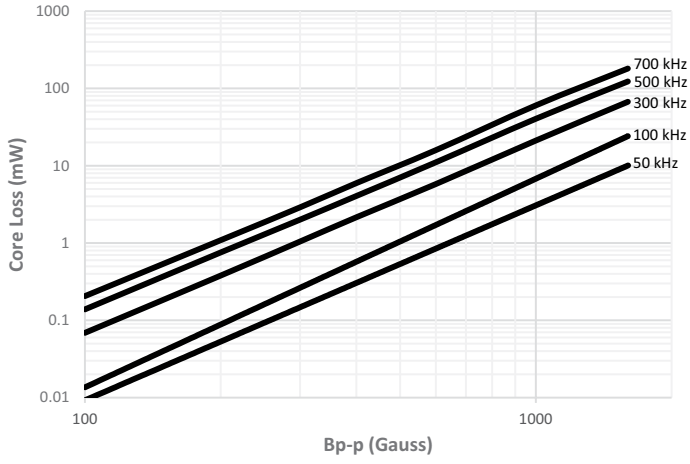


Core loss vs B_{p-p}

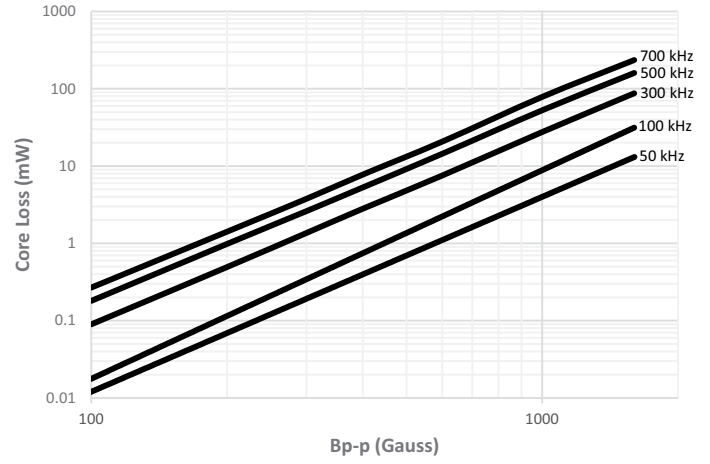


Core loss vs B_{p-p}

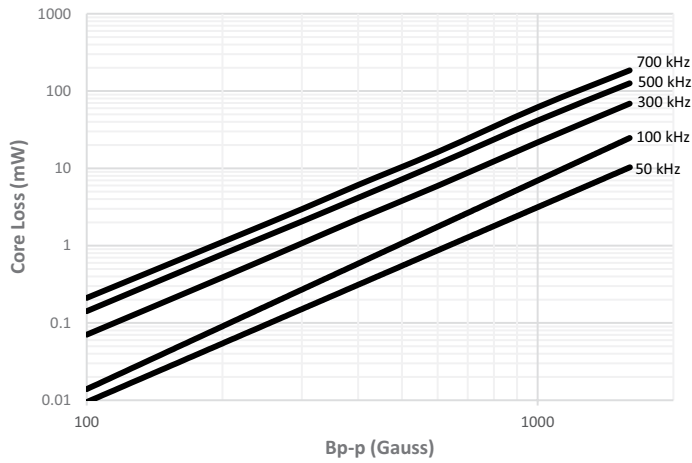
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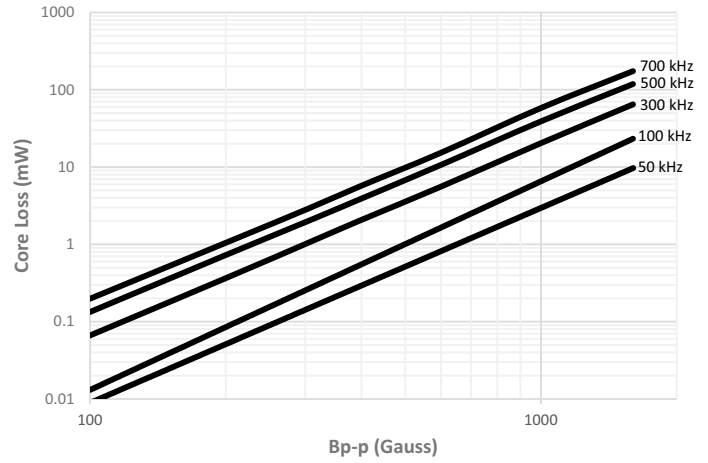
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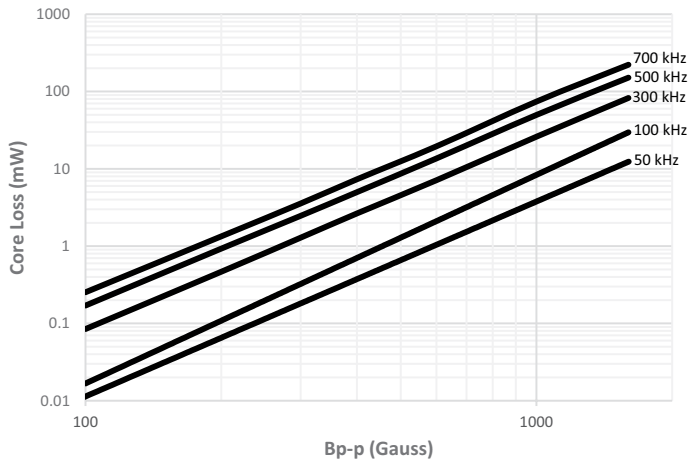
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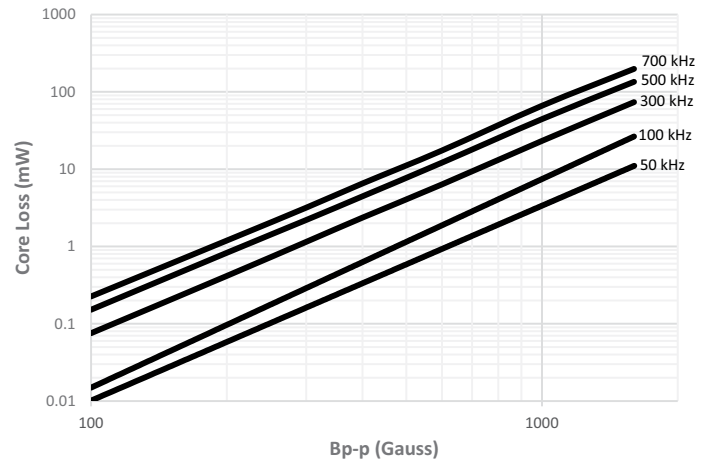
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HCM1A0503V2-1R5-R

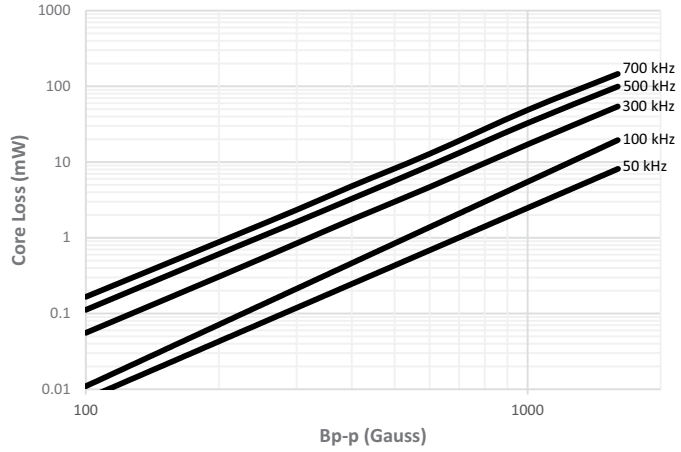


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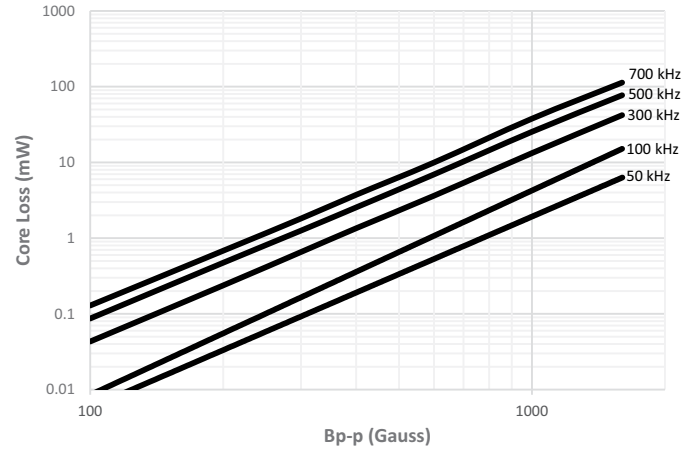


Core loss vs B_{p-p}

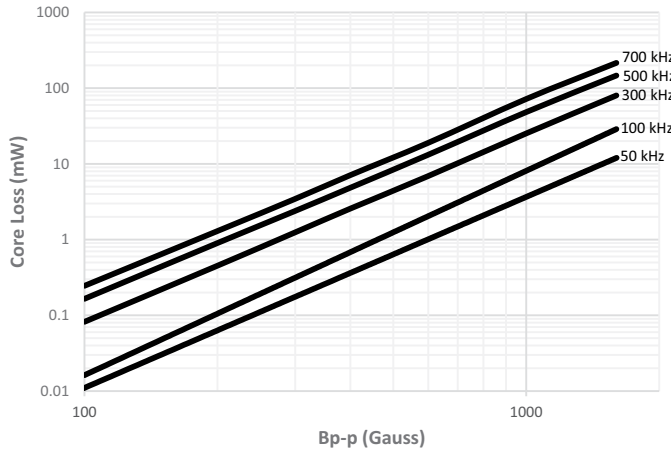
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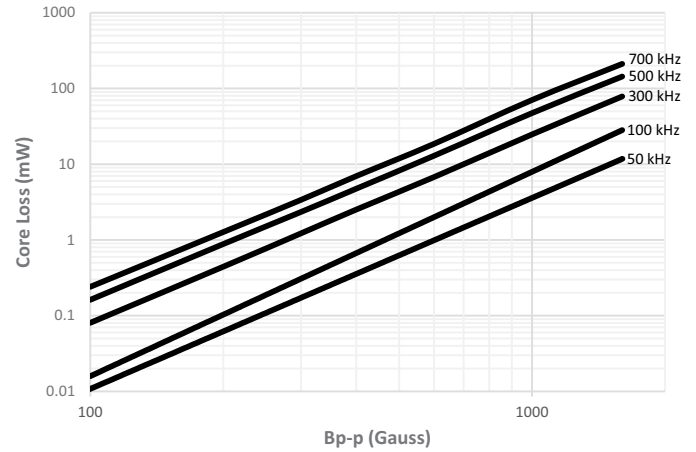
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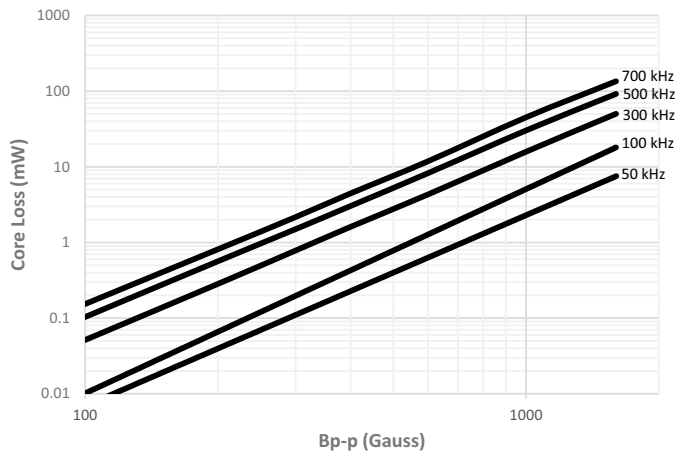
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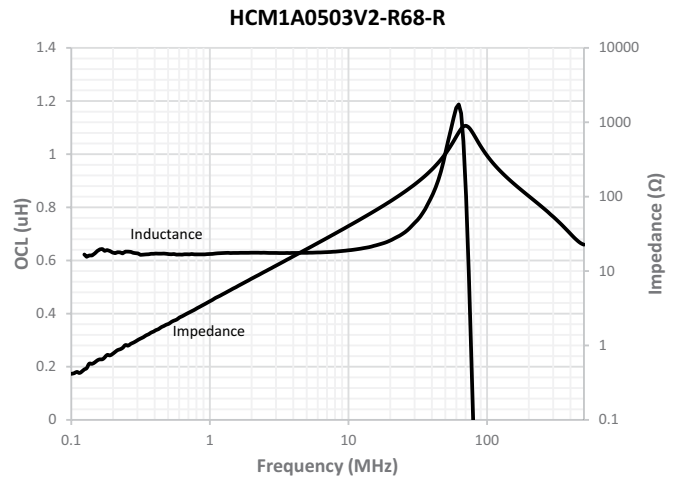
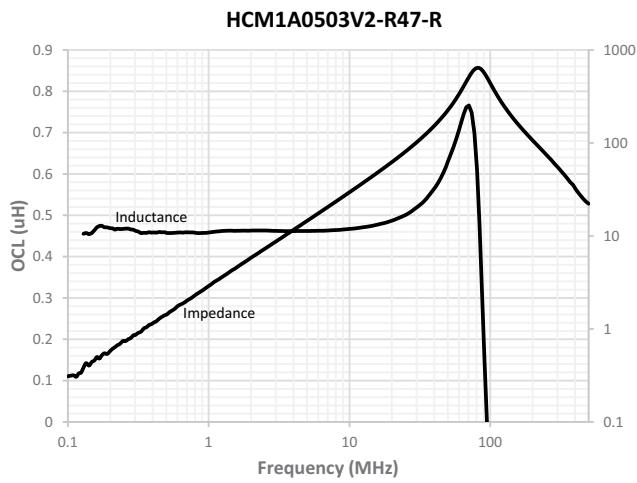
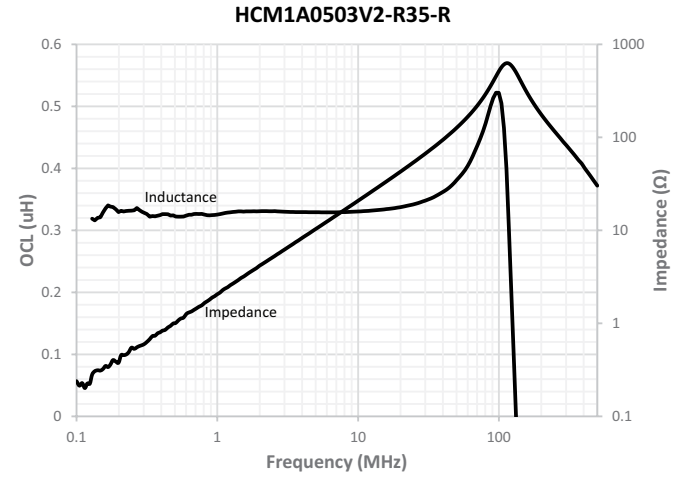
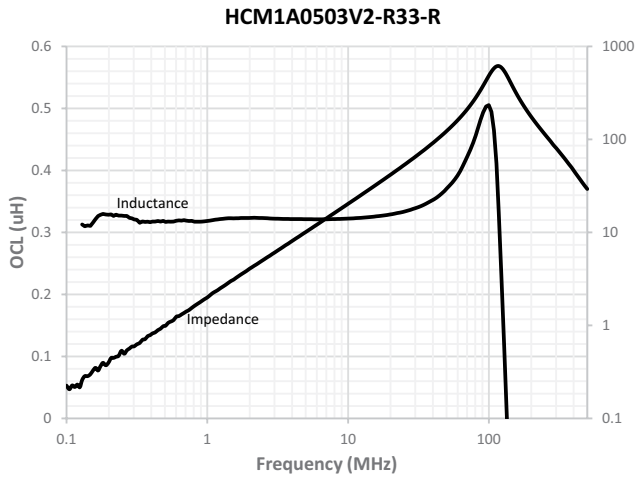
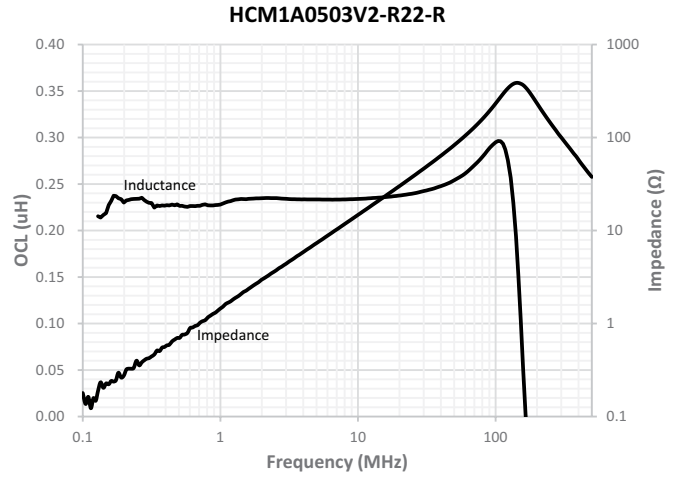
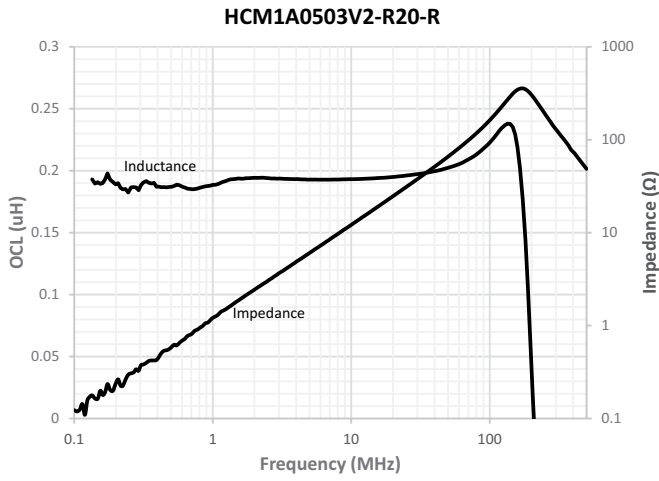
HCM1A0503V2-6R8-R



HCM1A0503V2-100-R

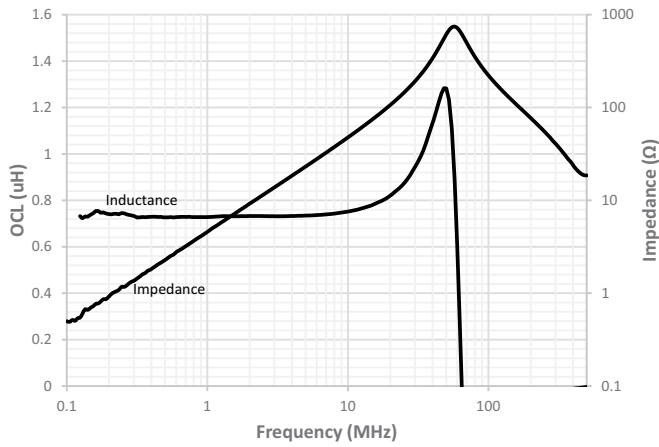


Inductance and impedance vs. frequency

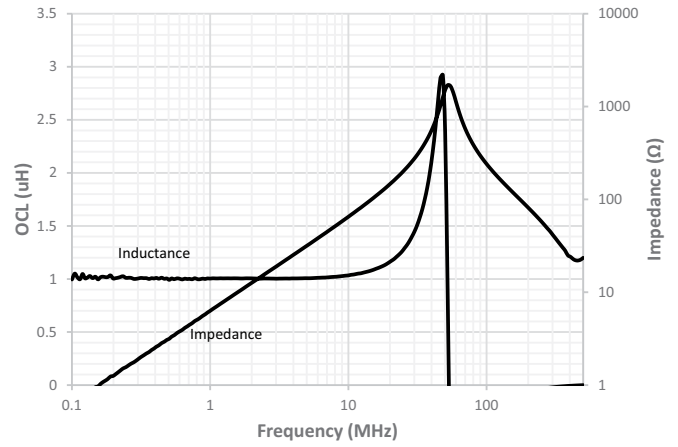


Inductance and impedance vs. frequency

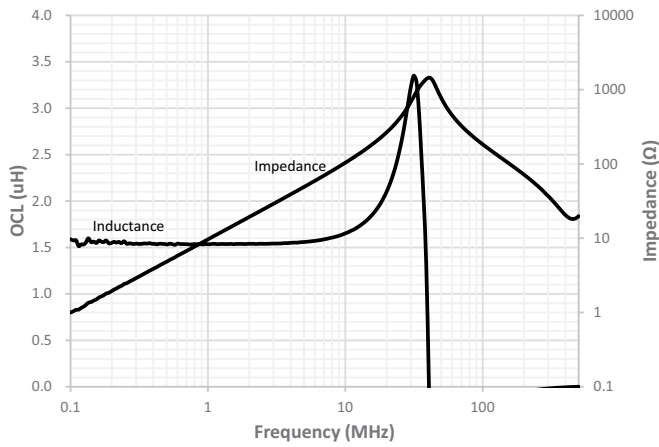
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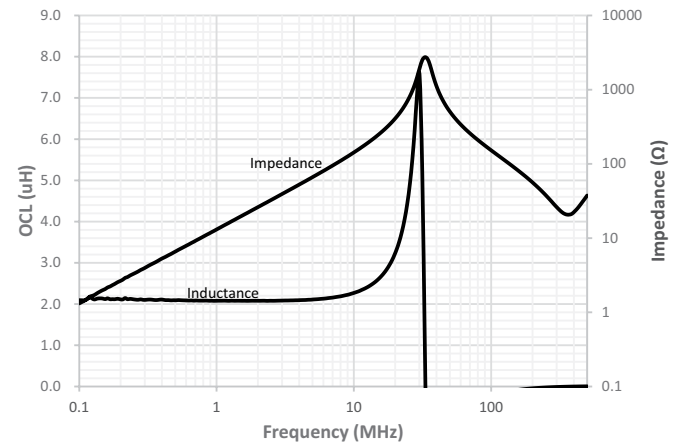
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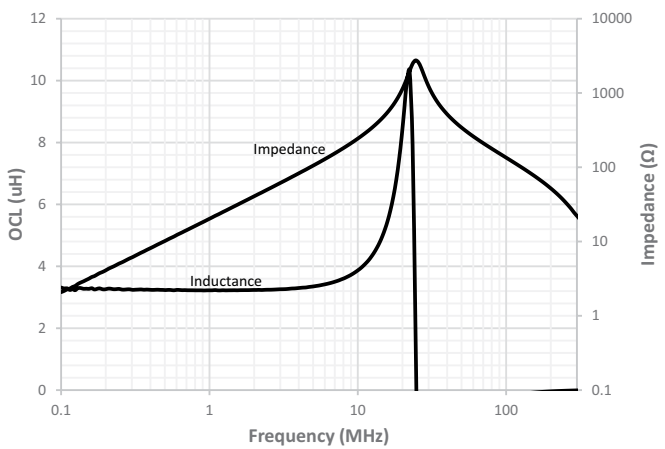
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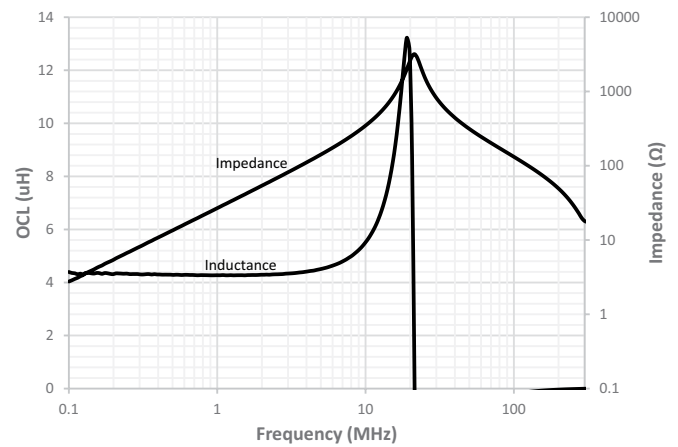
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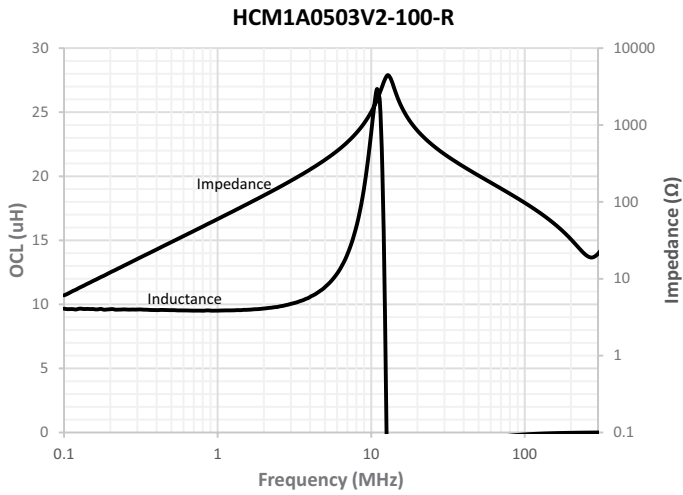
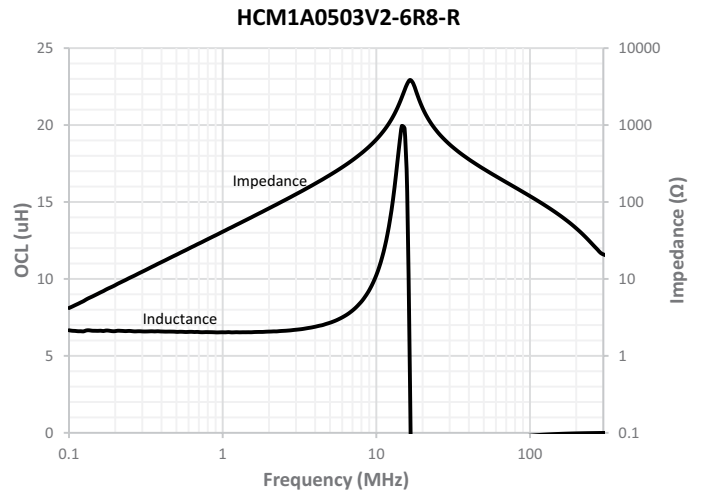
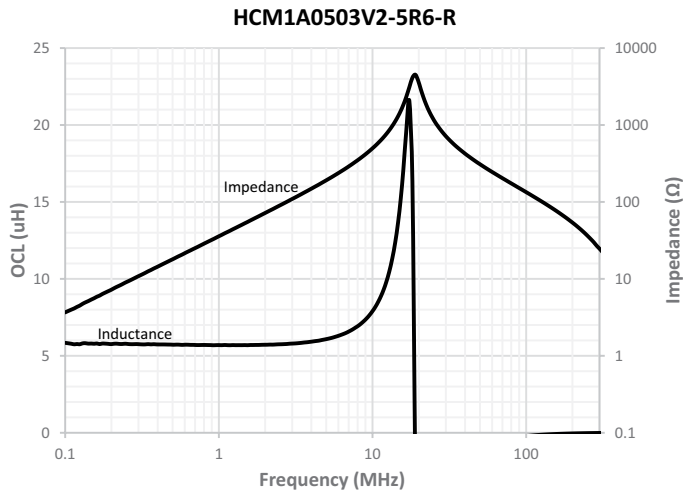
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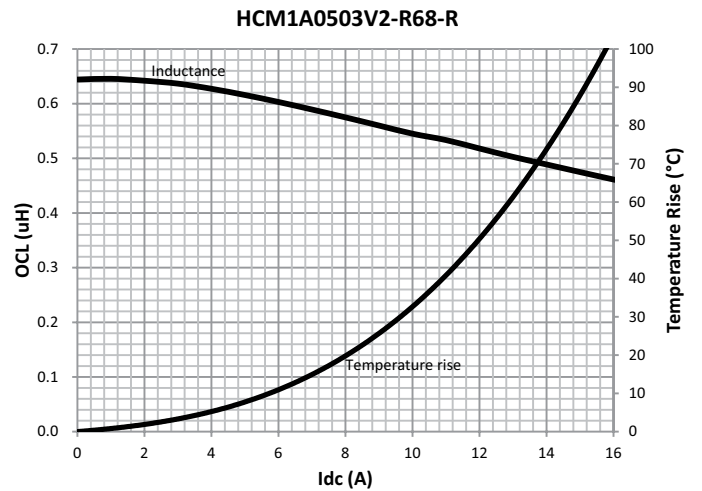
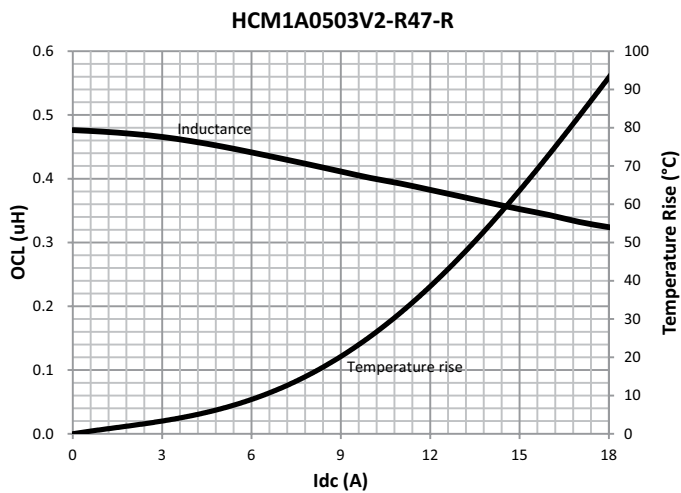
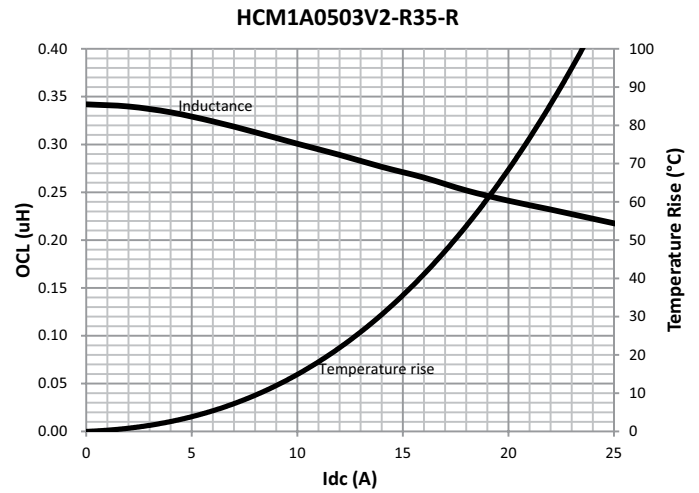
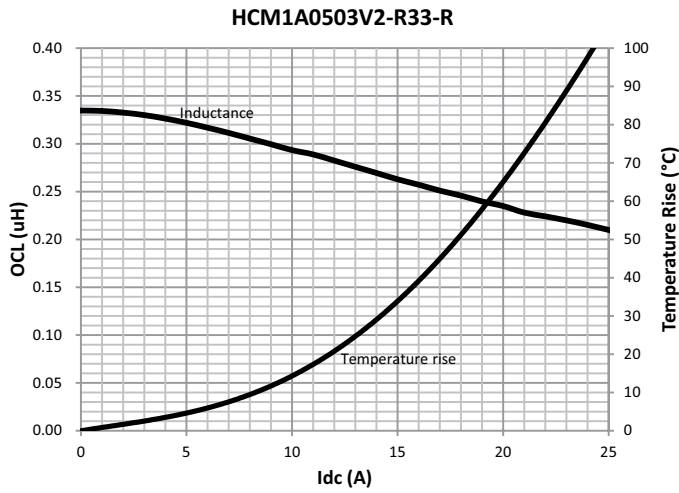
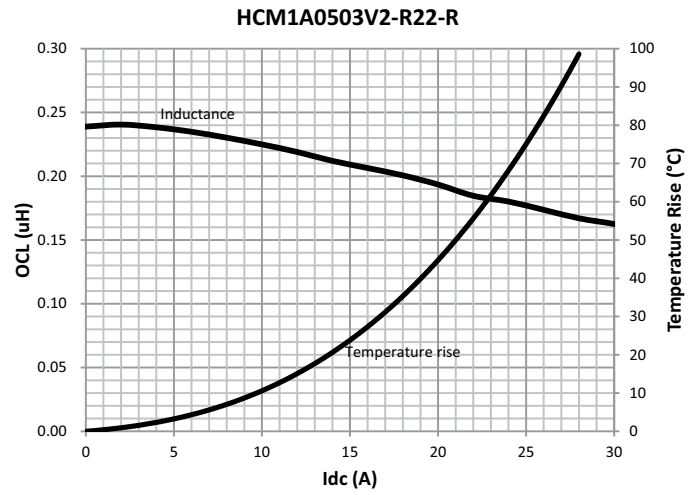
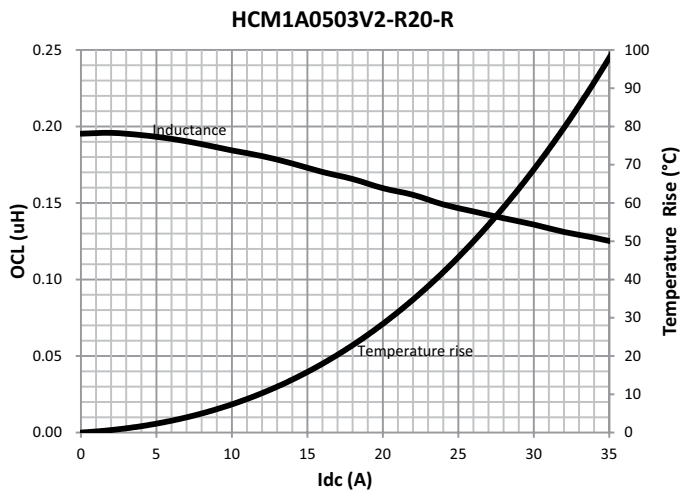
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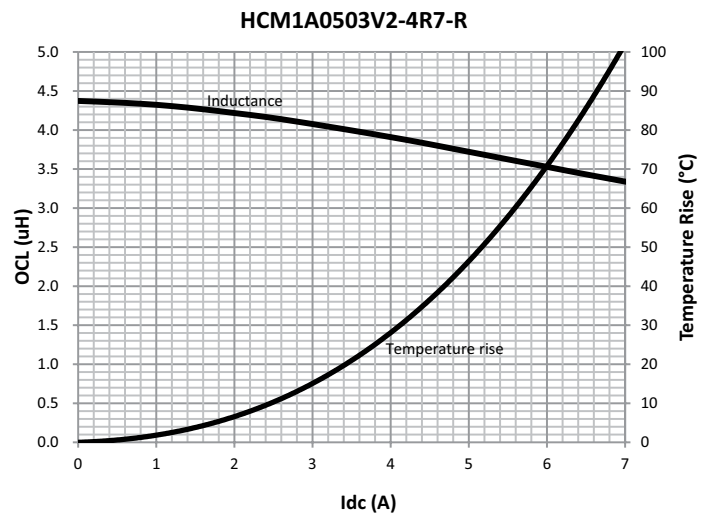
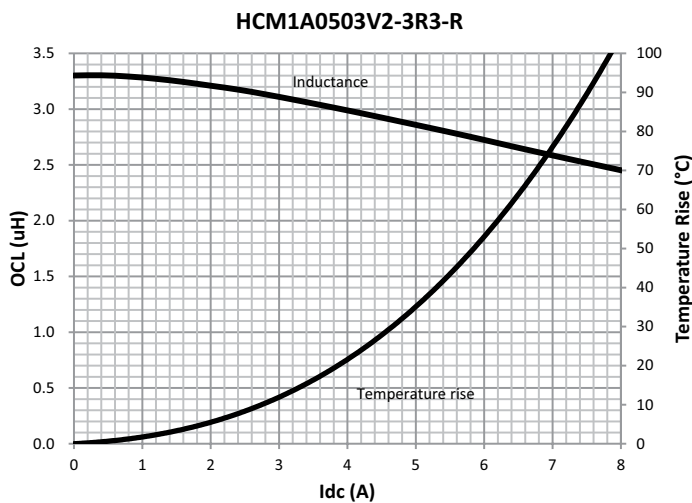
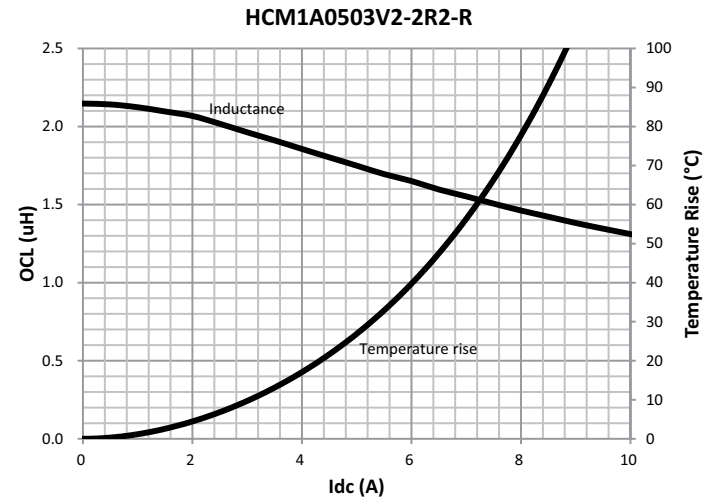
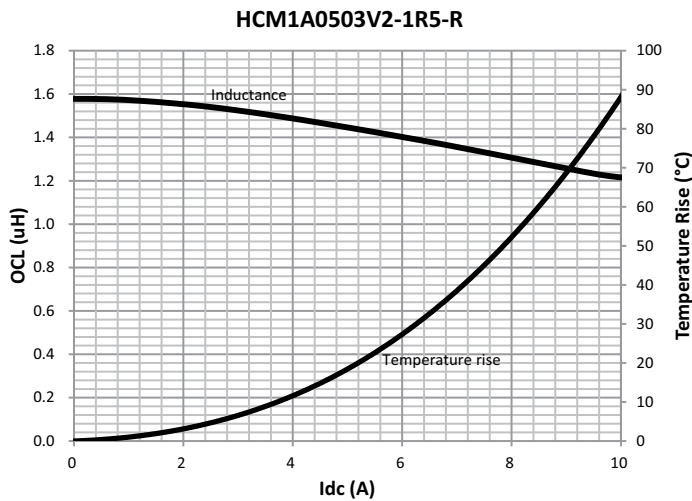
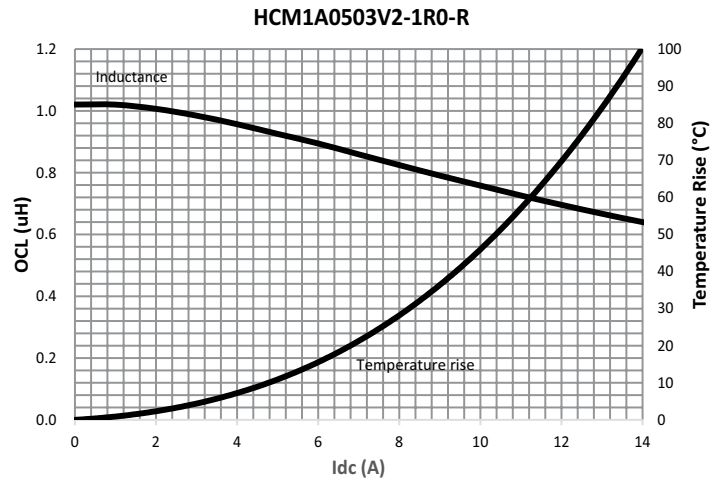
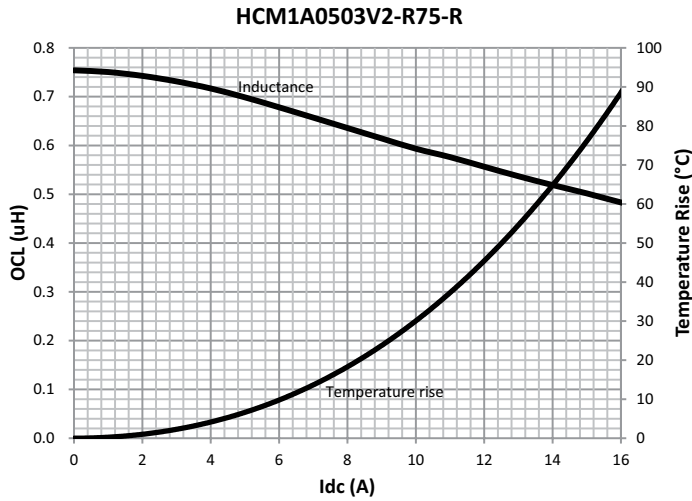
Inductance and impedance vs. frequency



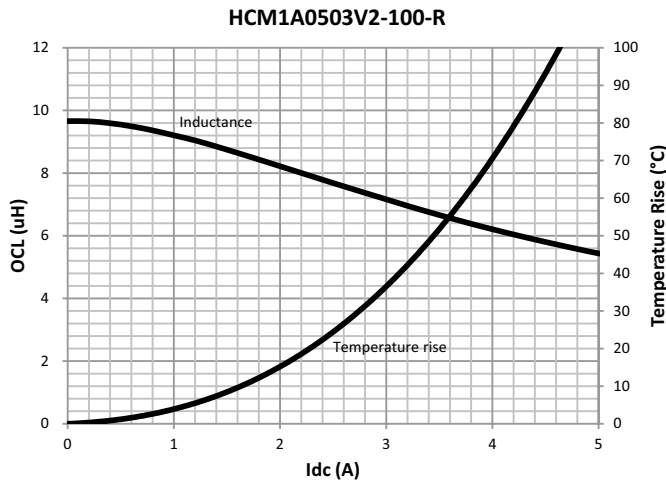
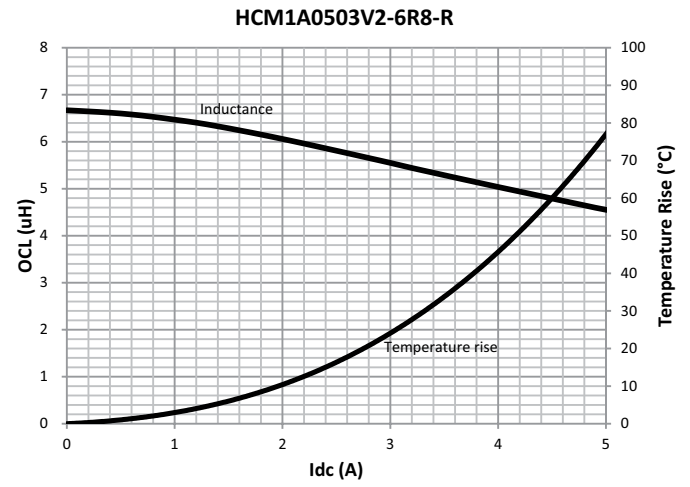
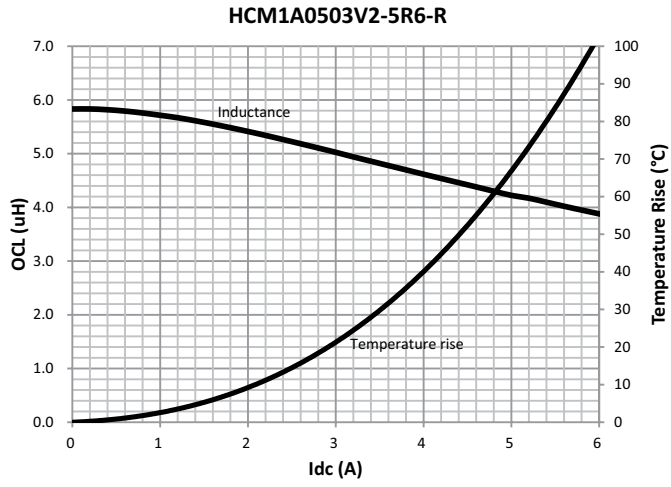
Inductance and temperature rise vs. current



Inductance and temperature rise vs. current



Inductance and temperature rise vs. current



Solder reflow profile

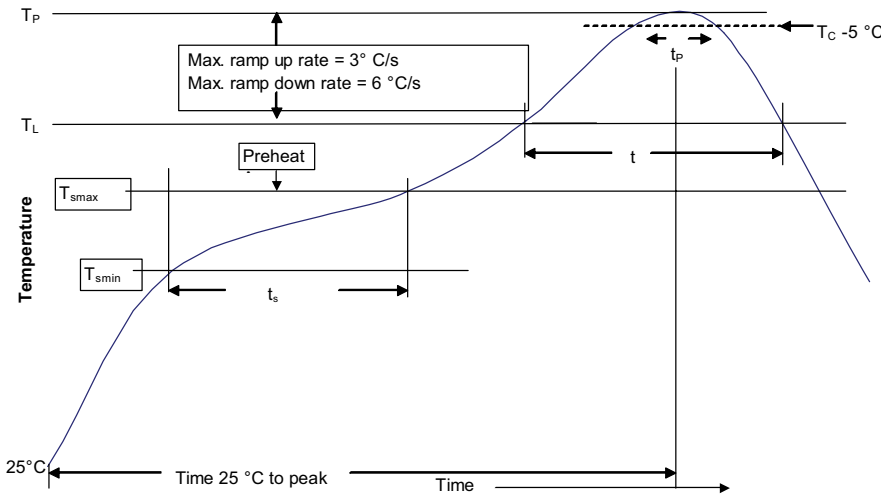


Table 1 - Standard SnPb solder (T_c)

Package thickness	Volume mm^3 <350	Volume mm^3 \geq 350
<2.5 mm)	235 °C	220 °C
\geq 2.5 mm	220 °C	220 °C

Table 2 - Lead (Pb) free solder (T_c)

Package thickness	Volume mm^3 <350	Volume mm^3 350 - 2000	Volume mm^3 >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 – 2.5 mm	260 °C	250 °C	245 °C
>2.5 mm	250 °C	245 °C	245 °C

Reference J-STD-020

Profile feature	Standard SnPb solder	Lead (Pb) free solder
Preheat and soak	<ul style="list-style-type: none"> Temperature min. (T_{smin}) Temperature max. (T_{smax}) Time (T_{smin} to T_{smax}) (t_s) 	<ul style="list-style-type: none"> 100 °C 150 °C 60-120 seconds
Average ramp up rate T_{smax} to T_p	3 °C/ second max.	3 °C/ second max.
Liquidous temperature (T_L) Time at liquidous (t_L)	<ul style="list-style-type: none"> 183 °C 60-150 seconds 	<ul style="list-style-type: none"> 217 °C 60-150 seconds
Peak package body temperature (T_p)*	Table 1	Table 2
Time (t_p)** within 5 °C of the specified classification temperature (T_c)	20 seconds**	30 seconds**
Average ramp-down rate (T_p to T_{smax})	6 °C/ second max.	6 °C/ second max.
Time 25 °C to Peak temperature	6 minutes max.	8 minutes max.

* Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.

** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

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