Effective January 2014

MPI2520 High Current, Low Profile, Miniature Power Inductors







Product specifications

Part Number⁵	OCL1 (μH)±20%	l _{rms} ² (Amps)	l _{sat} ³ (Amps)	DCR (mΩ) @ 25°C typical	DCR (mΩ) @ 25°C max	K-Factor⁴				
R0 —1.0mm Height										
MPI2520R0-R47-R	0.47	4.1	4.4	28	34	2887				
MPI2520R0-1R0-R	0.9	3.2	3.2	50	60	1925				
MPI2520R0-1R5-R	1.5	2.4	2.6	80	96	1444				
MPI2520R0-2R2-R	2.2	2.2	2.4	103	124	1283				
MPI2520R0-3R3-R	3.3	1.6	1.6	190	228	1050				
MPI2520R0-4R7-R	4.7	1.4	1.4	240	288	825				
R1 - 1.2mm Height										
MPI2520R1-R47-R	0.47	4.5	4.8	20	24	2310				
MPI2520R1-1R0-R	1.0	3.7	4.0	35	42	1925				
MPI2520R1-1R5-R	1.5	2.9	5.2	55	66	1444				
MPI2520R1-2R2-R	2.2	2.3	2.1	75	90	1255				
MPI2520R1-3R3-R	3.3	1.8	2.4	105	126	962				
MPI2520R1-4R7-R	4.7	0.6	.5	150	180	⁹ 2ئ				
MPI2520R1-5R6-R	5.	1.5	1.5	200	240	67J				
MPI2520R1-6R8-R	6.8	1.3	1.3	300	360	679				
MPI2520R1-100-B	10.0	1.1	1.2	390	165	525				

ndustance (OCL) Test Paran sters 1. Ope. Circu WHz, 0.1Vrms, 0.0Ac , 25 C

 I_{ms}: DC current for an approximative hperature rise of 40°C without ore loss. Derating is necessive first of currents. PCB layout trace this kness and width, air-flow, and proximity of other heat generating components will affect the temperature is used a trace because of the part net evened 40°E where the temperature of the part. not exceed 125°C under worst case upe at ng conditions verified in the end Recommender MP125-V2 application.

3. Isa: Peak current for approximative 10% rolloff at +2

K-factor Us

K-factor US to retermine B_{pp} for core loss (see graph). $B_{pp} = K = \Delta I, B_{pp}$:(Gauss), K: (K-factor from table), L: (In: uctance in µH), ΔI (Peak to peak ripple current in Amps).

Part Number Definition: MPI2520Rx-yyy-R 5.

MPI2520Rx = Product code and size

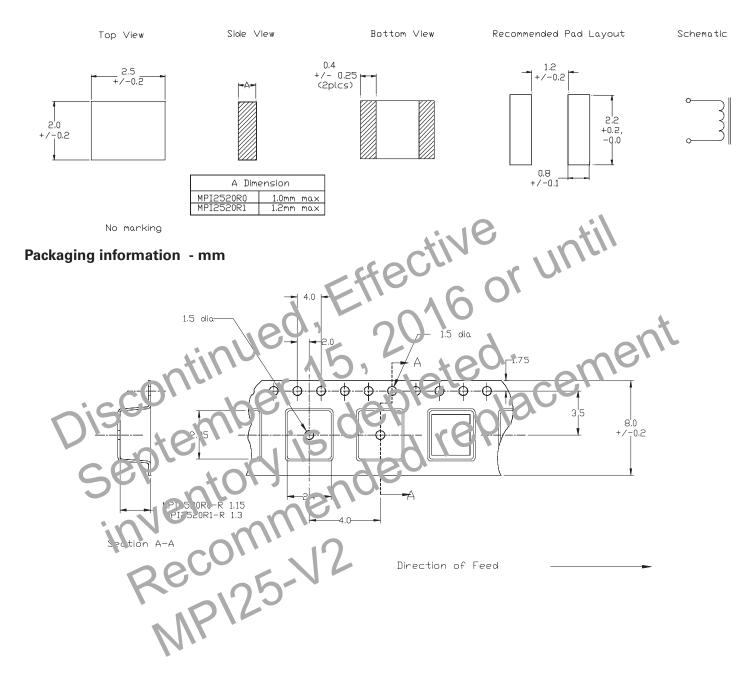
, yyy = Inductance value in μ H, R = decimal point, if no R is present then third character = number of zeros.

- "-R" suffix = RoHS compliant

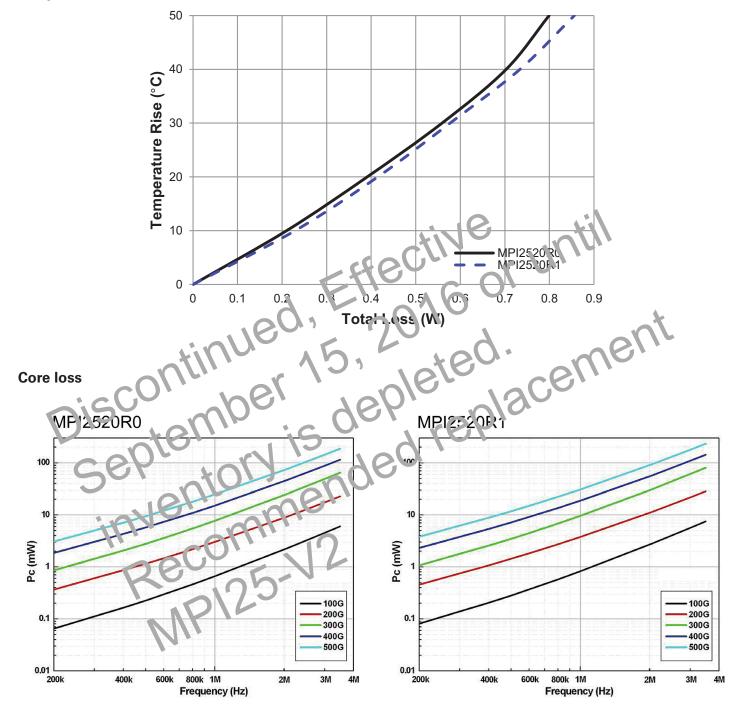
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Dimensions - mm

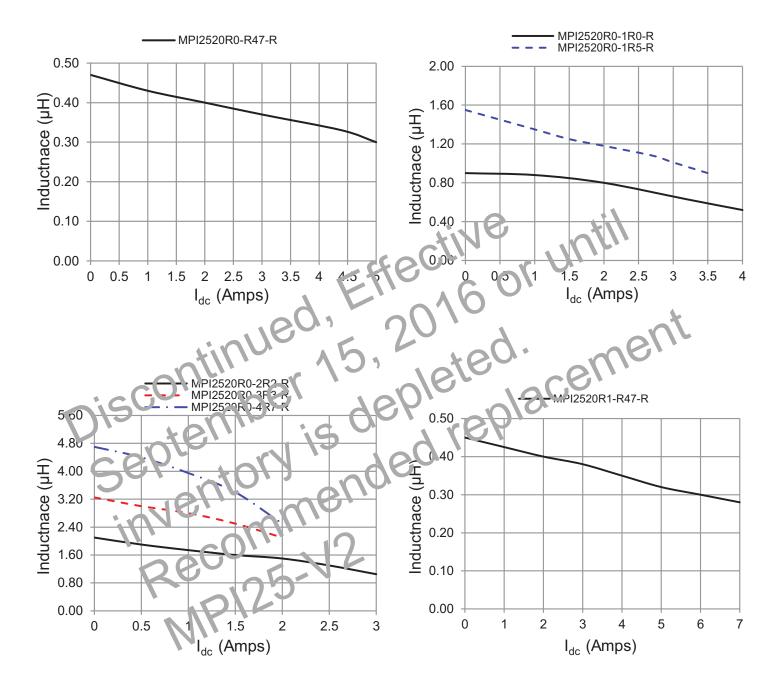


Temperature rise vs. total loss

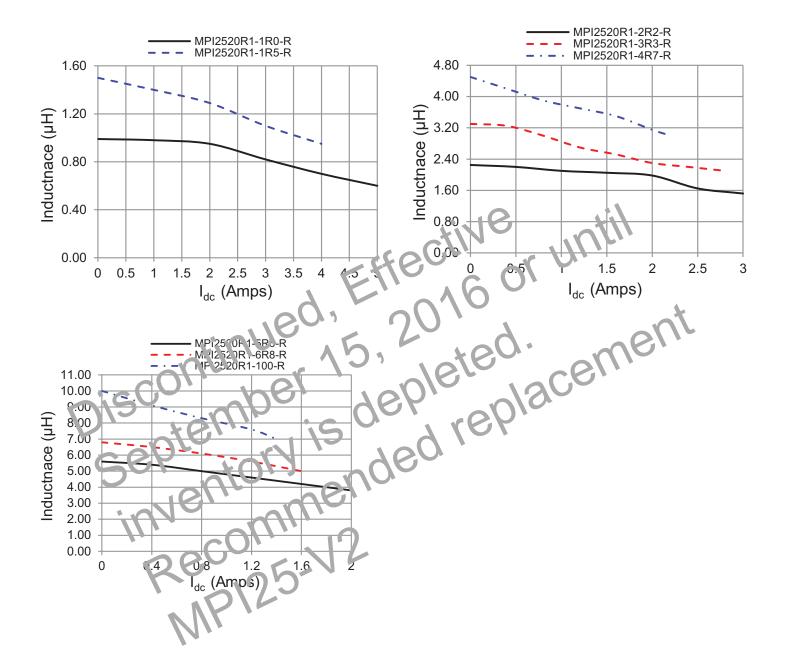


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Inductance characteristics



Inductance characteristics



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Solder reflow profile

Tp	T _c -5℃	Table 1 - Sta	andard SnF	b Solder (T _c)		
TL		Package Thickness	Volume mm ³ <350	Volume mm³ ≥350		
Preheat t +		<u><2.5mm</u> ≥2.5mm	235°C 220°C	220°C 220°C		
	Table 2 - Lead (Pb) Free Solder (T _c)					
		Package Thickness	Volume mm³ <350	Volume mm³ 350 - 2000	Volume mm ³ >2000	
		<1.6mm	260°C	260°C	260°C	
		1.6 – 2.5mn	n 260°C	250°C	245°C	
	1.1	<u>>2.5mm</u>	250°C	245°C	245°C	
25°						
Time 25°C to Peak Time	$\Theta \cup \Box$	r U'				
Reference JDEC J-STD-020D	16	0/				
Profile Feature	Slanda	Standard SnPb Solder		Lead (Pb) Free Solder		
Preheat and Soak • Temperature min. 1 _{smin})	7.4	10.0°C		151°C		
• Ten pera ure max. (T _{smax})	10	50°C		200°C		
• Tin e T _{smin} to T _{smax} , (t _s)	60-	60 120 Seconds		60-120 Seconds		
Average ramp up rate T _{smax} to Tp	3°C/	3°C/ Second 11a .		3°C/ Second Max.		
Liquidous temperature (TL)	Y	103 CO		217°C		
Tim. at I quidous (t_)	50-	60-150 Seconds		60-150 Seconds		
Peak package body tombe ature (Tp)*		Taole 1		Table 2		
Time (tp)** within 5 °C of the specified classification temperature	T _{c/} 20	20 Seconds**		30 Seconds**		
Average ramp down rate (Tp to Ts m, x)	6°C/	6°C/ Second Max.		6°C/ Second Max.		
Time 25°C to Peak Temper ture	6 N	6 Minutes Max. 8 Minutes Max.				
* Tolerance for period of equiperature (T_{i}) is defined as a supplier mini-	num and a upor ma	vipup				

* Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum. ** Tolerance for tine at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

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